Q.1 (11.7 of J&K)

The following measurements are found from a 3-bit unipolar DAC with $V_{ref} = 8$ V: (-0.01,1.03,2.02,2.96,3.95,5.02,6.00,7.08). In units of LSBs, find the offset error, gain error, maximum DNL, and maximum INL.

Q.2 (11.10 of J&K)

Consider the following measured voltage values for a 2-bit DAC with a reference voltage of 4 V:

 $\{00\leftrightarrow 0.01 \text{ V}\} \ \{01\leftrightarrow 1.02 \text{ V}\} \ \{10\leftrightarrow 1.97 \text{ V}\} \ \{11\leftrightarrow 3.02 \text{ V}\}$

In the units of LSB, find the offset error, gain error, worst absolute and relative accuracies, and worst differential nonlinearity. Restate the relative accuracy in teams of an N-bit accuracy.