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1 Overview

1.1 Introduction 1

1.1 Introduction

This manual describes the VxWorks operating system, and how to use VxWorks facilities in the development of real-time applications and systems. It covers the following topics:

- kernel facilities, kernel-based applications, and kernel customization
- multitasking facilities
- POSIX standard interfaces
- memory management
- I/O system
- local file systems
- flash file system support with TrueFFS
- error detection and reporting
- target tools, such as the kernel shell, kernel object-module loader, and target symbol table
- C++ development
- shared memory objects (VxMP)
NOTE: This book provides information about facilities available in the VxWorks kernel. For information about facilities available to real-time processes, see the VxWorks Application Programmer’s Guide.

1.2 Related Documentation Resources

The companion volume to this book, the VxWorks Application Programmer’s Guide, provides material specific process-based (RTP) applications and process management.

Detailed information about VxWorks libraries and routines is provided in the VxWorks API references. Information specific to target architectures is provided in the VxWorks BSP references and in the VxWorks Architecture Supplement.

For information about BSP and driver development, see the VxWorks BSP Developer’s Guide and the VxWorks Device Driver Guide.

The VxWorks networking facilities are documented in the Wind River Network Stack for VxWorks 6 Programmer’s Guide and the VxWorks PPP Programmer’s Guide.

For information about migrating applications, BSPs, drivers, and projects from previous versions of VxWorks and the host development environment, see the VxWorks Migration Guide and the Wind River Workbench Migration Guide.

The Wind River IDE and command-line tools are documented in the Wind River compiler and GNU compiler guides, the Wind River Workbench user’s guide, and the Wind River tools API and command line references.

1.3 VxWorks Configuration and Build

This document describes VxWorks features; it does not go into detail about the mechanisms by which VxWorks-based systems and applications are configured and built. The tools and procedures used for configuration and build are described in the Wind River Workbench documentation.
NOTE: In this book, as well as in the VxWorks API references, VxWorks components are identified by the names used in component description files, which is in the form of INCLUDE_FOO. Similarly, configuration parameters are identified by their configuration parameter names, such as NUM_FOO_FILES.

Component and parameter names can be used directly to identify components and configure VxWorks if you work with the command-line configuration facilities.

Wind River Workbench provides fuller descriptions of the components and their parameters in the GUI. But you can also use a simple search facility to locate a component based on its component name. Once you have located the component, you can access the component’s parameters through the GUI.
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2.1 Introduction

This chapter provides an overview of the VxWorks kernel architecture and detailed discussions of those features of interest to developers who work directly with kernel facilities. In general, kernel developers can modify and extend the VxWorks kernel in following ways:

- By reconfiguring and rebuilding VxWorks with various standard components to suit the needs of their application development environment, as well as the needs of their deployed products.
- By creating kernel-based applications that can either be interactively downloaded and run on a VxWorks target system, or configured to execute at boot time and linked with the operating system image.
- By creating custom kernel libraries that can be built into the operating system.
- By creating custom kernel components—such as file systems or networking protocols—that can be configured into VxWorks using the operating system configuration utilities. The kernel system-call interface can also be extended to accommodate custom APIs that should be accessible to applications running in user space (as real-time process—RTP—applications).

See 2.5 Kernel Images, Components, and Configuration, p.19; as well as other chapters throughout this book for information about VxWorks facilities and their use. Chapter 3. Multitasking, for example, includes discussion of features that are available only in the kernel (such as ISRs and watchdog timers). The VxMP and component is also only available to kernel-based applications. See 12. Shared-Memory Objects: VxMP.

Section 2.7 Kernel-Based Applications, p.52 provides information about creating kernel-based applications. For information about RTP applications, see the VxWorks Application Programmer’s Guide: Applications and Processes.


See 2.9 Custom Kernel Components, p.69, and 2.10 Custom System Calls, p.99, for information on extending the operating system.

See 2.11 Kernel Schedulers, p.116 for information about the VxWorks native scheduler, the POSIX thread scheduler for processes, and the scheduler framework for customized schedulers.
Developers can also write or port drivers and BSPs for VxWorks. These topics are covered by other books in the VxWorks documentation set; see the *VxWorks Device Driver's Guide* and the *VxWorks BSP Developer's Guide*.

### 2.2 Kernel Architecture

Historically, the VxWorks operating system provided a single memory space with no segregation of the operating system from user applications. All tasks ran in supervisor mode. Although this model afforded performance and flexibility when developing applications, only skilled programming could ensure that kernel facilities and applications coexisted in the same memory space without interfering with one another.\(^1\)

With the release of VxWorks 6.0, the operating system provides support for real-time processes (RTPs) that includes execution of applications in user mode and other features common to operating systems with a clear delineation between kernel and applications. This architecture is often referred to as the *process model*. VxWorks has adopted this model with a design specifically aimed to meet the requirements of determinism and speed that are required for hard real-time systems. (For information about VxWorks processes and developing applications to run in processes, see *VxWorks Application Programmer's Guide: Applications and Processes*.) VxWorks 6.x provides full MMU-based protection of both kernel and user space.

At the same time, VxWorks 6.x maintains a high level of backward compatibility with VxWorks 5.5. Applications developed for earlier versions of VxWorks, and designed to run in kernel space, can be migrated to VxWorks 6.x kernel space with minimal effort (in most cases, merely re-compilation). For more information on this topic, see the *VxWorks Migration Guide*.

Naturally, new applications can be designed for kernel space as well, when other considerations outweigh the advantages of protection that executing applications as processes affords. These considerations might include:

---

1. The VxWorks 5.x optional product VxVMI provides write protection of text segments and the VxWorks exception vector table, as well as an architecture-independent interface to the CPU’s memory management unit (MMU). In addition, specialized variants of VxWorks such as VxWorks AE and VxWorks AE653 provide memory protection, but in a manner different from that provided in the current release.
Size. The overall size of a system is smaller without components that provided for processes and MMU support.

Speed. Depending on the number of system calls an application might make, or how much I/O it is doing when running as a process in user space, it might be faster running in the kernel.

Kernel-only features. Features such as watchdog timers, ISRs, and VxMP are available only in the kernel. In some cases, however, there are alternatives for process-based applications (POSIX timers, for example).

Hardware access. If the application requires direct access to hardware, it can only do so from within the kernel.

VxWorks is flexible in terms of both the modularity of its features and its extensibility. The operating system can be configured as a minimal kernel that provides a task scheduler, interrupt handling, dynamic memory management, and little else. Or, it can be configured with components for executing applications as processes, file systems, networking, error detection and reporting, and so on.

The operating system can also be extended by adding custom components or modules to the kernel itself (for example, for new file systems, networking protocols, or drivers). The system call interface can then be extended by adding custom APIs, which makes them available to process-based applications.

### 2.2.1 Operating System Facilities

VxWorks provides a core set of facilities that are commonly provided by the kernel of a multitasking operating system:

- Startup facilities for system initialization (see 2.3 System Startup, p.12).
- Clocks and timers (see 3.4 Watchdog Timers, p.207 and 4.6 POSIX Clocks and Timers, p.225).
- Exception and interrupt handling (see Exception Task, p.10, 3.2.6 Task Exception Handling, p.151, 3.3.10 Signals, p.202, and 3.5 Interrupt Service Routines, p.209).
- Task management (see 3.2 Tasks and Multitasking, p.130).
2.2 Kernel Architecture

- Intertask and interprocess communication (see 3.3 Intertask and Interprocess Communications, p.157).
- Signals (see 3.3.10 Signals, p.202).
- Memory management (see 5. Memory Management).
- I/O system (see 6. I/O System).
- File systems (see 7. Local File Systems).

In addition, the VxWorks kernel also provides:

- The WDB target agent, which is required for using the host development tools with VxWorks. It carries out requests transmitted from the tools (via the target server) and replies with the results (see 10.6 WDB Target Agent, p.540).
- Facilities for error detection and reporting (see 9. Error Detection and Reporting).
- A target-based shell for direct user interaction, with a command interpreter and a C-language interpreter (see 10.2 Kernel Shell, p.495).
- A specialized facilities for multi-processor intertask communication through shared memory (see 12. Shared-Memory Objects: VxMP).

For information about basic networking facilities, see the Wind River Network Stack for VxWorks 6 Programmer’s Guide.

2.2.2 System Tasks

Depending on its configuration, VxWorks includes a variety of system tasks, which are always running. These are described below.

**Root Task**

The root task `tRootTask` is the first task executed by the kernel. The entry point of the root task is `usrRoot()` initializes most VxWorks facilities. It spawns such tasks as the logging task, the exception task, the network task, and the `tRlogind` daemon. Normally, the root task terminates and is deleted after all initialization has completed. For more information `tRootTask` and `usrRoot()`, see the VxWorks BSP Developer’s Guide.
Logging Task

The log task, \texttt{tLogTask}, is used by VxWorks modules to log system messages without having to perform I/O in the current task context. For more information, see 6.5.3 Message Logging, p.337 and the API reference entry for \texttt{logLib}.

Exception Task

The exception task, \texttt{tExcTask}, supports the VxWorks exception handling package by performing functions that cannot occur at interrupt level. It is also used for actions that cannot be performed in the current task’s context, such as task suicide. It must have the highest priority in the system. Do not suspend, delete, or change the priority of this task. For more information, see the reference entry for \texttt{excLib}.

Network Task

The \texttt{tNetTask} daemon handles the task-level functions required by the VxWorks network. Configure VxWorks with the \texttt{INCLUDE_NET_LIB} component to spawn the \texttt{tNetTask} task. For more information on \texttt{tNetTask}, see the VxWorks Device Driver Guide.

WDB Target Agent Task

The WDB target agent task, \texttt{tWdbTask}, is created if the target agent is set to run in task mode. It services requests from the host tools (by way of the target server); for information about this server, see the host development environment documentation. Configure VxWorks with the \texttt{INCLUDE_WDB} component to include the target agent. See 10.6 WDB Target Agent, p.540 for more information about WDB.

Tasks for Optional Components

The following VxWorks system tasks are created if their components are included in the operating system configuration.

\texttt{tShellnum}

If you have included the kernel shell in the VxWorks configuration, it is spawned as a task. Any routine or task that is invoked from the kernel shell, rather than spawned, runs in the \texttt{tShellnum} context.

The task name for a shell on the console is \texttt{tShell0}. The kernel shell is re-entrant, and more than one shell task can run at a time (hence the number suffix). In addition, if a user logs in remotely (using \texttt{rlogin} or \texttt{telnet}) to a VxWorks target, the name reflects that fact as well. For example, \texttt{tShellRem1}.
For more information, see \textit{10.2 Kernel Shell}, p.495. Configure VxWorks with the \texttt{INCLUDE\_SHELL} component to include the kernel shell.

\textbf{tRlogind}

If you have included the kernel shell and the \texttt{rlogin} facility in the VxWorks configuration, this daemon allows remote users to log in to VxWorks. It accepts a remote login request from another VxWorks or host system and spawns \texttt{tRlogInTask\_hexNumber} and \texttt{tRlogOutTask\_hexNumber} (for example, \texttt{tRlogInTask\_5c4d0}). These tasks exist as long as the remote user is logged on. Configure VxWorks with the \texttt{INCLUDE\_RLOGIN} component to include the \texttt{rlogin} facility.

\textbf{tTelnetd}

If you have included the kernel shell and the \texttt{telnet} facility in the VxWorks configuration, this daemon allows remote users to log in to VxWorks with \texttt{telnet}. It accepts a remote login request from another VxWorks or host system and spawns the input task \texttt{tTelnetInTask\_hexNumber} and output task \texttt{tTelnetOutTask\_hexNumber}. These tasks exist as long as the remote user is logged on. Configure VxWorks with the \texttt{INCLUDE\_TELNET} component to include the \texttt{telnet} facility.

\textbf{tPortmapd}

If you have included the RPC facility in the VxWorks configuration, this daemon is RPC server that acts as a central registrar for RPC services running on the same machine. RPC clients query the \texttt{tPortmapd} daemon to find out how to contact the various servers. Configure VxWorks with the \texttt{INCLUDE\_RPC} component to include the portmap facility.

\textbf{tJobTask}

The \texttt{tJobTask} executes jobs—that is, function calls—on the behalf of tasks. (The \texttt{tExcTask} task executes jobs on the behalf of ISRs.) It runs at priority 0 while waiting for a request, and dynamically adjusts its priority to match that of the task that requests job execution. Configure VxWorks with the \texttt{INCLUDE\_JOB\_TASK} component to include the job facility. For more information see, \textit{Task Deletion and Deletion Safety}, p.144.
2.3 System Startup

When a VxWorks system is powered on, the boot loader copies an operating system image into memory and directs the CPU to begin executing it. The boot loader is most often located in ROM (although it can also be stored on a disk). The VxWorks image can be stored on a host or network file system, as is usually the case during development—or stored in ROM with the boot loader, as is often the case with production units. The VxWorks boot loader is actually a scaled-down version of VxWorks itself, whose sole purpose is to load a system image and initiate its execution. (See 2.4 Boot Loader, p.12.) For more information about system startup, see the VxWorks BSP Developer’s Guide: Overview of a BSP.

2.4 Boot Loader

The VxWorks boot loader (often referred to simply as the VxWorks bootrom) is essentially a VxWorks-based boot loading application whose purpose is to boot load a target with the VxWorks kernel. The VxWorks host installation tree includes default boot loader images for each BSP. Users can also create custom boot loaders if they do not wish to use the default configuration (for example, if they do not wish to load the system image over a network).

Boot loaders are useful in a development environment to load a VxWorks image that is stored on a host system, where VxWorks can be quickly modified and rebuilt. They can also be used in production systems in which the boot loader and operating system are stored on a disk.

Self-booting VxWorks image can also be created, which do not require a boot loader. These images are commonly used in production systems (stored in ROM). For more information, see 2.5.1 VxWorks Image Types, p.19.

Typically the VxWorks boot loader is programmed in flash memory or EEPROM at an address such that it is the very first code that is executed by the processor when the target is powered up or rebooted. The procedure for getting the boot loader programmed in flash memory/EEPROM is target-dependant; and is described in the BSP reference documentation.

For information about booting from a local SCSI device on the target system, see 7.4.10 Booting from a Local dosFs File System Using SCSI, p.443.
In order to boot a target with the VxWorks kernel, the default boot loader requires user interaction to setup boot loader parameters such as the path to the file containing the VxWorks kernel (the *vxWorks* operating system image file). This interaction takes place using a console that is typically established by connecting the proper serial port of the target to a serial port on the host and starting a terminal application on the host (such as *tip* on Solaris or *HyperTerminal* on Windows). The BSP documentation describes the setup required to establish communication over the serial port of a particular target. After boot loader parameters have been entered by the user, the boot loader can then boot the target with the VxWorks kernel. See 2.4.2 Boot Loader Parameters and Commands, and Booting VxWorks, p.14.

The VxWorks boot loader can be customized to meet the flash memory/EEPROM size constraints of a particular board as well as the manner in which it retrieves the VxWorks image file. See 2.4.3 Customizing and Building Boot Loaders, p.15.

---

**WARNING:** If the size of the VxWorks persistent memory region (defined with the `PM_RESERVED_MEM` parameter) is increased beyond the default, the boot loader must be reconfigured as well. If the size of the region does not match in the boot loader and VxWorks image, corruption of the region is likely to occur when the system boots. See 9.2.2 Configuring the Persistent Memory Region, p.481.

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### 2.4.1 Boot Loader Image Types

Boot loader images come in three varieties: compressed, uncompressed, and ROM-resident. The three are functionally the same but have different memory requirements and execution times. The BSP reference documentation specifies which types are available for a specific target.

**bootrom**

A ROM boot loader image that is almost entirely compressed. It has a small uncompressed portion executed by the processor immediately after power up/reboot. This portion is responsible for decompressing the compressed section of the ROM image into RAM and for making the processor switch execution to RAM. The compression of the image allows it to be much smaller than other types of boot loader images hence it uses less flash memory/ROM. However the decompression operation increases the boot time. The *bootrom* image can also be created in alternate formats. The *bootrom.hex* version is in Motorola S-Record format. The *bootrom.bin* version is in binary format.
bootrom_uncmp
A ROM boot loader image that copies itself to RAM and makes the processor switch execution to RAM. Because the image is not compressed, it is larger than the bootrom image. However it has faster boot time because there is no decompression operation required. The bootrom_uncmp image can also be created in alternate formats. The bootrom_uncmp.hex version is in Motorola S-Record format. The bootrom_uncmp.bin version is in binary format.

bootrom_res
A ROM loader image that copies only the data segment to RAM on startup; the text segment stays in ROM. This means that the processor always executes instructions out of ROM. Thus it is sometimes described as being ROM-resident. This type of boot loader image is the one that requires the least amount of RAM to boot load the VxWorks kernel. Because only the data segment is copied to RAM this image initially boots faster than the other types but the loading operation is normally slower because fetching instructions from flash memory/ROM is slower than fetching them from RAM (as is done with the other types of boot loader images). The bootrom_res image can also be created in alternate formats. The bootrom_res.hex version is in Motorola S-Record format. The bootrom_res.bin version is in binary format.

The default boot loader images are located in installDir/vxworks-6.x/target/config/bspName.

Note that the default boot loader image is configured for a networked development environment. For information about creating a custom boot loader, see 2.4.3 Customizing and Building Boot Loaders, p.15.

2.4.2 Boot Loader Parameters and Commands, and Booting VxWorks

When you boot the VxWorks kernel with the default boot loader (from ROM, diskette, or other medium), you must interactively change the default boot loader parameters so that the loader can find the VxWorks kernel image on the host and load it onto the target. The default boot program is designed for a networked target, and needs to have the correct host and target network addresses, the full path and name of the file to be booted, the user name, and so on.

For information about setting up a cross-development environment, booting a target system, using boot loader commands, setting boot loader parameters, and so on, see the Wind River Workbench User’s Guide: Setting up Your Hardware.
2.4.3 Customizing and Building Boot Loaders

Boot loaders may need to be customized and rebuilt for a variety of reasons, including:

- The target that is not on a network.
- An alternate boot process, such as booting over the Target Server File System, is preferable. (See *Reconfiguring for Booting with the Target Server File System (TSFS)*, p. 17.)
- To statically re-define the default boot loader parameters. Some systems do not have nonvolatile RAM, which means that when the default boot loader image is used, the boot loader parameters have to be re-entered manually each time the target is rebooted. (Note that Pentium boot loaders automatically write boot parameter changes back to disk.)
- The boot loader needs to use a driver or protocol that is not included in the default boot loader image.
- Boot loaders need to be produced for production systems. In addition to setting the boot loader parameters appropriately, features that are not required (such as the network stack) can be removed to reduce the size of the boot loader image.

Note that self-booting systems can be created by including a kernel application in a ROM operating system image, which is programmed into flash. No boot loader is required in this case. See *2.5.1 VxWorks Image Types*, p. 19.

Changing Boot Loader Parameters

Boot loader parameters include the IP addresses of the host and target systems, the location of VxWorks image file, and so on. These parameters are described in the *Wind River Workbench User’s Guide: Setting up Your Hardware*.

Boot loader parameters are defined in the `installDir/vxworks-6.x/target/config/bspName/config.h` file. To change parameters, edit the `DEFAULT_BOOT_LINE` macro:

```c
#define DEFAULT_BOOT_LINE    BOOT_DEV_NAME \ 
    "(0,0)wrSbc8260:vxworks " \ 
    "e=192.168.100.51 " \ 
    "h=192.168.100.155 " \ 
    "g=0.0.0.0 " \ 
    "u=anonymous pw=user " \ 
    "f=0x00 tn=wrSbc8260"
```

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Reconfiguring Boot Loader Components

A boot loader can be re-configured with just those VxWorks components that are required for a given system. For example, networking components can be removed if networking is not used to boot the system.

To add or remove components from a boot loader, you will need to use two BSP configuration files, the first for reference purposes only:

```
installDir/vxworks-6.x/target/config/configAll.h
installDir/vxworks-6.x/target/config/bspName/config.h
```

The `configAll.h` file provides the default VxWorks configuration for all BSPs. The INCLUDED SOFTWARE FACILITIES section of this header file lists all components that are included in the default configuration. The EXCLUDED FACILITIES section identifies those that are not.

To add or remove components from a boot loader configuration, refer to the `configAll.h` file to determine the default, and then change the default for a specific BSP by editing the `config.h` file, defining or un-defining the appropriate components.

⚠️ CAUTION: Do not edit the `configAll.h` file. Any changes that you make will affect all BSPs. You should only edit the `config.h` files in the individual BSP directories.

For example, the INCLUDED SOFTWARE FACILITIES section of `configAll.h` has the following entry for the core networking component:

```
#define INCLUDE_NETWORK /* network subsystem code */
```

If you are creating a boot loader for a wrSbc8260 board that does not require networking, you would add the following line to the `config.h` file in `installDir/vxworks-6.x/target/config/wrSbc8260`:

```
#undef INCLUDE_NETWORK
```

Reconfiguring Memory

The persistent-memory region is an area of RAM at the top of system memory specifically reserved for an error records and core dumps. If you increase the size of the persistent memory region beyond the default, you must create a new boot loader with the same `PM_RESERVED_MEM` value. The memory area between `RAM_HIGH_ADRS` and `sysMemTop()` must be big enough to copy the VxWorks boot loader. If it exceeds the `sysMemTop()` limit, the boot loader may corrupt the
area of persistent memory reserved for core dump storage when it loads VxWorks.
The boot loader, must therefore be rebuilt with a lower `RAM_HIGH_ADRS` value.

**WARNING:** If the boot loader is not properly configured (as described above), this could lead into corruption of the persistent memory region when the system boots.

See [9.2.2 Configuring the Persistent Memory Region](#), p.481 for more information.

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**Reconfiguring for Booting with the Target Server File System (TSFS)**

The simplest way to boot a target from a host that is not on a network is over the Target Server File System. This does not involve configuring SLIP or PPP. The TSFS can be used to boot a target connected to the host by one or two serial lines. Configure VxWorks with the `INCLUDE_WDB_TSFS` component.

To configure a boot program for TSFS, edit the boot line parameters defined by `DEFAULT_BOOT_LINE` in `config.h` (or change the boot loader parameters at the boot prompt). The boot device parameter must be `tsfs`, and the file path and name must be relative to the root of the host file system defined for the target server.

Regardless of how you specify the boot line parameters, you must reconfigure (as described below) and rebuild the boot image.

If two serial lines connect the host and target (one for the target console and one for WDB communications), `config.h` must include the lines:

```c
#undef CONSOLE_TTY
#define CONSOLE_TTY 0
#undef WDB_TTY_CHANNEL
#define WDB_TTY_CHANNEL 1
#undef WDB_COMM_TYPE
#define WDB_COMM_TYPE WDB_COMM_SERIAL
#define INCLUDE_TSFS_BOOT
```
If one serial line connects the host and target, config.h must include the lines:

```c
#define INCLUDE_TSFS_BOOT_VIO_CONSOLE
```

With any of these TSFS configurations, you can also use the target server console to set the boot loader parameters by including the INCLUDE_TSF_S_BOOT_VIO_CONSOLE component in VxWorks. This disables the auto-boot mechanism, which might otherwise boot the target before the target server could start its virtual I/O mechanism. (The auto-boot mechanism is similarly disabled when CONSOLE_TTY is set to NONE, or when CONSOLE_TTY is set to WDB_TTY_CHANNEL.) Using the target server console is particularly useful for a single serial connection, as it provides an otherwise unavailable means of changing boot loader parameters from the command line.

When you build the boot image, select bootrom.hex for the image type (see Building Boot Loaders, p.18).

For more information about the TSFS, see the 7.8 Target Server File System: TSFS, p.457.

Reconfiguring for Booting from a SCSI Device

For information about booting from a SCSI device on the target system, see 7.4.10 Booting from a Local dosFs File System Using SCSI, p.443.

Building Boot Loaders

To build a boot loader, use the command make bootLoaderType in the installDir/vxworks-6.x/target/config/bspName directory. For example:

```
% make bootrom
```

The different types of boot loader images that can be built are described in 2.4.1 Boot Loader Image Types, p.13.
2.5 Kernel Images, Components, and Configuration

The VxWorks distribution includes a variety of system image for each target shipped. Each system image is a binary module that can be booted and run on a target system. The system image consists of a set of components linked together into a single non-relocatable object module with no unresolved external references.

In most cases, you will find the supplied system image adequate for initial development. However, later in the cycle you may want to create a custom VxWorks image, with components specifically selected to support your applications and development requirements. And finally, you will want to reconfigure the operating system with only those components needed for your production system.

VxWorks is a flexible, scalable operating system with numerous facilities that can be tuned, and included or excluded, depending on the requirements of your application and the stage of the development cycle. For example, various networking and file system components may be required for one application and not another, and the kernel configuration facilities provide a simple means for either including them in, or excluding them from, a VxWorks system. In addition, it may be useful to build VxWorks with various target tools during development (such as the kernel shell), and then exclude them from the production system.

Using a default VxWorks image, you can download and run kernel-based applications. If you configure VxWorks with the appropriate components and initialization settings, you can start kernel-based applications automatically at boot time. (See 2.7 Kernel-Based Applications, p.52.) VxWorks can also be configured to run process-based applications interactively and automatically. (See VxWorks Application Programmer’s Guide: Applications and Processes.)

If the default VxWorks components do not provide all the facilities required for a system, developers can create custom VxWorks components for features that they wish to add to the operating system, such as new file systems and networking protocols (see 2.9 Custom Kernel Components, p.69).

2.5.1 VxWorks Image Types

Different types of VxWorks system images can be produced for a variety of storage, loading, and execution scenarios. Default versions of the following images are provided in the VxWorks installation. Customized versions with different
components can also be created. Note that only one image type requires a boot loader, and that the others are self-booting.

The various VxWorks image types, their use, and behavior are:

**vxWorks**
This VxWorks image type is intended for use during development and is often referred to as downloadable. It is also useful for production systems in which the boot loader and system image are stored on disk. In a development environment, the image is usually stored on the host system (or a server on the network), downloaded to the target system by the boot loader, and loaded into RAM. The symbol table is maintained on the host (in the file *vxWorks.sym*), where it is used by the host development tools. Leaving the symbol table on the host keeps the image size down and reduces boot time. If VxWorks is reconfigured with the INCLUDE_STANDALONE_SYM_TBL component, the symbol table is included in the VxWorks image.

**vxWorks_rom**
A VxWorks image that is stored in ROM on the target. It copies itself to RAM and then makes the processor switch execution to RAM. Because the image is not compressed, it is larger than the other ROM-based images and therefore has a slower startup time; but it has a faster execution time than vxWorks_romResident.

**vxWorks_romCompress**
A VxWorks image that is stored in ROM on the target. It is almost entirely compressed, but has small uncompressed portion executed by the processor immediately after power up/reboot. This small portion is responsible for decompressing the compressed section of the ROM image into RAM and for making the processor switch execution to RAM. The compression of the image allows it to be much smaller than other images. However the decompression operation increases the boot time. It takes longer to boot than vxWorks_rom but takes up less space than other ROM-based images. The run-time execution is the same speed as vxWorks_rom.

**vxWorks_romResident**
A VxWorks image that is stored in ROM on the target. It copies only the data segment to RAM on startup; the text segment stays in ROM. Thus it is described as being ROM-resident. It has the fastest startup time and uses the smallest amount of RAM, but it runs slower than the other image types because the ROM access required for fetching instructions is slower than fetching them from RAM. It is obviously useful for systems with constrained memory resources.
The default VxWorks image files can be found in sub-directories under installDir/vxworks-6.x/target/proj/projName. For example:

/home/moi/myInstallDir/vxworks-6.1/target/proj/wrSbc8260_diab/default_rom/vxWorks_rom

For many production systems it is often necessary to store a kernel-based application module that is linked with VxWorks in ROM. VxWorks can be configured to execute the application automatically at boot time. The system image can also simply store the application module to allow for its being called by other programs, or for interactive use by end-users (for example, diagnostic programs).

To produce a ROM-based system, you must link the module with VxWorks, and build an image type that is suitable for ROM. See 2.7.7 Linking Kernel-Based Application Object Modules with VxWorks, p.65. If you wish to have the application start automatically at boot time, you must also configure VxWorks to do so (see 2.7.9 Configuring VxWorks to Run Applications Automatically, p.68). Also see 2.7.8 Image Size Considerations, p.66.

Note that, during development, VxWorks must be configured with the WDB target agent communication interface that is required for the type of connection used between your host and target system (network, serial, and so on). By default, it is configured for an Enhanced Network Driver (END) connection. For more information, see 10.6 WDB Target Agent, p.540. Also note that before you use the host development tools such as the shell and debugger, you must start a target server that is configured for the same mode of communication.

For information about configuring VxWorks with different operating system facilities (components), see 2.5.3 Reconfiguring VxWorks, p.26.

If you are going to store boot image in flash, and want to use TrueFFS as well, see 8.3.5 Creating a Region in Flash for a Boot Image, p.471.

2.5.2 VxWorks Components

A VxWorks component is the basic unit of functionality with which VxWorks can be configured. While some components are autonomous, others may have dependencies on other components, which need to be included in the configuration of the operating system for runtime operation. The kernel shell is an example of a component with many dependencies. The symbol table is an example of a component upon which other components depend (the kernel shell and module loader; for more information, see 10. Target Tools).

The names, descriptions, and configurable features of VxWorks can be displayed with the GUI configuration facilities in the host IDE. The IDE provides facilities for
configuring VxWorks with selected components, setting component parameters, as well as automated mechanisms for determining dependencies between components during the configuration and build process.

The command-line operating system configuration tool—vxprj—uses the naming convention that originated with configuration macros to identify individual operating system components. The convention identifies components with names that begin with INCLUDE. For example, INCLUDE_MSG_Q is the message queue component. In addition to configuration facilities, the vxprj tool provides associated features for listing the components included in a project, and so on.

For information about the host IDE and CLI facilities used for configuring and building VxWorks, see the Wind River Workbench User’s Guide and the VxWorks Command-Line Tools User’s Guide.

Textual configuration files identify components with macro names that begin with INCLUDE, as well as with user-friendly descriptions. (For information about configuration files, see 2.9.1 Component Description Language, p.70.)

In this book, components are identified by their macro name. The GUI configuration facilities provide a search facility for finding individual components in the GUI component tree based on the macro name.

Some of the commonly used VxWorks components are described in Table 2-1. Names that end in XXX represent families of components, in which the XXX is replaced by a suffix for individual component names. For example, INCLUDE_CPLUS_XXX refers to a family of components that includes INCLUDE_CPLUS_MIN and others.

Note that Table 2-1 does not include all components provided in the default configuration of VxWorks, and that the VxWorks simulator provides more components by default.

<table>
<thead>
<tr>
<th>Component</th>
<th>Default</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>INCLUDE_ANSI_XXX</td>
<td>*</td>
<td>Various ANSI C library options</td>
</tr>
<tr>
<td>INCLUDE_BOOTLINE_INIT</td>
<td></td>
<td>Parse boot device configuration information</td>
</tr>
<tr>
<td>INCLUDEBOOTP</td>
<td>*</td>
<td>BOOTP support</td>
</tr>
<tr>
<td>INCLUDE_CACHE_SUPPORT</td>
<td>*</td>
<td>Cache support</td>
</tr>
</tbody>
</table>

Table 2-1 Key VxWorks Components
### Key VxWorks Components (cont'd)

<table>
<thead>
<tr>
<th>Component</th>
<th>Default</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>INCLUDE_CPLUS</td>
<td>*</td>
<td>Bundled C++ support</td>
</tr>
<tr>
<td>INCLUDE_CPLUS_XXX</td>
<td></td>
<td>Various C++ support options</td>
</tr>
<tr>
<td>INCLUDE_DEBUG</td>
<td></td>
<td>Kernel shell debug facilities</td>
</tr>
<tr>
<td>INCLUDE_EDR_XXX</td>
<td></td>
<td>Error detection and reporting facilities.</td>
</tr>
<tr>
<td>INCLUDE_DOSFS</td>
<td></td>
<td>DOS-compatible file system</td>
</tr>
<tr>
<td>INCLUDE_FLOATING_POINT</td>
<td>*</td>
<td>Floating-point I/O</td>
</tr>
<tr>
<td>INCLUDE_FORMATTED_IO</td>
<td>*</td>
<td>Formatted I/O</td>
</tr>
<tr>
<td>INCLUDE_FTP_SERVER</td>
<td></td>
<td>FTP server support</td>
</tr>
<tr>
<td>INCLUDE_IO_SYSTEM</td>
<td>*</td>
<td>I/O system package</td>
</tr>
<tr>
<td>INCLUDE_LOADER</td>
<td></td>
<td>Target-resident kernel object module loader package</td>
</tr>
<tr>
<td>INCLUDE_LOGGING</td>
<td>*</td>
<td>Logging facility</td>
</tr>
<tr>
<td>INCLUDE_MEM_MGR_BASIC</td>
<td>*</td>
<td>Core partition memory manager</td>
</tr>
<tr>
<td>INCLUDE_MEM_MGR_FULL</td>
<td>*</td>
<td>Full-featured memory manager</td>
</tr>
<tr>
<td>INCLUDE_MIB2_XXX</td>
<td></td>
<td>Various MIB-2 options</td>
</tr>
<tr>
<td>INCLUDE_MMU_BASIC</td>
<td>*</td>
<td>Bundled MMU support</td>
</tr>
<tr>
<td>INCLUDE_MSG_Q</td>
<td>*</td>
<td>Message queue support</td>
</tr>
<tr>
<td>INCLUDE_NETWORK</td>
<td>*</td>
<td>Network subsystem code</td>
</tr>
<tr>
<td>INCLUDE_NFS</td>
<td></td>
<td>Network File System (NFS)</td>
</tr>
<tr>
<td>INCLUDE_NFS_SERVER</td>
<td></td>
<td>NFS server</td>
</tr>
<tr>
<td>INCLUDE_PIPES</td>
<td>*</td>
<td>Pipe driver</td>
</tr>
<tr>
<td>INCLUDE_POSIX_XXX</td>
<td></td>
<td>Various POSIX options</td>
</tr>
<tr>
<td>INCLUDE_PROTECT_TEXT</td>
<td></td>
<td>Text segment write protection</td>
</tr>
</tbody>
</table>
Table 2-1  **Key VxWorks Components (cont’d)**

<table>
<thead>
<tr>
<th>Component</th>
<th>Default</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>INCLUDE_PROTECT_VEC_TABLE</td>
<td></td>
<td>Vector table write protection</td>
</tr>
<tr>
<td>INCLUDE_PROXY_CLIENT</td>
<td>*</td>
<td>Proxy ARP client support</td>
</tr>
<tr>
<td>INCLUDE_PROXY_SERVER</td>
<td></td>
<td>Proxy ARP server support</td>
</tr>
<tr>
<td>INCLUDE_RAWFS</td>
<td></td>
<td>Raw file system</td>
</tr>
<tr>
<td>INCLUDE_RLOGIN</td>
<td></td>
<td>Remote login with <code>rlogin</code></td>
</tr>
<tr>
<td>INCLUDE_ROMFS</td>
<td></td>
<td>ROMFS file system</td>
</tr>
<tr>
<td>INCLUDE_RTP</td>
<td></td>
<td>Real-time process support.</td>
</tr>
<tr>
<td>INCLUDE_SCSI</td>
<td></td>
<td>SCSI support</td>
</tr>
<tr>
<td>INCLUDE_SCSI2</td>
<td></td>
<td>SCSI-2 extensions</td>
</tr>
<tr>
<td>INCLUDE_SECURITY</td>
<td></td>
<td>Remote login security package</td>
</tr>
<tr>
<td>INCLUDE_SELECT</td>
<td></td>
<td>Select facility</td>
</tr>
<tr>
<td>INCLUDE_SEM_BINARY</td>
<td>*</td>
<td>Binary semaphore support</td>
</tr>
<tr>
<td>INCLUDE_SEM_COUNTING</td>
<td>*</td>
<td>Counting semaphore support</td>
</tr>
<tr>
<td>INCLUDE_SEM_MUTEX</td>
<td>*</td>
<td>Mutual exclusion semaphore support</td>
</tr>
<tr>
<td>INCLUDE_SHELL</td>
<td></td>
<td>Kernel (target) shell</td>
</tr>
<tr>
<td>INCLUDE_XXX_SHOW</td>
<td></td>
<td>Various system object show facilities</td>
</tr>
<tr>
<td>INCLUDE_SIGNALS</td>
<td>*</td>
<td>Software signal facilities</td>
</tr>
<tr>
<td>INCLUDE_SM_OBJ</td>
<td></td>
<td>Shared memory object support (requires VxMP)</td>
</tr>
<tr>
<td>INCLUDE_SNMPD</td>
<td></td>
<td>SNMP agent</td>
</tr>
</tbody>
</table>

By default, VxWorks includes both *libc* and GNU *libgcc*, which are provided with the **INCLUDE_ALL_INTRINSICS** component. If you wish to exclude one or the other library, you can do so by reconfiguring the kernel with either.
INCLUDE_DIAB_INTRINSICS or INCLUDE_GNU_INTRINSICS, respectively. Note that these libraries are available in the kernel to enable dynamically downloading and running kernel object modules.

### 2.5.3 Reconfiguring VxWorks

The default VxWorks image is designed for the development environment, and it contains the basic set of components that are necessary to interact with the system using the host development tools.

However, you will need to reconfigure and rebuild VxWorks if you want to include additional, or alternative features. Notable among these are support for real-time processes and the ROMFS file system. (Note, however, that the default VxWorks simulator provides more facilities, including full support for real-time processes and ROMFS).

You will also want to reconfigure the operating system as you approach a final production version of your application. For production systems you will likely want to remove components required for host development support, such as the WDB target agent and debug components (INCLUDE_WDB and INCLUDE_DEBUG), as well as to remove any other operating system components not required to support your application. Considerations include reducing the memory requirements of the system, speeding up boot time, and security concerns.

Note that a variety of types of VxWorks images (including applications) can be built for programming entire systems into ROM. See 2.5.1 VxWorks Image Types, p.19.

For information about using the Wind River IDE and command-line tools to configure and build VxWorks, see the Wind River Workbench User’s Guide and the VxWorks Command-Line Tools User’s Guide.

### 2.5.4 VxWorks Configuration Profiles

In addition to components and component bundles, configuration profiles can be used to configure a VxWorks system. Profiles provide a convenient way of providing a base line of operating system functionality that is different from the default configuration available with the VxWorks product installation. The following profiles are available:
PROFILE_MINIMAL_KERNEL—Minimal VxWorks Kernel Profile
This profile provides the lowest level at which a VxWorks system can operate. It consists of the micro-kernel, and basic CPU and BSP support. This profile is meant to provide a very small VxWorks systems that can support multitasking and interrupt management at a very minimum, but semaphores and watchdogs are also supported by default. (See VxWorks Profiles for a Scaling the Operating System, p.27.)

PROFILE_BASIC_KERNEL—Basic VxWorks Kernel Profile
This profile builds on the minimal kernel profile, adding support for message queues, task hooks, and memory allocation and deallocation. Applications based on this profile can be more dynamic and feature rich than the minimal kernel. (See VxWorks Profiles for a Scaling the Operating System, p.27.)

PROFILE_BASIC_OS—Basic VxWorks OS Profile
This profile provides a small operating system on which higher level constructs and facilities can be built. It supports an I/O system, file descriptors, and related ANSI routines. It also supports task and environment variables, signals, pipes, coprocessor management, and a ROMFS file system. (See VxWorks Profiles for a Scaling the Operating System, p.27.)

PROFILE_COMPATIBLE—VxWorks 5.5 Compatible Profile
This profile provides the minimal configuration that is compatible with VxWorks 5.5.

PROFILE_DEVELOPMENT—VxWorks Kernel Development Profile
This profile provides a VxWorks kernel that includes development and debugging components.

PROFILE_ENHANCED_NET—Enhanced Network Profile
This profile adds to the default profile certain components appropriate for typical managed network client host devices. The primary components added are the DHCP client and DNS resolver, the Telnet server (shell not included), and several command-line-style configuration utilities.

VxWorks Profiles for a Scaling the Operating System

VxWorks has always been a scalable operating system. Prior to VxWorks 6.2, the scalability of the operating system was based on components and parameter values. The components were provided in pre-compiled libraries, which were linked into the system by way of symbolic references. While this is convenient, the
code in the libraries has to be factored to support any component selected at project build time. This is a bit inefficient and slow for some applications, depending on their time and space constraints.

With VxWorks 6.2, the operating system can be configured in a more refined way, allowing for more smaller and more precise configurations of operating system features. VxWorks can be easily scaled from a micro-kernel to a full-featured operating system.

To make VxWorks more scalable, many of the VxWorks kernel libraries were re-factored to eliminate or minimize dependence on other components, or split into sub-components for the 6.2 release. In addition, operating system profiles were provided for building the operating system from source code, which means that it can be scaled down to smaller sizes (less than 100 KB) in a simple, pre-determined manner. The configurations built from source code also provide improved system performance, because only the required source facilities are compiled.

The scaled-down profiles for VxWorks produce systems that have a much smaller footprint than a standard out-of-box configuration. They are meant for kernel-based applications that are relatively simple, that need not much more than kernel and inter-task synchronization facilities and a simple I/O system. Many of these configurations can be achieved in 100 KB or less when built from source. The reduced feature content of these profiles, and building from source, enables this size reduction. As of this release, networking is not supported in any of the scaled down profiles.

Users can also use the scaled down profiles as a predefined component set for a given class of applications, instead of manually adjusting the component configuration of a standard system (that is, one inclusive of a network stack, debug agent and other runtime tools such as the loader, and so on). The scaled down profiles are ideally suited for small fixed-function systems that are statically resourced and need relatively simpler kernel functionality to operate.

NOTE: The VxWorks scalability profiles are built from source code, so you must install VxWorks source to use them. For this release, the profiles can only be built with the Wind River compiler. In addition, the profiles are not available for all BSPs. They are provided for integrator1136jfs, wrSbcPowerQuiccII, and bcm1250.
VxWorks Scalability Profiles: A Multi-Level View of The System

The scalability of the VxWorks system progresses through several well-defined levels. Starting with a very simple micro-kernel at the lowest level, the system scales through three distinct levels, to reach the default VxWorks configuration. At each level, the system’s capabilities progressively grow in sophistication and depth, in order to support more sophisticated capabilities at the next level. Each level is capable of supporting applications of increasing complexity and sophistication.

Each level corresponds to a profile on which you can base your projects (using either Wind River Workbench or the vxprj command-line tool). Each profile is capable of producing a fully linked VxWorks image that can boot and run applications. It is important to note that profiles are not monolithic. A profile is merely an initial set of components that are included when projects are created. Thereafter users can add or remove components as desired to get the final system configuration.

Three profiles provide for scaling VxWorks below the default configuration. The profiles start with the tiniest and simplest configuration a VxWorks kernel can achieve. The two other profiles gradually increase the level of kernel functionality before the system reaches a standard out-of-box configuration. Figure 2-1 illustrates the relationship between the three profiles and the functionality they provide.
The following sections describe the salient characteristics and component mix of the various profiles.
Minimal Kernel Profile

The minimal kernel profile (PROFILE_MINIMAL_KERNEL) provides the lowest level at which a VxWorks system can operate at. This profile consists of the micro-kernel and basic support code for the CPU and BSP. The minimal kernel environment is meant to support very small VxWorks systems that can provide multitasking and interrupt management at a very minimum. Support for semaphores (binary, counting, and mutexes) and watchdogs is also present, but is optional. A subset of the ANSI C library is available in the minimal kernel profile. These include support for `ctype`, string manipulation, and routines converting numbers to strings and vice versa.

A unique characteristic of the minimal kernel profile is that it is a static system. There is no support for dynamic memory allocation (that is, `malloc()`, `free()`, and related routines are not supported). Both system and application components are expected to declare storage for all objects at compile time (that is, statically). The absence of dynamic allocation in this profile also implies that the kernel cannot dynamically instantiate objects like tasks, semaphores and watchdogs. Familiar API’s like `taskSpawn()`, `taskDelete()`, `semXCreate()`, `semDelete()`, and so on, are not available. However the same kind of objects can be instantiated statically. For more information, see 2.7.3 Static Instantiation of Kernel Objects, p.56.

The minimal kernel profile is a very tiny, limited environment that is suited only for small, fixed-function systems. Consequently, it is also a very limited programming environment. It is pertinent to note that there are significant capabilities that are absent from a minimal kernel, which would otherwise be present in other more feature rich configurations of VxWorks. These are the following:

- Dynamic memory allocation and deallocation. The ability to destroy or terminate kernel objects.
- Memory protection (that is, detection of null pointer and other invalid memory accesses).
- Support for task hooks (that is, `taskHookLib`).
- Floating point and other coprocessor support.
- The I/O system.
- Signals.
- Processes (RTPs).
- ISR Objects (that is, `isrLib`).
- WDB target agent.
- Networking.
- C++ support.
- System Viewer logging.
Device Drivers for Minimal Kernel Systems

Since there is no I/O system in this profile, there is no support for traditional device access API’s like `open()`, `close()`, `read()`, `write()`, and so on. Device drivers written for such systems have to be standalone programs that manage devices directly. DMA-safe buffers (if needed) must be allocated at compile time. Since there is no support for `malloc()` or `free()`, there is correspondingly no support for `cacheDmaMalloc()` or `cacheDmaFree()` either.

Formatted Character String Output

The `INCLUDE_FORMATTED_OUT_BASIC` component supplies capability for formatted output with routines like `printf()`, `sprintf()`, `snprintf()`, `printfExc()`, and so on. In the minimal and basic kernel profiles, these routines are limited to outputting only integer values and strings. The supported formats are `%d`, `%s`, `%c`, `%x`, `%X`, `%u`, and `%o` only. Floating point, 64-bit, or vector type formats are not supported in this profile.

Users should note that there is no I/O system in the minimal kernel profile. Therefore, there are no file descriptors, and the notion that `printf()` output is sent to file descriptor 1 does not hold true in this profile. The `printf()` routine does work for the formats above, but its output is sent to the console device through a dedicated function. Any assumption about file descriptors, standard output or standard error should be avoided in this profile because they do not exist in that form.

Minimal kernel systems are meant to be the foundation for building small fixed-function controller type systems that are traditionally implemented using micro-controllers.

Minimal Kernel Profile Components

The components that make up the VxWorks minimal kernel profile are listed in Table 2-2.
### Table 2-2  Minimal Kernel Profile Components

<table>
<thead>
<tr>
<th>Component</th>
<th>Library In API Reference</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>INCLUDE_EDR_STUB</td>
<td></td>
<td>Error detection and reporting stub (always present).</td>
</tr>
<tr>
<td>INCLUDE_ANSI_CTYPE</td>
<td>ansiCtype</td>
<td>ANSI ctype routines like <code>isalpha.o</code>, <code>isntrnrl.o</code>, <code>isdigit.o</code>, and so on.</td>
</tr>
<tr>
<td>INCLUDE_ANSI_BSEARCH</td>
<td></td>
<td>ANSI <code>bsearch()</code> routine</td>
</tr>
<tr>
<td>INCLUDE_ANSI_STDLIB_NUMBERS</td>
<td>ansiStdlib</td>
<td>ANSI stdlib string-number conversion routines.</td>
</tr>
<tr>
<td>INCLUDE_ANSI_STRING</td>
<td>ansiString</td>
<td>Full collection of ANSI string routines.</td>
</tr>
<tr>
<td>INCLUDE_ANSI_ABS</td>
<td></td>
<td>ANSI <code>abs()</code> routine.</td>
</tr>
<tr>
<td>INCLUDE_ANSI_MEMCHR</td>
<td>ansiString</td>
<td>ANSI <code>memchr()</code> routine.</td>
</tr>
<tr>
<td>INCLUDE_ANSI_MEMCPY</td>
<td>ansiString</td>
<td>ANSI <code>memcpy()</code> routine.</td>
</tr>
<tr>
<td>INCLUDE_ANSI_MEMSET</td>
<td>ansiString</td>
<td>ANSI <code>memset()</code> routine.</td>
</tr>
<tr>
<td>INCLUDE_ANSI_MEMCMP</td>
<td>ansiString</td>
<td>ANSI <code>memcmp()</code> routine.</td>
</tr>
<tr>
<td>INCLUDE_ANSI_MEMMOVE</td>
<td>ansiString</td>
<td>ANSI <code>memmove()</code> routine.</td>
</tr>
<tr>
<td>INCLUDE_ANSI_STRCAT</td>
<td>ansiString</td>
<td>ANSI <code>strcat()</code> routine.</td>
</tr>
<tr>
<td>INCLUDE_ANSI_STRNCAT</td>
<td>ansiString</td>
<td>ANSI <code>strncat()</code> routine.</td>
</tr>
<tr>
<td>INCLUDE_ANSI_STRCMP</td>
<td>ansiString</td>
<td>ANSI <code>strcmp()</code> routine.</td>
</tr>
<tr>
<td>INCLUDE_ANSI_STRNCMP</td>
<td>ansiString</td>
<td>ANSI <code>strncmp()</code> routine.</td>
</tr>
<tr>
<td>INCLUDE_ANSI_STRCPY</td>
<td></td>
<td>ANSI <code>strcpy()</code> routine.</td>
</tr>
<tr>
<td>INCLUDE_ANSI_STRNCPY</td>
<td></td>
<td>ANSI <code>strncpy()</code> routine.</td>
</tr>
<tr>
<td>INCLUDE_ANSI_STRLEN</td>
<td></td>
<td>ANSI <code>strlen()</code> routine.</td>
</tr>
<tr>
<td>INCLUDE_REBOOT_HOOKS</td>
<td>rebootLib</td>
<td>Support for reboot hooks; that is <code>rebootHookAdd()</code></td>
</tr>
</tbody>
</table>
### Minimal Kernel Profile Components

<table>
<thead>
<tr>
<th>Component</th>
<th>Library In API Reference</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>INCLUDE_VXEVENTS</td>
<td>eventLib, semEvLib and msgQEvLib</td>
<td>VxWorks events support.</td>
</tr>
<tr>
<td>INCLUDE_SEM_BINARY</td>
<td>semBLib</td>
<td>Support for binary semaphores.</td>
</tr>
<tr>
<td>INCLUDE_SEM_MUTEX</td>
<td>semMLib</td>
<td>Support for mutex semaphores.</td>
</tr>
<tr>
<td>INCLUDE_SEM_COUNTING</td>
<td>semCLib</td>
<td>Support for counting semaphores.</td>
</tr>
<tr>
<td>INCLUDE_TASK_UTIL</td>
<td>taskUtilLib</td>
<td>Programmatic interface for modifying task information.</td>
</tr>
<tr>
<td>INCLUDE_WATCHDOGS</td>
<td>wdLib</td>
<td>Support for watchdog timers.</td>
</tr>
<tr>
<td>INCLUDE_HOOKS</td>
<td>hookLib</td>
<td>Hook routine table support (hookLib).</td>
</tr>
<tr>
<td>INCLUDE_VX_NATIVE_SCHEDULER</td>
<td>fioLib has lib</td>
<td>VxWorks native scheduler - priority based preemptive scheduling.</td>
</tr>
<tr>
<td>INCLUDE_FORMATTED_OUT_BASIC</td>
<td>fioLib has lib</td>
<td>Support for printf(), sprintf(), snprintf(), oprintf(), and printErr() only. No support for scanf() and its variants. No support for vprintf() or its variants. No support for floating point or vector formats.</td>
</tr>
<tr>
<td>INCLUDE_BOOT_LINE_INIT</td>
<td>bootParseLib</td>
<td>Parse boot device configuration information</td>
</tr>
</tbody>
</table>

**Programming Guidelines For Minimal Kernel Systems**

The most basic programming guideline for developers of systems based on the minimal kernel profile is that they restrict their application to using the facilities provided by this profile. Of course, other components and advanced functionality are available from higher profiles, but they come at a cost to both system footprint and performance. The component set for the minimal kernel profile is designed to provide the foundation for small statically-resourced kernel-based applications. Statically-resourced systems are systems in which all application and kernel entities are pre-allocated by the compiler at system build time. There are no
memory allocations done at runtime. Therefore minimal kernel systems spend less
time initializing themselves, and have no chance of resource unavailability. There
is a much higher level of determinism in these systems because resources are
guaranteed to be available as soon as the system boots. All the application need do
is to initialize the storage appropriately before it is ready for business.

Static instantiation of kernel objects of explained in detail in 2.7.3 Static
Instantiation of Kernel Objects, p.56.

Static instantiation of system resources does have its own shortcomings. It is not
suitable for systems that are inherently dynamic in nature, that is, ones that have
widely differing amounts of loads, or those that need to instantiate and destroy
objects on demand. Hence statically-resourced systems are not suitable for all
types of applications.

Basic Kernel Profile

The level above the minimal kernel is represented by the basic kernel profile
(PROFILE_BASIC_KERNEL). The basic kernel profile is meant to support small
VxWorks systems that build on the minimal kernel to provide more capabilities
meant to support moderately complex applications. In systems with more
advanced facilities, components from the basic kernel profile serve as a foundation
for higher-level kernel and operating system services. Systems based on the basic
kernel profile are still little more than a kernel. In addition to the a minimal kernel
system, the basic kernel profile offers support for the following facilities:

- Inter-task communication using message queues.
- Support for task hooks.
- Memory allocation and free (using memPartLib).
- Ability to dynamically create and delete kernel objects such as tasks,
  semaphores, watchdogs and message queues (enabled by memPartLib).
- Support for ANSI string routine strdup(), which relies on malloc().

Device Drivers for Basic Kernel Systems

Like the minimal kernel profile, there is no I/O system present in the basic kernel
profile. Hence device drivers for such systems must be standalone programs,
managing devices directly. Since malloc() and free() are supported,
cacheDmaMalloc() and cacheDmaFree() are available starting with this profile.

Basic kernel systems are still basically just kernels, and do not support memory
protection.
Formatted Character String Output

The very same limitations on formatted output apply to the basic kernel profile, as are present for the minimal kernel profile. See Formatted Character String Output, p.32.

Basic Kernel Profile Components

The components listed in Table 2-3 make up the VxWorks basic kernel profile.

Table 2-3 Basic Kernel Profile Components

<table>
<thead>
<tr>
<th>Component</th>
<th>Library In API Reference</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>INCLUDE_ANSI_STRDUP</td>
<td>ansiString</td>
<td>ANSI strdup() routine.</td>
</tr>
<tr>
<td>INCLUDE_TASK_CREATE_DELETE</td>
<td>taskLib</td>
<td>Support for taskSpawn(), taskCreate(), taskDelete() and exit().</td>
</tr>
<tr>
<td>INCLUDE_TASK_RESTART</td>
<td>taskLib</td>
<td>Support for taskRestart().</td>
</tr>
<tr>
<td>INCLUDE_EXC_TASK</td>
<td>excLib</td>
<td>Support for excJobAdd().</td>
</tr>
<tr>
<td>INCLUDE_ISR_OBJECTS</td>
<td>isrLib</td>
<td>Interrupt service routine objects library.</td>
</tr>
<tr>
<td>INCLUDE_MSG_Q</td>
<td>msgQLib</td>
<td>Message queue support with msgQInitialize(), msgQReceive(), msgQSend() and so on.</td>
</tr>
<tr>
<td>INCLUDE_MSG_Q_CREATE_DELETE</td>
<td>msgQLib</td>
<td>Message Queue creation and deletion support with msgQCreate() and msgQDelete().</td>
</tr>
<tr>
<td>INCLUDE_MSG_Q_INFO</td>
<td></td>
<td>Support for msgQInfoGet().</td>
</tr>
<tr>
<td>INCLUDE_SEM_DELETE</td>
<td>semLib</td>
<td>Support for semaphore deletion with semDelete().</td>
</tr>
<tr>
<td>INCLUDE_SEM_BINARY_CREATE</td>
<td>semBLib</td>
<td>Support for semBCreate().</td>
</tr>
<tr>
<td>INCLUDE_SEM_COUNTING_CREATE</td>
<td>semCLib</td>
<td>Support for semCCreate().</td>
</tr>
<tr>
<td>INCLUDE_SEM_MUTEX_CREATE</td>
<td>semMLib</td>
<td>Support for semMCreate().</td>
</tr>
</tbody>
</table>
Programming Guidelines For Basic Kernel Systems

The basic kernel profile is an evolution of the minimal kernel profile. The notable additions in this profile are support for message queues and task hooks and support for memory allocation and deallocation. This allows applications based on this profile to be more dynamic and feature rich than the minimal kernel. This profile is, however, still a kernel and not an operating system. It has a full complement of intertask communications mechanisms and other kernel features, but does not have operating system capabilities such as an I/O system, file system support, or higher-level constructs such as pipes and so on.

<table>
<thead>
<tr>
<th>Component</th>
<th>Library In API Reference</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>INCLUDE_SEM_INFO</td>
<td>Support for semInfo().</td>
<td></td>
</tr>
<tr>
<td>INCLUDE_TASK_INFO</td>
<td>Support for taskInfoGet().</td>
<td></td>
</tr>
<tr>
<td>INCLUDE_TASK_HOOKS</td>
<td>taskHookLib</td>
<td>Support for adding/removing hook routines at task creation, deletion and task switches.</td>
</tr>
<tr>
<td>INCLUDE_WATCHDOGS_CREATE_DELETE</td>
<td>wdLib</td>
<td>Support for wdCreate() and wdDelete().</td>
</tr>
<tr>
<td>INCLUDE_MEM_MGR_BASIC</td>
<td>memPartLib</td>
<td>Memory partition manager; malloc() and free().</td>
</tr>
<tr>
<td>INCLUDE_HASH</td>
<td>hashLib</td>
<td>Hash table management library.</td>
</tr>
<tr>
<td>INCLUDE_LSTLIB</td>
<td>lstLib</td>
<td>Doubly linked list subroutine library.</td>
</tr>
<tr>
<td>INCLUDE_RNG_BUF</td>
<td>rngLib</td>
<td>Ring buffer management library.</td>
</tr>
<tr>
<td>INCLUDE_POOL</td>
<td>poolLib</td>
<td>Memory pool management library.</td>
</tr>
</tbody>
</table>
Basic OS Profile

The basic OS profile (PROFILE_BASIC_OS) builds upon the basic kernel profile to offer a relatively simple real-time operating system. This configuration is relatively similar to a VxWorks 5.5 configuration without the network stack and debug assistance tools. With this profile, the system is now an operating system instead of a kernel. The new capabilities added in this profile are the following:

- I/O system.
- Standard I/O file descriptors and associated API support.
- APIs for directory and path manipulations, and disk utilities.
- Support for select().
- TTY and Pipe driver support.
- Support for logging (logLib).
- Support for task and environment variables (envLib, taskVarLib).
- Support for coprocessor management (coprocLib) and floating point.
- Full-featured memory partition manager (memLib).
- Full ANSI library support. Adds support for assert(), setjmp() and longjmp(), stdio, stdlib, and time library routines.

Device Drivers for Basic OS Systems

Device drivers for the basic OS can now use the IO system, associated capabilities like select(), and so on. File descriptor based I/O and associated APIs are available. Another major addition is coprocessor support, which typically provides support for hardware floating point operations. Vector operations (that is, PowerPC Altivec) are also available with the coprocessor support infrastructure (coprocLib). More advanced ANSI routines are available, that use the standard I/O system, ANSI time facilities and mathematical routines.

Formatted Character String Output

Full ANSI formatted I/O routines are available starting with the basic OS profile. Formatted output routines like printf(), sprintf(), and so on can handle floating point or vector types if applicable. Formatted input routines such as scanf() and so on are also available. These routines send their output to the standard I/O file descriptors as expected. These capabilities are available with the INCLUDE_FORMATTED_IO component.

Basic OS Profile Components

The components included with the basic OS profile are listed in Table 2-5.
## 2.5 Kernel Images, Components, and Configuration

<table>
<thead>
<tr>
<th>Component</th>
<th>Library In API Reference</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>INCLUDE_ANSI_ASSERT</td>
<td>ansiAssert</td>
<td>ANSI <code>assert()</code> routine.</td>
</tr>
</tbody>
</table>
| INCLUDE_ANSI_LOCALE    | ansiLocale               | ANSI locale routines `localeconv()` and `setlocale()`.
| INCLUDE_ANSI_LONGJMP   | ansiSetjmp               | ANSI `setjmp()` and `longjmp()` routines.        |
| INCLUDE_ANSI_MATH      | ansiMath                 | ANSI math routines.                              |
| INCLUDE_ANSI_STDIO     | ansiStdio                | ANSI stdio routines.                             |
| INCLUDE_ANSI_STDLIB    | ansiStdlib               | ANSI stdlib routines.                            |
| INCLUDE_ANSI_ABORT     | ansiStdlib               | ANSI `abort()` routine.                          |
| INCLUDE_ANSI_TIME      | ansiTime                 | ANSI time routines                               |
| INCLUDE_ANSI_STRERROR  | ansiStdio                | POSIX `strerror()` routine.                     |
| INCLUDE_POSIX_CLOCKS  | clockLib                 | POSIX clock library support for `clock_getres()`, `clock_setres()`, `clock_gettime()` and `clock_settime()`.
| INCLUDE_ENV_VARS       | envLib                   | Environment variable library; `getenv()`, `setenv()` and so on. |
| INCLUDE_TASK_VARS      | taskVarLib               | Task variables support library; `taskVarAdd()`, `taskVarDelete()`, `taskVarGet()`, `taskVarSet()` and so on. |
| INCLUDE_SIGNALS        | sigLib                   | Software signal library; support for `signal()`, `kill()` and so on. |
| INCLUDE_EDR_PM         |                          | Error detection and reporting persistent memory region manager. |
| INCLUDE_TTY_DEV        | ttyLib                   | TTY device driver.                               |
| INCLUDE_FLOATING_POINT | floatLib                 | Floating point scanning and formatting library. |
| INCLUDE_FORMATTED_IO   | fioLib                   | Full formatted I/O support; `printf()`, `scanf()`, and variants. |
Systems using the basic OS profile build upon the minimal and basic profile functionality, and are meant to provide the basic mechanisms on which a full-fledged OS API can be built.

### 2.5.5 Customizing VxWorks Code

VxWorks operating system code can itself be customized. This section introduces customization of `usrClock()`, hardware initialization, and more general features of the operating system.

#### System Clock Modification

During system initialization at boot time, the system clock ISR—`usrClock()`—is attached to the system clock timer interrupt. For every system clock interrupt, `usrClock()` is called to update the system tick counter and to run the scheduler.
You can add application-specific processing to `usrClock()`. However, you should keep in mind that this is executed in interrupt context, so only limited functions can be safely called. See 3.5.4 Special Limitations of ISRs, p.211 for a list of routines that can be safely used in interrupt context.

Long power management, if used, allows the processor to sleep for multiple ticks. See 2.6 Power Management, p.42. The `usrClock()` routine, and therefore `tickAnnounce()`, is not called while the processor is sleeping. Instead, `usrClock()` is called only once, after the processor wakes, if at least one tick has expired. Application code in `usrClock()` needs to verify the tick counter each time it is called, to avoid losing time due to setting the processor into sleep mode.

**Hardware Initialization Customization**

When the application requires custom hardware, or when the application requires custom initialization of the existing hardware, the BSP needs to be modified to perform the initialization as required. What BSP modifications are required depend on the type of hardware and the type of initialization that needs to be performed. For information about adding or customizing device drivers, the VxWorks Device Driver Developer’s Guide, specifically the introductory sections. For information about custom modifications to BSP code, see the VxWorks BSP Developer’s Guide.

**Other Customization**

The directory `installDir/vxworks-6.x/target/src/usr` contains the source code for certain portions of VxWorks that you may wish to customize. For example, `usrLib.c` is a common place to add target-resident routines that provide application-specific development aids.

If you modify one of these files, an extra step is necessary before rebuilding your VxWorks image: you must replace the modified object code in the appropriate VxWorks archive. The `make` file in `installDir/vxworks-6.x/target/src/usr` automates the details; however, because this directory is not specific to a single architecture, you must specify the value of the CPU variable on the `make` command line:

```
c:\installDir\vxworks-6.x\target\src\usr> make CPU=cputype TOOL=tool
```

This step recompiles all modified files in the directory, and replaces the corresponding object code in the appropriate architecture-dependent directory. After that, the next time you rebuild VxWorks, the resulting system image includes your modified code.
The following example illustrates replacing `usrLib` with a modified version, rebuilding the archives, and then rebuilding the VxWorks system image. For the sake of conciseness, the `make` output is not shown. The example assumes the `pcPentium` BSP; replace the BSP directory name and CPU value as appropriate for your environment.

```
c:\> cd installDir\vxworks-6.1\target\src\usr

installDir\vxworks-6.1\target\src\usr> copy usrLib.c usrLib.c.orig
installDir\vxworks-6.1\target\src\usr> copy developDir\usrLib.c usrLib.c
installDir\vxworks-6.1\target\src\usr> make CPU=PENTIUM TOOL=diab

installDir\vxworks-6.1\target\src\usr> cd installDir\vxworks-6.1\target\config\pcPentium

installDir\vxworks-6.1\target\config\pcPentium2> make
```

### 2.6 Power Management

Starting with the VxWorks 6.2 release, enhanced power management facilities are provided for the Intel Architecture (IA). Facilities provided in earlier releases for other architectures remain the same. The new facilities will be provided for the other architectures in future releases. See 2.6.1 Power Management for IA Architecture, p.42 and 2.6.2 Power Management for Other Architectures, p.50.

#### 2.6.1 Power Management for IA Architecture

VxWorks power management facilities provide for managing the power and performance states of the CPU. These facilities can be used to control CPU power use based on the following:

- CPU utilization
- CPU temperature thresholds
- task and ISR-specific performance states

The VxWorks power management facilities utilize key concepts of the Advanced Configuration and Power Interface (ACPI) Specification, version 3.0. The ACPI specification has not been implemented for VxWorks because of its unsuitability for hard real-time systems and for all the architectures supported by VxWorks. However, the ACPI specification provides useful definitions of power states and power-state transitions, as well as of thermal management, and these definitions
have been incorporated into the design of the VxWorks power management facilities.

**ACPI Processor Power and Performance States**

The ACPI 3.0 specification defines processor power states as well as the transitions that take the processor from one state to the other. Essentially it defines the processor power state machine. This aspect of the specification enables the mapping of the power management features of a CPU to one of the defined states, whether the CPU is ACPI compliant or not. For example, ACPI defines that in power state C1, the processor does not execute instructions but the contents of the caches remain valid (bus snooping still takes place). Many CPUs support such a power state, but manufacturers often use different names to identify that state.

In addition to defining processor power states, ACPI defines performance states where the CPU executes instructions, but not necessarily at its maximum throughput. These states correspond to voltage and or frequency scaling capabilities present on some CPUs, and provide a power management scheme with which power can be managed even if the system is not idle.

*Figure 2-2* illustrates the ACPI-defined power states that apply to the CPU power management for VxWorks.
The G0 global system state is also known as the working state. ACPI requires that all processor power states reside under the G0 state, which is why other G states are not deemed relevant to this feature. The C0 to Cn states are processor power states. Key features of these processor power are as follows:

- In the C0 state the processor is fetching and executing instructions
- In the C1 to Cn states the processor is not executing instructions.
- The higher the power state number, the greater the power saving, but at the cost greater latency in reaction to external events.
- State transitions occur to and from the C0 state. For example, after going from the C0 state to the C1 state the processor must transition back to the C0 state before going to the C2 state. This is because transitions are triggered by software and only the C0 state is allowed to execute instructions.

Under the C0 power state reside the processor performance states. In each of these states the CPU is executing instructions but the performance and power savings in each P-state vary. The higher the performance state number, the greater the power saving, but the slower the rate of instruction execution. Taking the Speedstep technology of the Pentium processor as an example, it defines various voltage-frequency power settings that are mapped to the ACPI-defined P-states. Note that unlike the C-state transitions, P-state transitions can occur between any two states.

See the VxWorks Architecture Supplement for information about which states are supported

**ACPI Thermal Management**

Much like the processor power management concepts, the thermal management concepts defined in ACPI 3.0 are applicable to non ACPI-compliant hardware. Some of these concepts are:

- Differentiation between active cooling and passive cooling. Actively cooling a system consists of activating a cooling device such as a fan. Passive cooling is achieved by reducing the power consumed by a device. In this case device includes processors and therefore this is relevant to this feature.
- Critical Shut Down. It is the temperature threshold at which a device or system is shut down so as to protect it from heat induced damages.
- Notification of temperature changes. This allows a power management entity to actively or passively manage the temperature of a system or device without the need to poll the devices to obtain their operating temperature.
- Processor Throttling. This is the link between thermal management and the processor power states. ACPI equations define how to manage the performance states of a processor so as to attempt to keep it inside a temperature zone.

VxWorks Power Management Facilities

The architecture of the VxWorks power management facilities is composed of two basic elements: a power management framework and a power manager. The power management framework is effectively a layer between the power manager and the CPU. It transfers status information from the CPU to the power manager, and executes control of the CPU based on instructions from the power manager. *Figure 2-3 Power Management Architecture*, p.45 illustrates this relationship.

The power management framework is designed to accommodate two use cases for controlling power consumption: one minimizes the power level of the CPU based
on how much work it has to do (and its temperature); the other runs tasks and ISRs at different performance states based on their priority.

Wind River provides the power management framework and a two power managers—only one of which can be used at a time (see Wind River Power Managers, p. 48).

You can develop your own power manager for either of the two use cases supported by the power management framework (see Power Management Framework and Use Cases, p. 46). The APIs provided by the power management framework give you the control mechanisms for a power manager (see Power Management Framework API, p. 49).

**Power Management Framework and Use Cases**

The VxWorks power management framework is designed to serve two use cases: one bases the control of power consumption on how much work needs to be done; and the other bases the control of power consumption on task-specific performance states.

One use case involves controlling the power consumed by the CPU based on how much work the CPU has to do. The idea is to keep the power level of the CPU as low as possible while preventing the system from going into overload. That is, prevent running at 100% CPU utilization for a user-defined period of time. It is quite clear that the writers of the ACPI specification had this use case in mind while writing the document.

The second use case is based on the premise that power can be controlled by having tasks and ISRs execute at different performance states (P-states). For example, a task that performs work queued up by interrupts may need to run at the P0 performance state (highest performance) while a maintenance task with no hard deadlines can afford to run at the lowest performance state.

The first use case is more global in nature in that the entire system is running in a certain power state. It is also a scenario in which the power consumption is dynamically adapted to the work that is required of the processor. One of the drawback of the approach however is that it makes it difficult to guarantee deadlines can be met, as a piece of code is not guaranteed to run in the same performance state on every invocation. The second use case provides a finer granularity of control and can be more deterministic, since each task can be set to run in the same performance state at all times. This comes at the price of increased context switching and interrupt handling times.
CPU Utilization Based Power Management

A CPU utilization based power manager is one that uses CPU utilization and CPU temperature to control the power consumption of the CPU. There are really two aspects to this approach. One is to transition the CPU from the C0 power state (executing state) to one of the other C-states (non-executing states) when the VxWorks kernel becomes idle. The other aspect is to control the performance state (P-state) of the CPU so as to keep it inside a specified range of CPU utilization and, optionally, inside a temperature range. In order to support a power manager using this approach, the power management framework has the following features:

- The framework notifies the power manager when the VxWorks kernel goes idle.
- The framework notifies the power manager when the VxWorks kernel comes out of idle state.
- The framework allows the power manager to transition the CPU from the C0 state to any of the non-executing power states: C1, C2, ...Cn. Note that the transition back to the C0 state occurs when an external event takes place (that is, an interrupt) and therefore this is not a state transition the framework can allow the power manager to perform/control.
- The framework allows the power manager to transition the CPU between performance states (P-states) based on the CPU utilization over a user-defined time interval. This is achieved by the framework keeping track of CPU utilization and reporting that figure to the power manager.
- The framework computes the CPU utilization over two user-specified time intervals. Having two intervals makes it easier for the power manager to implement a quick ramp up, slow ramp down policy through the performance states. The sampling intervals can be modified dynamically.
- The framework notifies the power manager when a CPU-utilization interval has elapsed and provides the CPU utilization figure to the power manager at that time.

NOTE: While the power management framework allows for various power management methods to be used, the power manager itself must be designed to ensure that it uses the capacities of the framework in a coherent manner. For example, the framework cannot prevent contention for the CPU if both of the use cases described above are implemented at the same time. For the same reason, only one power manager should be included in the configuration of VxWorks (the two power managers provided by Wind River are mutually exclusive of one another).
The framework allows the power manager to specify a high and a low temperature threshold for the purpose of being notified whenever the temperature of the CPU crosses either threshold. These thresholds can be modified dynamically. The purpose for these is to allow the power manager to implement a cooling policy such as reducing the CPU performance state to lower power consumption, hence lowering temperature.

The full-featured CPU utilization power manager provided by Wind River is an example of this type of power management. See Wind River Power Managers, p.48.

Task Performance-Based Power Management

The per-task performance power manager is based on the premise that power can be controlled by having tasks execute at different performance states (P-states). For example, a task that performs work queued up by interrupts may need to run at the P0 performance state (highest performance) while a maintenance task with no hard deadlines can afford to run at the lowest performance state. In order to support a power manager using this approach, the power management framework has the following features:

- The framework allows a performance state (P-state) to be assigned to each task and allows that state to be set during context switches.
- The framework allows a single performance state to be assigned for all interrupts in the system so that execution of ISRs can be performed in a performance state other than the one of the interrupted task.

Wind River Power Managers

Wind River provides two power managers that implement CPU-utilization-based power management:

- A full-featured CPU-utilization-based power manager. It manages the C-state of the CPU when VxWorks goes idle, as well as managing the P-state of the CPU based on CPU utilization. See CPU Utilization Based Power Management, p.47.
- A light version of a CPU-utilization-based power manager, which simply manages the C-state of the CPU when VxWorks goes idle. It implements the same power management algorithm that was provided for VxWorks 6.0 and 6.1; and is included in the default configuration of VxWorks configuration so that the power management behavior of the operating system is the same as in versions prior to 6.2. See 2.6.2 Power Management for Other Architectures, p.50 for more information about its features.
The components used to configure VxWorks with these power managers are listed in *Configuring VxWorks With Power Management Facilities*, p. 50.

**Power Management Framework API**

Table 2-5 describes the API provided by the power management framework. Power managers use this API to plug into the framework. The routines are available only in the kernel.

<table>
<thead>
<tr>
<th>Routine</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>cpuPwrCStateSet()</td>
<td>Sets the CPU in a specified power state (C-state).</td>
</tr>
<tr>
<td>cpuPwrPStateSet()</td>
<td>Sets the CPU in a specified performance state (P-state).</td>
</tr>
<tr>
<td>cpuPwrPStateGet()</td>
<td>Returns the performance (P-state) state of the CPU.</td>
</tr>
<tr>
<td>cpuPwrTaskPStateSet()</td>
<td>Sets the performance state (P-state) of a task.</td>
</tr>
<tr>
<td>cpuPwrTaskPStateGet()</td>
<td>Gets the performance state (P-state) of a task.</td>
</tr>
<tr>
<td>cpuPwrTempThreshSet()</td>
<td>Sets the CPU temperature thresholds for the CPU (high and low).</td>
</tr>
<tr>
<td>cpuPwrTempThreshGet()</td>
<td>Returns the temperature thresholds for the CPU.</td>
</tr>
<tr>
<td>cpuPwrUtilPeriodSet()</td>
<td>Sets the two time intervals over which CPU utilization is computed.</td>
</tr>
<tr>
<td>cpuPwrEventHandlersSet()</td>
<td>Registers a set of handlers for the various power management events.</td>
</tr>
</tbody>
</table>

For more information about the routines and the power states supported with VxWorks, see the API reference for `cpuPwrLib`.

Also see the *VxWorks Architecture Supplement* for the mappings between the ACPI specification C-states and P-states and the power modes supported by the CPU in question.
Configuring VxWorks With Power Management Facilities

Configure VxWorks with the INCLUDE_CPU_PWR_MGMT component for the power management framework.

Use one of the power manager components provided by Wind River:

- INCLUDE_CPU_UTIL_PWR_MGR, which is full-featured CPU-utilization-based power manager.
- INCLUDE_CPU_LIGHT_PWR_MGR, which is a light version of a CPU-utilization-based power manager.

For full descriptions of these power managers, see Wind River Power Managers, p.48.

A custom power manager can be used in place of a Wind River power manager. It can be included as an application module or a custom component. For more information, see 2.7.7 Linking Kernel-Based Application Object Modules with VxWorks, p.65 and 2.9 Custom Kernel Components, p.69.

Power Management and System Performance

Performance is a concern for the CPU power management framework because it interfaces to the interrupt handling and scheduling sub-systems in a way that affects the interrupt latency and the task context switch time.

2.6.2 Power Management for Other Architectures

NOTE: For this release, the power management facilities available for architectures other than IA are the same as provided for VxWorks 6.0 and 6.1. These facilities are described in this section.

The features described in 2.6.1 Power Management for IA Architecture, p.42 will be available for other architectures in future releases.

Power management allows the processor to conserve power by entering a low power state. While in this mode, processor register values and memory contents are retained. This feature is implemented by putting the CPU in a non-executing state while the kernel is idle. This has no impact on the operation of peripheral devices except for the system timer when long sleep mode (described below) is selected. The VxWorks power management facilities provide two modes of operation:
- **Short Sleep Mode**
  
  In short sleep mode, the CPU is put in a low power state until the next interrupt occurs, including the system clock interrupt. The maximum amount of time the CPU can sleep is therefore the interval in between system clock interrupts. The short sleep mode does not require special BSP support. It is provided at the architecture level. Refer to the *VxWorks Architecture Supplement* to determine if this mode is supported for your CPU.

- **Long Sleep Mode**
  
  In long sleep mode, the CPU is put in a low power state until the next interrupt occurs, excluding the system clock interrupt. This allows the CPU to sleep until the next system event is scheduled to occur; such as a task that times out on a semaphore take operation or a watchdog that fires. This mode requires BSP support because the system clock interrupt source needs to be turned off, and a mechanism must schedule the CPU to wake after a specified amount of time.

To provide power management support for your system, configure VxWorks with the `INCLUDE_POWER_MGMT_BSP_SUPPORT` component.

For more information, see the *VxWorks BSP Developer’s Guide*. Also see *System Clock Modification*, p.40.
2.7 Kernel-Based Applications

VxWorks applications that execute in the kernel are created as relocatable object modules. (They are referred to here as kernel-based application modules to avoid any confusion with process-based applications.) When a kernel-based application module is built, user code is linked to the required VxWorks API libraries, and an ELF binary is produced. Kernel-based application modules use VxWorks facilities by including header files that define operating system interfaces and data structures.

Kernel-based application modules can be either:

- Downloaded and dynamically linked to the operating system by the object module loader.
- Statically linked to the operating system, making them part of the system image.

Downloading kernel-based application modules is useful for rapid development and debugging, as the operating system image does not need to be rebuilt for each iteration of the application. It can also be used for diagnostic facilities with production systems. Various development tools, including the debugger and the shell (host or kernel), can be used to download and manage modules. Modules can be downloaded to a target from any host file system for which the kernel has support (NFS, ftp, and so on); they can also be stored on the target itself in RAM, flash, ROM, or on disk. Once they have been loaded into the target, kernel-based application modules can be started interactively from the shell or IDE.

Kernel-based application modules that are statically linked to the operating system can be run interactively from the shell or IDE. VxWorks can also be configured to start them automatically at boot time. Static linking and automatic startup are obviously suitable for production systems.

Note that an application that runs in kernel space is not executed as a process; it is simply another set of tasks running in kernel space. The kernel is not protected from any misbehavior that a kernel-based application module might engage in—and the applications are similarly not protected from each other—kernel-based applications and the kernel run in the same address space in supervisor mode.

Note that VxWorks can also be configured with support for applications that execute in user space as processes. See VxWorks Application Programmer’s Guide: Applications and Processes.

2.7.1 Application Structure
 Kernel-based application code is similar to common C or C++ applications, with the exception that it does not require a traditional `main()` routine (unlike a VxWorks process-based application). It simply requires an entry point routine that starts all the tasks required to get the application running.

**NOTE:** If your kernel-based application includes a `main()` routine, do not assume that it will start automatically. Kernel-based application modules that are downloaded or simply stored in the system image must be started interactively (or be started by another application that is already running). The operating system can also be configured to start applications automatically at boot time (see Section 2.7.9 Configuring VxWorks to Run Applications Automatically, p.68).

The entry-point routine performs any data initialization that is required, and starts all the tasks that the running application uses. For example, an kernel-based application might have a routine named like `myAppStartUp()`, which could look something like this:

```c
void myAppStartUp (void)
{
  runFoo();
  tidThis = taskSpawn("tThis", 200, 0, STACK_SIZE,
  (FUNCPTR) thisRoutine,0,0,0,0,0,0,0,0,0,0);
  tidThat = taskSpawn("tThat", 220, 0, STACK_SIZE,
  (FUNCPTR) thatRoutine,0,0,0,0,0,0,0,0,0,0);
  tidAnother = taskSpawn("tAnother", 230, 0, STACK_SIZE,
  (FUNCPTR) anotherRoutine,0,0,0,0,0,0,0,0,0,0);
  return (OK);
}
```

For information about VxWorks tasks and multitasking, see Section 3. Multitasking. For information about working with C++ see Section 11. C++ Development.

### 2.7.2 VxWorks Header Files

Many kernel-based applications make use of VxWorks operating system facilities or utility libraries. This usually requires that the source module refer to VxWorks header files. The following sections discuss the use of VxWorks header files.

VxWorks header files supply ANSI C function prototype declarations for all global VxWorks routines. VxWorks provides all header files specified by the ANSI X3.159-1989 standard.

VxWorks system header files are in the directory `installDir/vxworks-6.x/target/h` and its subdirectories.
VxWorks Header File: vxWorks.h

The header file `vxWorks.h` must be included first by every kernel-based application module that uses VxWorks facilities. It contains many basic definitions and types that are used extensively by other VxWorks modules. Many other VxWorks header files require these definitions. Include `vxWorks.h` with the following line:

```c
#include <vxWorks.h>
```

Other VxWorks Header Files

Kernel-based applications can include other VxWorks header files, as needed, to access VxWorks facilities. For example, a module that uses the VxWorks linked-list subroutine library must include the `lstLib.h` file with the following line:

```c
#include <lstLib.h>
```

The API reference entry for each library lists all header files necessary to use that library.

ANSI Header Files

All ANSI-specified header files are included in VxWorks. Those that are compiler-independent or more VxWorks-specific are provided in `installDir/vxworks-6.x/target/h` while a few that are compiler-dependent (for example `stddef.h` and `stdarg.h`) are provided by the compiler installation. Each toolchain knows how to find its own internal headers; no special compile flags are needed.

ANSI C++ Header Files

Each compiler has its own C++ libraries and C++ headers (such as `iostream` and `new`). The C++ headers are located in the compiler installation directory rather than in `installDir/vxworks-6.x/target/h`. No special flags are required to enable the compilers to find these headers. For more information about C++ development, see 11. C++ Development.

**NOTE:** In releases prior to VxWorks 5.5 we recommended the use of the flag `-nostdinc`. This flag should not be used with the current release since it prevents the compilers from finding headers such as `stddef.h`.

The -I Compiler Flag

By default, the compiler searches for header files first in the directory of the source module and then in its internal subdirectories. In general,
installDir/vxworks-6.x/target/h should always be searched before the compilers’ other internal subdirectories; to ensure this, always use the following flag for compiling under VxWorks:

```
-I %WIND_BASE%/target/h %WIND_BASE%/target/h/wrn/coreip
```

Some header files are located in subdirectories. To refer to header files in these subdirectories, be sure to specify the subdirectory name in the include statement, so that the files can be located with a single -I specifier. For example:

```c
#include <xWorks.h>
#include <sys/stat.h>
```

**VxWorks Nested Header Files**

Some VxWorks facilities make use of other, lower-level VxWorks facilities. For example, the tty management facility uses the ring buffer subroutine library. The tty header file `tyLib.h` uses definitions that are supplied by the ring buffer header file `rngLib.h`.

It would be inconvenient to require you to be aware of such include-file interdependencies and ordering. Instead, all VxWorks header files explicitly include all prerequisite header files. Thus, `tyLib.h` itself contains an include of `rngLib.h`. (The one exception is the basic VxWorks header file `vxWorks.h`, which all other header files assume is already included.)

Generally, explicit inclusion of prerequisite header files can pose a problem: a header file could get included more than once and generate fatal compilation errors (because the C preprocessor regards duplicate definitions as potential sources of conflict). However, all VxWorks header files contain conditional compilation statements and definitions that ensure that their text is included only once, no matter how many times they are specified by include statements. Thus, a kernel-based application module can include just those header files it needs directly, without regard to interdependencies or ordering, and no conflicts will arise.

**VxWorks Private Header Files**

Some elements of VxWorks are internal details that may change and so should not be referenced in a kernel-based application. The only supported uses of a module’s facilities are through the public definitions in the header file, and through the module’s subroutine interfaces. Your adherence ensures that your application code is not affected by internal changes in the implementation of a VxWorks module.

Some header files mark internal details using `HIDDEN` comments:
Internal details are also hidden with private header files: files that are stored in the directory installDir/vxworks-6.x/target/h/private. The naming conventions for these files parallel those in installDir/vxworks-6.x/target/h with the library name followed by .h. For example, the private header file for semLib is installDir/vxworks-6.x/target/h/private/semLibP.h.

2.7.3 Static Instantiation of Kernel Objects

The VxWorks APIs have a long established convention for the creation and deletion of kernel entities. Objects such as tasks, semaphores, message queues and watchdogs are instantiated using their respective creation APIs (for example, taskSpawn(), semXCreate(), and so on) and deleted using their respective delete APIs (for example, msgQDelete(), wdDelete(), and so on.). Object creation is a two-step process: first the memory for the object is allocated from the system, which is then initialized appropriately before the object is considered usable. Object deletion involves invalidation of the object, followed by freeing its memory back to the system. Thus, object creation and deletion are dependent on dynamic memory allocation, usually through the malloc() and free() routines.

Dynamic creation and deletion of objects at run-time is a convenient programming paradigm, though it has certain disadvantages for some real-time critical applications. First, the allocation of memory from the system cannot always be guaranteed. Should the system run out of memory the application cannot create the resources it needs to function. The application must then resort to a suitable error recovery process if any exists, or abort in some fashion. Second, dynamic allocation of memory is a relatively slow operation that may potentially block the calling task. This makes dynamic allocation non-deterministic in performance.

Static instantiation of objects is a faster, more deterministic alternative to dynamic creation. In static instantiation, the object is declared as a compile time variable. Thus the compiler allocates storage for the object in the program being compiled. No more allocation need be done. At runtime the objects memory is available immediately at startup for the initialization step. Initialization of preallocated memory is much more deterministic and faster than dynamic creation. Such static declaration of objects cannot fail, unless the program itself is too large to fit in the systems memory.

Many applications are suited to exploit static instantiation of objects in varying degrees. Most applications require some resources to be created, that last for the
lifetime of the application. These resources are never deleted. In lieu of the latter, objects that last for the lifetime of the application are ideally suited for static (that is, compile time) allocation. To the extent that they are instantiated statically (which we shall see below), the application is that much more fail safe and fast to launch.

See *VxWorks Profiles for a Scaling the Operating System*, p.27 for information about operating system profiles of particular relevance for static instantiation.

**Dynamic Instantiation of an Object**

```c
struct my_object * pMyObj;
...
pMyObj = (struct my_object *) malloc (sizeof (struct my_object));
if (pMyObj != NULL)
    {
        objectInit (pMyOjb);
        return (OK);
    }
else
    {
        /* failure path */
        return (ERROR);
    }
```

**Static Instantiation of an Object**

```c
struct my_object myObj;
...
objectInit (&myOjb);
/* myObj now ready for use */
```

Static instantiation of objects has the following advantages:

- The application logic is made simpler by not having to consider the case when dynamic allocation fails.
- Compile time declaration of objects does not take up space in the executable file or flash memory. If an object is merely declared at compile time but not initialized, it is placed by the compiler in the uninitialized data section (also known as the bss section). Uninitialized data is required by the ANSI C standard to be of value zero. Hence the uninitialized data section (the bss section) of a program does not occupy any space in an executable file or in VxWorks ROM images. Uninitialized data does contribute to the programs runtime footprint in memory, but so does dynamic allocation. The program will not consume any more memory footprint than it did with dynamic allocation of objects.
Using static instantiation whenever possible is more robust, deterministic and fast. Static instantiation of objects is therefore much better suited for real-time applications. On the other hand some applications are inherently dynamic in nature. For these, dynamic creation and deletion is always available.

Static Instantiation of Kernel Objects

Starting with VxWorks 6.2, kernel objects such as tasks, semaphores, message queues and watchdogs can be instantiated statically using the same principles outlined above.

Normally these objects are created using the appropriate create routines for that type of object, and deleted using the appropriate delete routine. As mentioned before, creation and deletion involve dynamic memory allocation and free respectively.

Static instantiation of objects is a two-step process. First the object to be created is declared, usually at global scope. Next the declared object is initialized using an initialization routine, which makes it ready for use. In contrast, dynamic creation with create routines is a one-step process. Static instantiation of objects is thus a little less convenient, but more deterministic. Users can choose the style that suits their purpose.

NOTE: Static instantiation should only be used for objects that are kernel-resident. It is not meant to be used to create objects in a process (RTP).

The following sections describe an alternative static instantiation method for each of these entities.

Scope Of Static Declarations

The macros declaring kernel objects (that is VX_BINARY_SEMAPHORE, VX_TASK, and so on) are usually declared as global variables. Since all these kernel objects are used for inter-task communication and synchronization, their IDs are the means by which other tasks use these objects. Hence global objects and global IDs are the common method by which these objects are accessed and used. However it is not always necessary that they be global. An object declaration can also be done at function scope provided the object stays in a valid scope for the duration of its use.
Static Instantiation of Tasks

The taskSpawn() routine has been the standard method for instantiating tasks. This API relies on dynamic allocations. In order to instantiate tasks statically several macros have been provided to emulate the dynamic instantiation capability provided by taskSpawn() and related routines.

The VX_TASK macro declares a task object at compilation time. It takes two arguments: the task name and its stack size. When calling taskSpawn() the name may be a NULL pointer, but when using the VX_TASK macro, a name is mandatory. The stack size must evaluate to a non-zero integer value and must be a compile-time constant.

The VX_TASK_INSTANTIATE macro is the static equivalent of the taskSpawn() routine. It initializes and schedules the task, making it run according to its priority. VX_TASK_INSTANTIATE evaluates to the task ID of the spawned task if it was successful, or ERROR if not.

The following example illustrate spawning tasks statically:

```c
#include <vxWorks.h>
#include <taskLib.h>
VX_TASK(myTask,4096);
int myTaskId;

STATUS initializeFunction (void)
{
    myTaskId = VX_TASK_INSTANTIATE(myTask, 100, 0, 4096, pEntry, 0,1,2,3,4,5,6,7,8,9);
    if (myTaskId != ERROR)
        return (OK); /* instantiation succeeded */
    else
        return (ERROR);
}
```
Sometimes users may prefer to initialize a task, but keep it suspended until needed later. This can be achieved by using the VX_TASK_INITIALIZER macro, as illustrated below. Since the task is left suspended, users are responsible for calling taskActivate() in order to run the task.

```c
#include <vxWorks.h>
#include <taskLib.h>

VX_TASK(myTask, 4096);
int myTaskId;

STATUS initializeFunction (void)
{
    myTaskId = VX_TASK_INITIALIZER(myTask, 100, 0, 4096, pEntry, 
                                    0, 1, 2, 3, 4, 5, 6, 7, 8, 9);

    if (myTaskId != NULL)
    {
        taskActivate (myTaskId);
        return (OK);
    }
    else
    {
        return (ERROR);
    }
}
```

It is the programmer’s responsibility to pass the same name to VX_TASK_INSTANTIATE as was used in the VX_TASK declaration, which is myTask in this case. The arguments to VX_TASK_INSTANTIATE and their meaning are the same as those passed to taskSpawn(). This makes the usage of VX_TASK_INSTANTIATE consistent with taskSpawn(). Please note the backslash that continues the argument list on the succeeding line. This backslash character is crucial if the arguments span more than one line. This is to ensure correct macro expansion.

Sometimes users may want to initialize tasks and leave them suspended till they are needed at a later point of time. In these cases the macro VX_TASK_INITIALIZER should be used instead. It too will return a task ID on successful initialization or NULL if not. After a task is initialized with VX_TASK_INITIALIZER, it must be explicitly activated (that is, scheduled) using the taskActivate() routine. Otherwise it remains suspended.

For more information, see the API reference for taskLib.

### Static Instantiation Of Semaphores

The macros VX_BINARY_SEMAPHORE, VX_COUNTING_SEMAPHORE and VX_MUTEX_SEMAPHORE are used to declare a semaphore of type binary, counting, and mutex respectively. These macros take the semaphore name as an
argument. The declared semaphores are initialized by calling routines `semBInitialize()`, `semCInitialize()` and `semMInitialize()` respectively.

The three `semXInitialize()` routines are the equivalents of their respective `semXCreate()` routines, the only difference being that the semaphore name used in the associated `VX_XXX_SEMAPHORE` be passed to the `semXInitialize()` routines. The return value from the `semXInitialize()` routines is a semaphore ID that is then used to perform all operations on the semaphores.

The following example illustrates static instantiation of a binary semaphore:

```c
#include <vxWorks.h>
#include <semLib.h>

VX_BINARY_SEMAPHORE(mySemB);   /* declare the semaphore */
SEM_ID mySemBId;               /* semaphore ID for further operations */

STATUS initializeFunction (void)
{
    if (mySemBId = semBInitialize (mySemB, options, 0)) == NULL)
        return (ERROR);  /* initialization failed */
    else
        return (OK);
}
```

For more information, see the API reference for `semLib`.

### Static Instantiation of Message Queues

The macro `VX_MSG_Q` is used to declare a message queue at compile time. It takes three parameters: the name, the maximum number of messages in the message queue, and the maximum size of each message.

After this declaration, the `msgQInitialize()` routine is used to initialize the message queue and make it ready for use.

The following example illustrates static instantiation of a message queue:

```c
#include <vxWorks.h>
#include <msgQLib.h>

VX_MSG_Q(myMsgQ,100,16);         /* declare the msgQ */
MSG_Q_ID myMsgQId;               /* MsgQ ID to send/receive messages */

STATUS initializeFunction (void)
{
    if (myMsgQId = msgQInitialize (myMsgQ, 100, 16, options)) == NULL)
        return (ERROR);  /* initialization failed */
    else
        return (OK);
}
```
As with other static instantiation macros it is crucial to pass exactly the same name used in the \texttt{VX\_MSG\_Q} declaration to the \texttt{msgQInitialize()} routine, or else compilation errors will result. Also it is crucial to pass exactly the same values for the message size and maximum number of messages as was passed to the \texttt{VX\_MSG\_Q} macro.

For more information, see the API reference for \texttt{msgQLib}.

**Static Instantiation of Watchdogs**

The macro \texttt{VX\_WDOG} is used to declare a watchdog at compile time. It takes one parameter, the name of the watchdog. After this declaration, the routine \texttt{wdInitialize()} is used to initialize the watchdog and make it ready for use.

```c
#include <vxWorks.h>
#include <wdLib.h>

VX_WDOG(myWdog);    /* declare the watchdog */
WDOG_ID myWdogId;   /* watchdog ID for further operations */

STATUS initializeFunction (void)
{
    if ((myWdogId = wdInitialize (myWdog)) == NULL)
        return (ERROR);       /* initialization failed */
    else
        return (OK);
}
```

As with other static instantiation macros it is crucial to pass exactly the same name used in the \texttt{VX\_WDOG} declaration to the \texttt{wdInitialize()} routine, or else compilation errors will result.

For more information, see the API reference for \texttt{wdLib}.

### 2.7.4 Applications and VxWorks Kernel Component Requirements

VxWorks is a highly configurable operating system. When kernel-based application modules are built independently of the operating system (see \textit{2.7.5 Building Kernel-Based Application Modules}, p.63), the build process cannot determine if the instance of VxWorks on which the application will eventually run has been configured with all of the components that the application requires (for example, networking and file systems). It is, therefore, useful for application code to check for errors indicating that kernel facilities are not available (that is, check the return values of API calls) and to respond appropriately.

When kernel-based application modules are linked with the operating system, the build system generates errors with regard to missing components. Both the IDE
and CLI tools also provide a mechanisms for checking dependencies and for reconfiguring VxWorks accordingly.

2.7.5 Building Kernel-Based Application Modules

The VxWorks environment provides simple mechanisms for building kernel-based application modules, including a useful set of default makefile rules. Both the IDE and command line can be used to build applications. For command line use, the `wrenv` utility program can be used to open a command shell with the appropriate host environment variables set. See *VxWorks Command-Line Tools User’s Guide: Creating a Development Shell with wrenv* and the *VxWorks Command-Line Tools User’s Guide*.

Using Makefile Include Files for Kernel-based Application Modules

You can make use of the VxWorks makefile structure to put together your own application makefiles quickly and efficiently. If you build your application directly in a BSP directory (or in a copy of one), you can use the makefile in that BSP, by specifying variable definitions that include the components of your application.

You can specify values for these variables either from the `make` command line, or from your own makefiles (when you take advantage of the predefined VxWorks `make` include files).

**ADDED_CFLAGS**
Application-specific compiler options for C programs.

**ADDED_C++FLAGS**
Application-specific compiler options for C++ programs.

Additional variables can be used to link kernel-based application modules with the VxWorks image; see 2.7.7 Linking Kernel-Based Application Object Modules with VxWorks, p.65.

For more information about makefiles, see the *VxWorks Command-Line Tools User’s Guide: Building Applications and Libraries*.

You can also take advantage of the makefile structure if you develop kernel-based application modules in separate directories. Example 2-1 illustrates the general scheme. Include the makefile headers that specify variables, and list the object modules you want built as dependencies of a target. This simple scheme is usually sufficient, because the makefile variables are carefully designed to fit into the default rules that `make` knows about.
Example 2-1  **Skeleton Makefile for Kernel-based Application Modules**

```
# Makefile - makefile for ...
#
# Copyright ...
#
# DESCRIPTION
# This file specifies how to build ...

## It is often convenient to override the following with "make CPU=..."
CPU        = cputype
TOOL       = diab

include $(WIND_BASE)/target/h/make/defs.bsp

## Only redefine make definitions below this point, or your definitions
## will be overwritten by the makefile stubs above.

exe :
  myApp.o

For information about build options, see the VxWorks Architecture Supplement for the target architecture in question. For information about using makefiles to build applications, see the VxWorks Command-Line Tools User's Guide: Building Applications and Libraries.

Statically Linking Kernel-Based Application Modules

In general, kernel-based application modules do not need to be linked before being downloaded to the target. However, when several modules cross reference each other they should be linked to form a single module. With C++ code, this linking should be done before the munch step. (See 11.4.1 Munching C++ Application Modules, p.563.)

The following example is a command to link several modules, using the Wind River linker for the PowerPC family of processors:

```
c:\> dld -o applic.o -r applic1.o applic2.o applic3.o
```

Similarly, this example uses the GNU linker:

```
c:\> ldppc -o applic.o -r applic1.o applic2.o applic3.o
```

In either case, the command creates the object module `applic.o` from the object modules `applic1.o, applic2.o, and applic3.o`. The `-r` option is required, because the object-module output must be left in relocatable form so that it can be downloaded and linked to the target VxWorks image.

2. However, if you are working with C++, it may be also convenient to copy the `.cpp.out` rule from `installDir/vxworks-6.x/target/h/make/rules.bsp` into your application’s makefile.
Any VxWorks facilities called by the kernel-based application modules are reported by the linker as unresolved externals. These are resolved by the loader when the module is loaded into VxWorks memory.

**WARNING:** Do not link each kernel-based application module with the VxWorks libraries. Doing this defeats the load-time linking feature of the loader, and wastes space by writing multiple copies of VxWorks system modules on the target.

### 2.7.6 Downloading Kernel-Based Application Object Modules to a Target

Kernel-based application object modules can be downloaded from the host development IDE or from the kernel shell. Once a module has been loaded into target memory, any subroutine in the module can be invoked, tasks can be spawned, debugging facilities employed with the modules, and so on. It is often useful to make use of a startup routine to run the application (see 2.7.1 Application Structure, p.52).

For information about using the kernel shell and module loader, see 10.2 Kernel Shell, p.495 and 10.3 Kernel Object-Module Loader, p.519. For information about using the IDE, see the Wind River Workbench User’s Guide.

### 2.7.7 Linking Kernel-Based Application Object Modules with VxWorks

In order to produce complete systems that include kernel-based application modules, the modules must be statically linked with the VxWorks image. The makefile variables listed below make it easy to identify the modules that you want statically linked into the run-time system:

- **MACH_EXTRA**
  Names of kernel-based application modules to include in the static link to produce a VxWorks run-time.

- **ADDED_MODULES**
  This variable is reserved for adding modules to a static link from the make command line—do not define a value for this variable in makefiles. Its value is used in the same way as **MACH_EXTRA**, to include additional modules in the link. Reserving a separate variable for use from the command line avoids the danger of overriding any object modules that are already listed in **MACH_EXTRA**.
LIB_EXTRA

Linker options to include additional archive libraries (you must specify the complete option, including the -L for each library). These libraries appear in the link command before the standard VxWorks libraries.

For more information about using makefile variables, see 2.7.5 Building Kernel-Based Application Modules, p.63.

To include your kernel-based application modules in the system image using a makefile, identify the names of the application object modules (with the .o suffix) with MACH_EXTRA. For example, to link the module myMod.o with the operating system, add a line like the following:

MACH_EXTRA = myMod.o

Building the system image with the module linked in is the final part of this step. In the target directory, execute the following command:

c:\> make vxWorks

For information about how to have kernel-based applications start automatically at boot time, see 2.7.9 Configuring VxWorks to Run Applications Automatically, p.68.

2.7.8 Image Size Considerations

The size of the system image is often an important consideration, particularly when kernel-based application modules are linked with the operating system. This is true whether the image is loaded by a boot loader or is self-booting (see 2.5.1 VxWorks Image Types, p.19).

⚠️ CAUTION: For ROM-based images, ensure that ROM_SIZE configuration parameter reflects the capacity of the ROMs used (in both config.h and the BSP makefile).

Boot Loader and Downloadable Image

Generally, VxWorks boot loader code is copied to a start address in RAM above the constant RAM_HIGH_ADRS, and the boot loader in turn copies the downloaded system image starting at RAM_LOW_ADRS. The values of these constants are architecture dependent, but in any case the system image must not exceed the space between the two. Otherwise the system image will overwrite the boot loader code while downloading, potentially killing the booting process.
To help avoid this, the last command executed when you build a new VxWorks image is \texttt{vxsize}, which shows the size of the new executable image and how much space (if any) is left in the area below the space used for boot ROM code:

\begin{verbatim}
  vxsize 386 -v 00100000 00020000 vxWorks
  vxWorks: 612328(t) + 69456(d) + 34736(b) = 716520 (235720 bytes left)
\end{verbatim}

(In this output, \texttt{t} stands for text segment, \texttt{d} for data segment, and \texttt{b} for bss.)

Make sure that \texttt{RAM\_HIGH\_ADRS} is less than \texttt{LOCAL\_MEM\_SIZE}. If your new image is too large, \texttt{vxsize} issues a warning. In this case, you can reconfigure the boot loader to copy the boot ROM code to a sufficiently high memory address by increasing the value of \texttt{RAM\_HIGH\_ADRS} in \texttt{config.h} and in the BSP’s makefile (both values must agree). Then rebuild the boot loader. For more information, see \textit{2.4 Boot Loader}, p.12.

\textbf{Self-Booting Image}

For self-booting images, the data segment of a ROM-resident VxWorks system is loaded at \texttt{RAM\_LOW\_ADRS} (defined in the makefile) to minimize fragmentation.

For a CPU board with limited memory (under 1 MB of RAM), make sure that \texttt{RAM\_HIGH\_ADRS} is less than \texttt{LOCAL\_MEM\_SIZE} by a margin sufficient to accommodate the data segment. Note that \texttt{RAM\_HIGH\_ADRS} is defined in both the BSP makefile and \texttt{config.h} (both values must agree).
2.7.9 Configuring VxWorks to Run Applications Automatically

VxWorks can be configured to start kernel-based applications automatically at boot time. To do so:

1. VxWorks must be configured with the INCLUDE_USER_APPL component.

2. A call to an application entry-point routine must be added to the *usrAppInit()* routine in *installDir/vxworks-6.x/target/proj/projDir/usrAppInit.c*.

   Assuming, for example, that the application entry point routine *myAppStartUp()* starts all the required application tasks, then a call to that routine would be added as follows:

   ```c
   void usrAppInit (void)
   {
     #ifdef USER_APPL_INIT
     USER_APPL_INIT; /* for backwards compatibility */
     #endif

     /* add application specific code here */
     myAppStartUp();
   }
   ```

3. The kernel-base application object modules must be linked with the kernel image (see 2.7.7 Linking Kernel-Based Application Object Modules with VxWorks, p.65).

2.8 Custom Kernel Libraries

2.9 Custom Kernel Components

A VxWorks component is the basic unit of functionality with which VxWorks can be configured. While some components are autonomous, others may have dependencies on other components, which need to be included in the configuration of the operating system for runtime operation. (For introductory information about the components provided with VxWorks, see 2.5.2 VxWorks Components, p.21.)

VxWorks kernel components are described in Component Description Files (CDFs). Both IDE and CLI facilities use these files for configuring the operating system with selected components, for setting component parameter values, and so on. The IDE also uses information in CDFs to display the names and descriptions of components, and to provide links to online help. Components are described in CDFs using the Component Description Language (CDL).

For information about the host IDE and CLI facilities used for configuring and building VxWorks, see the Wind River Workbench User’s Guide and the VxWorks Command-Line Tools User’s Guide.

This section describes the CDL, and provides instruction on how to use the language and files to create or modify software components that can be configured into a VxWorks system.

Note that functionality can be added to VxWorks in the form of kernel modules that do not have to be defined as VxWorks components (see 2.7 Kernel-Based Applications, p.52). However, in order to make use of the either the IDE or CLI configuration facilities, to define dependencies between components, and so on, extensions to the operating system should be defined using the CDL.

If you develop components that you wish to make available to applications running in user space as real-time processes, also see 2.10 Custom System Calls, p.99.
2.9.1 Component Description Language

The component description language (CDL) is used in component description files (CDFs) to describe software components. Each CDF carries the suffix .cdf. A single file can define more than one component. The conventions for component description files are presented in 2.9.2 Component Description File Conventions, p.84.

The Component Descriptor Language has six classes of objects:

- bundle
- component
- parameter
- folder
- selection
- initialization group

Note that a folder describes the order in which components are listed in the IDE; the initialization group defines the order in which they are initialized.

Each of these objects also has properties that are described using the CDL. For example, one of the properties of a component is the component’s name.

Based on the operating system components selected by the user, the configuration facilities (GUI or CLI) create the system configuration files `prjComps.h`, `prjParams.h`, and `prjConfig.c`, which are used in building the specified system. For information about how the configuration and code generation facilities work, see the Wind River Workbench User’s Guide and the VxWorks Command-Line Tools User’s Guide.

This following sections provide descriptions of each of the basic CDL objects and their respective properties.

Components

Components are the basic units of configurable software. They are the smallest, scalable unit in a system. With either the IDE or the command-line `vxprj` facility, a user can reconfigure VxWorks by including or excluding a component, as well as modifying some of its characteristics. The properties of a component include:

- identification of the object code (modules) and source code (configlettes and stubs) used in the build of a project
- configurable parameters, which are typically preprocessor macros used within a component’s configuration code
- integration information that controls how a component is integrated into an executable *system image* (for example, an initialization routine)
- user presentation information used in the IDE (such as the components name and description)

### Component Properties

The component object class defines the source and object code associated with a component, much of the integration information, and any associated parameters.

Dependencies among components can be detected and the related components can be automatically added by the configuration facility (GUI or CLI). It does so by analyzing the global symbols in each object module that belongs to the component, and then determining which other components provide the functionality.

As an example, a message logging component could be defined in CDL as follows:

```c
class INCLUDE_LOGGING {
    NAME message logging
    SYNOPSIS Provides logMsg support
    MODULES logLib.o
    INIT_RTN logInit (consoleFd, MAX_LOG_MSGS);
    CFG_PARAMS MAX_LOG_MSGS
    HDR_FILES logLib.h
}
```

For illustrative purposes, in another configuration system using `#ifdef/#endif` statements, the definition of the logging component might look like this:

```c
#ifdef INCLUDE_LOGGING
    logInit (consoleFd, MAX_LOG_MSGS);
#endif /* INCLUDE_LOGGING */
```

The component object includes the greatest number of properties. The more commonly used properties are described below.

**NAME**

The name that appears next to the component icon in the IDE’s operating system component hierarchy.

**SYNOPSIS**

A brief description of the component’s functionality, which is used in the IDE.

The next four properties are all related to the configuring of code in a project:

**MODULES**

Object files associated with the component.
CONFIGLETES
Common configuration source files having source code that typically makes use of parameters associated with the component. Configlette definitions may include the use of macros. For example:

```
CONFIGLETES $(MY_PARAM)\myFile.c
```

Where MY_PARAM may either be a build macro, or an environment variable.

The default directory for configlettes is `installDir/vxworks-6.x/target/config/comps/src`.

BSP_STUBS
Generic configuration source files that are copied into the BSP directory upon first use. The user can then make BSP-specific alterations to the copied files without affecting other BSPs. Note that a modified stub file is shared by all projects using the same BSP as a base.

**NOTE:** Source code can come in the form of a BSP stub.

HDR_FILES
Header files associated with your configlette code or initialization routine. These are header files that must be included in order for your configlette or initialization routine to compile.

ARCHIVE
The archive file in which to find object modules stored other than in the standard location.

The following property provides configuration information:

CFG_PARAMS
A list of configuration parameters associated with the component, typically a list of preprocessor macros. Each must be described separately by its own parameter object.

The next group of properties control integration of the component into the system image. There is initialization information and dependency information, such as that conveyed by linked symbols, required `#include` and `if/then #include` statements:

INIT_RTN
A one-line initialization routine.

LINK_SYMS
A list of symbols to look up in order to include components from an archive.
REQUIRES
A list of component(s) that do not otherwise have structural dependencies and must be included if this component is included. List only those components that cannot be detected from MODULES; that is, by their associated object files. For example, components with configlettes only or those that reference other components through function pointers. In this latter case, REQUIRES can specify a selection.

EXCLUDES
A list of components that cannot be included if this component is being included.

INCLUDE_WHEN
Sets a dependency to automatically include the specified component(s) when this component is included (that is, it handles nested includes).

INIT_BEFORE
Call the initialization routine of this component before the one specified by this property. This property is effective only in conjunction with _INIT_ORDER.

_INIT_ORDER
The component belongs to the specified initialization group. This property places the specified component at the end of the INIT_ORDER property list of the initialization group object.

Like NAME and SYNOPSIS before them, the following properties also affect user presentation in the IDE:

HELP
List reference pages associated with the component.

_CHILDREN
This component is a child component of the specified folder.

_DEFAULTS
This component is a default component of the specified folder. This property must be used in conjunction with _CHILDREN.

Minimal Component Property Requirements
The minimal set of elements required to define a component are:

- NAME
- CHILDREN for a folder component or _CHILDREN if the component belongs to a given Folder (the root folder is FOLDER_ROOT)
- CFG_PARAMS, if the component has configuration parameters.
The **SYNOPSIS** element is generally useful.

### Component Template

<table>
<thead>
<tr>
<th>Component</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>NAME</td>
<td>// readable name (e.g., &quot;foo manager&quot;). // should be in all lower case.</td>
</tr>
<tr>
<td>SYNOPSIS</td>
<td>desc // one-line description</td>
</tr>
<tr>
<td>MODULES</td>
<td>m1 m2 .. // object modules making up the service. // used to generate dependency // information. // it is important to keep this list // small, since the tool’s dependency // engine assumes that the component is // included if <em>any</em> of the modules are // dragged in by dependency. It may make // sense to split a large number of // modules into several distinct // components.</td>
</tr>
<tr>
<td>CONFIGLETTEs</td>
<td>l s2 .. // source files in the component that are // #included in the master configuration // file. // file paths are assumed to be relative // to $(WIND_BASE)/target/config/comps/src</td>
</tr>
<tr>
<td>BSP_STUBS</td>
<td>s1 s2 .. // source file stubs that should be copied // into the BSP and customized for the // component to work. // file paths are assumed to be relative // to $(WIND_BASE)/target/config/comps/src</td>
</tr>
<tr>
<td>HDR_FILES</td>
<td>h1 h1 .. // header files that need to be included // to use this component. Typically // contains prototypes for the // initialization routine.</td>
</tr>
<tr>
<td>CFG_PARAMS</td>
<td>p1 p2 .. // configuration parameters, typically // macros defined in config[All].h, that // can change the way a component works. // see Parameters, below, for more info.</td>
</tr>
<tr>
<td>INIT_RTN</td>
<td>init( ..) // one-line initialization routine. // if it needs to be more than one line, // put the code in a CONFIGLETTE.</td>
</tr>
<tr>
<td>LINK_SYMS</td>
<td>s1 s2 .. // reference these symbols in order to drag // in the component from the archive. // this tells the code generator how to // drag in components that don’t need to // be initialized.</td>
</tr>
<tr>
<td>REQUIRES</td>
<td>r1 r2 .. // other components required. Note:</td>
</tr>
</tbody>
</table>
// dependencies are automatically calculated based on a components MODULES and LINK_SYM. For example, because nfsLib.o calls rpcLib.o, the tool is able to figure out that INCLUDE_NFS requires INCLUDE_RPC. One only needs to list // requirements that cannot be detected from MODULE dependencies. Typically only needed for components that don't have associated MODULES (e.g., ones with just // configlettes).

EXCLUDES e1 e2 .. // other components that cannot coexist with this component

HELP h1 h2 .. // reference pages associated with the component.
// The default is the MODULES and INIT_RTN // of the component. For example, if the component has MODULE fooLib.o, then the manual page for fooLib is automatically associated with the component (if the manual page exists). Similarly for the components INIT_RTN. If there are any other relevant manual pages, they can be specified here.

MACRO_NEST expr // This is for Wind River internal use only. It is used to tell the bsp2prj script how to create a project corresponding to a given BSP. The 'bsp2prj' script assumes that if a BSP has macro INCLUDE_FOO defined, the corresponding component INCLUDE_FOO should be included in the project. That is not always the case. For example, the INCLUDE_AOUT component should only be included if both INCLUDE_AOUT *and* INCLUDE_LOADER are defined. So for the INCLUDE_AOUT component, we set MACRO_NEST = INCLUDE_LOADER. Similarly all WDB subcomponents have MACRO_NEST = INCLUDE_WDB.

ARCHIVE a1 // archive in which to find the MODULES. // default is lib$(CPU)$(TOOL)vx.a. // file path is assumed to be relative to $(WIND_BASE)/target/lib. // any archive listed here is automatically added to the VxWorks link-line when the component is included. // Note: the tool only analyzes archives associated with included components. This creates a chicken-and-egg problem, because the tool analyzes components
// before they are actually added.
// So if you add a component with an ARCHIVE,
// analysis will be done without the ARCHIVE.
// As a work-around, if a separate archive is
// used, create a dummy component that
// lets the tool know that a new archive
// should be read. Such a component
// should be called INSTALL_something.
// It should contain only NAME, SYNOPSIS,
// and ARCHIVE attributes. Only after the
// user adds it can he or she add other
// components from the archive.

INCLUDE_WHEN c1 c2...
// automatically include this component
// when some other components are included.
// All listed components must be included to
// activate the INCLUDE_WHEN (AND
// relationship). This allows, for example,
// msgQShow to be included whenever msgQ and
// show are included. Similarly, WDB fpp
// support can be included when WDB and fpp
// are included.

INIT_BEFORE c1
// if component c1 is present, our init
// routine must be called before c1.
// Only needed for component releases.

_CHILDREN fname
// Component is a child of folder or
// selection fname.

_INIT_ORDER gname
// Component is a member of init group gname
// and is added to the end of the
// initialization sequence by default (see
// INIT_BEFORE).

¡WARNING: The _INIT_ORDER and _CHILDREN elements should refer to elements
that have already been defined. If a component is a _CHILDREN element of a folder
that does not exist, it will not be displayed in the IDE.

Bundles

Bundle object can be used to associate components that are often used together,
which facilitates configuration of the operating system with generic sets of
facilities.
For example, the network kernel shell (BUNDLE_NET_SHELL) includes all the components are required to use the kernel shell with a network symbol table:

```plaintext
Bundle BUNDLE_NET_SHELL {
    NAME network kernel shell
    SYNOPSIS Kernel shell tool with networking symbol table
    HELP shell windsh tgtsvr
    COMPONENTS INCLUDE_SHELL \
        INCLUDE_LOADER \
        INCLUDE_DISK_UTIL \
        INCLUDE_SHOW_ROUTINES \
        INCLUDE_STAT_SYM_TBL \
        INCLUDE_DEBUG \
        INCLUDE_UNLOADER \
        INCLUDE_MEM_SHOW \
        INCLUDE_SYM_TBL_SHOW \
        INCLUDE_CPLUS \
        INCLUDE_NET_SYM_TBL
    _CHILDREN FOLDER_BUNDLES
}
```

**Bundle Properties**

- **NAME**
  The name of the bundle as it should appear in the IDE.

- **SYNOPSIS**
  A description of the bundle’s functionality, as it should appear in the IDE.

- **HELP**
  HTML help topics that the bundle is related to.

- **COMPONENTS**
  A list of components to add to the kernel configuration when this bundle is added.

- **_CHILDREN**
  This bundle is a child of the specified folder.

**Folders**

The IDE component hierarchy uses folders to group components logically. Instead of presenting all components in a flat list, it presents them hierarchically. For example, top-level folders might organize components under headings such as network, drivers, OS, and so on.

Folder objects provide a directory-type hierarchy for grouping components that are logically related. Folders allows for the graphical presentation of useful information about each component, such as:
- any hierarchical grouping it has with related components
- its associated dependencies on other components
- its configurable properties
- its integration into an existing system (for component releases)

Folder information only affects user presentation. It is not related to initialization group hierarchy, and thereby the startup sequence, which depend solely on a component’s initialization group. Folders can contain one or more components, selections, and other folders; they do not have parameter or initialization group objects associated with them.

Folders can contain more than one component. For example, the ANSI functionality is contained in a folder described in CDL, as follows:

```
Folder  FOLDER_ANSI {
NAME            ANSI C components (libc)
SYNOPSIS        ANSI libraries
CHILDREN        INCLUDE_ANSI_ASSERT \ 
                INCLUDE_ANSI_CTYPE \ 
                INCLUDE_ANSI_LOCALE \ 
                INCLUDE_ANSI_MATH \ 
                INCLUDE_ANSI_STDIO \ 
                INCLUDE_ANSI_STDLIB \ 
                INCLUDE_ANSI_STRING \ 
                INCLUDE_ANSI_TIME \ 
                INCLUDE_ANSI_STDIO_EXTRA
DEFAULTS        INCLUDE_ANSI_ASSERT INCLUDE_ANSI_CTYPE \ 
                INCLUDE_ANSI_MATH INCLUDE_ANSI_STDIO \ 
                INCLUDE_ANSI_STDLIB INCLUDE_ANSI_STRING \ 
                INCLUDE_ANSI_TIME
}
```

Folders offer great flexibility in grouping components. They allow groups to include more than just a default set of components that are included together. Components can be added and removed from the configuration individually (as defined by the CHILDREN property of the folder object).

**Folder Properties**

The following properties describe a folder:

**NAME**

The name that appears next to the folder icon in the IDE component hierarchy.

**SYNOPSIS**

A brief description of the folder.
CHILDREN
Components, folders, and selections belonging to this folder are called children.

DEFAULTS
The default component(s) that would be included if the folder were added without any overrides. Folder groupings can affect configuration when a folder is added, because components specified by a folder’s DEFAULTS property are added all at once.

NOTE: Use folder objects only when creating a new grouping of components (new or existing). Do not modify existing folders in order to include new components. CDL accommodates that by prepending an underscore to a property name, for example, _CHILDREN.

Folder Template

```
Folder   folder   {
   NAME    name    // readable name (e.g., "foo libraries").
   SYNOPSIS desc    // one-line description
   CHILDREN i1 i2 .. // containers and components
                     // that belong to this container.
   DEFAULTS i1 i2 .. // default CHILDREN.
                     // if the folder represents a complex
                     // subsystem (such as the WDB agent),
                     // this is used to suggest to the user
                     // which components in the folder are
                     // considered "default." That way the user
                     // can add the whole subsystem at once,
                     // and a reasonable set of subcomponents
                     // will be chosen.
}
```

Selections

Selections are similar to folders, but they are components that implement a common interface, for example, serial drivers, the timestamp mechanism, and the WDB communication interface. These components provide alternatives for the same service, and one or more can be selected for configuration in a single project. The selections CDL class is comparable to #ifdef/#else constructs in other configuration systems.
Selection information, like that for folders, is for user presentation only. It is not related to initialization group hierarchy, and thereby the startup sequence. Selections contain one or more components only.

Selections behave like folders, except they add a count for a range of available components; for example, a selection containing three components might tell the user only one can be configured at a time, or perhaps two of three. Because of the count, selections do not contain folders or other selections; nor do they have parameter or initialization group objects associated with them.

Selection Properties

The following properties describe a selection:

NAME
A readable name, the one that appears next to the selection icon in the IDE component hierarchy.

SYNOPSIS
A brief description of the selection.

COUNT
Set a minimum and maximum count from available options.

CHILDREN
Components from which to select.

DEFAULTS
The default component(s), depending on the count.

For example, the following example provides for selection from a set of timestamp drivers, for use with the timestamp component:

```plaintext
Selection SELECT_TIMESTAMP {
    NAME select timestamping
    COUNT 1-1
    CHILDREN INCLUDE_SYS_TIMESTAMP \ 
           INCLUDE_USER_TIMESTAMP \ 
           INCLUDE_SEQ_TIMESTAMP
    DEFAULTS INCLUDE_SEQ_TIMESTAMP
}
```

There are three timestamp drivers available, as indicated by the three values for the CHILDREN property. The COUNT permits a choice of one, that is, a minimum and a maximum of 1.
Selection Template

Selection selection {
    NAME name // readable name (for example, "foo communication path")
    SYNOPSIS desc // one-line description
    COUNT min-max // range of allowed subcomponents.
    // 1-1 means exactly one.
    // 1- means one or more.
    CHILDREN i1 i2.. // components from which to select
    DEFAULTS i1 i2.. // default CHILDREN.
    // this is not used for anything except to
    // to suggest to the user which components
    // in the selection we consider 'default.'
}

Parameters

Parameters are one of the primary means of configuring a component. Typically, one or more parameters control a component's features.

For example, the following parameter defines the maximum number of log messages in the logging queue:

Parameter MAX_LOG_MSGS {
    NAME max # queued messages
    TYPE uint
    DEFAULT 50
}

This is comparable to the C code macro assignment:

#define MAX_LOG_MSGS 50

Parameter Properties

The following properties describe a parameter:

NAME
The name that appears in the IDE.

TYPE
The type of parameter, which can be defined as int, uint, bool, string, exists, or untyped.
When a configuration parameter is defined as type of `exists` the `prjParams.h` file that is automatically generated as part of creating a project will contain either an `#define` or `#undef` statement for the parameter.

For example, the kernel configuration parameter `INCLUDE_CONSTANT_RDY_Q` has a type of `exists`. Thus, if it's value is set to `TRUE` (which is the default), the following will appear in the auto-generated `prjParams.h` file:

```c
#define INCLUDE_CONSTANT_RDY_Q
```

If the value is set to `FALSE`, the following will appear:

```c
#undef INCLUDE_CONSTANT_RDY_Q
```

**DEFAULT**

The default value of the parameter.

### Parameter Template

```
Parameter parameter {
    NAME name // readable name (e.g., "max open files")
    SYNOPSIS desc // one-line description.
    STORAGE storage // MACRO, REGISTRY, ... Default is MACRO.
    TYPE type // type of parameter:
        // int, uint, bool, string, exists.
        // Default is untyped.
        // more types will be added later.
    DEFAULT value // default value of the parameter.
        // default is none - in which case the user
        // must define a value to use the component.
        // for parameters of type "exists," the
        // value is TRUE if the macro should be
        // defined, or FALSE if undefined.
}
```

### Initialization Groups

A component must include information that controls how it is integrated into an executable `system image`. This means that an initialization routine must be identified, as well as the point in the initialization sequence that the routine should be called. Initialization groups assemble related components for initialization and, thus, define the system startup sequence. The definition of the initialization group hierarchy is related only to run-time behavior, and has no impact on GUI presentation.
An initialization group is a routine from which components and other initialization groups are called. The code in the routine is determined by the included components and their initialization code fragments. For example:

```
InitGroup usrIosExtraInit {
  INIT_RTN  usrIosExtraInit();
  SYNOPSIS extended I/O system
  INIT_ORDER INCLUDE_EXC_TASK \
    INCLUDE_LOGGING \
    INCLUDE_PIPES \
    ...
}
```

In this example, the components INCLUDE_EXC_TASK, INCLUDE_LOGGING, and INCLUDE_PIPES are part of the initialization group `usrIosExtraInit`. If those components are included, then their initialization routines are called in the order specified as part of this initialization group.

The code in an initialization group is synthesized by the configuration facility into the file `prjConfig.c`, which always part of every BSP project. To see a system’s initialization sequence, build the project and examine the generated code in `prjConfig.c`.

Initialization Group Properties

The following properties describe an initialization group:

**NAME**

The NAME property can be used to provide a short name for the initialization group; however, it does not appear anywhere in this release of the GUI.

**SYNOPSIS**

The SYNOPSIS property is used to provide a brief, readable definition of the initialization group.

**INIT_RTN**

The initialization routine that initializes an associated component(s).

**INIT_ORDER**

Components and initialization groups belonging to this initialization group listed in the order in which they are to be initialized.
Initialization Group Template

```
InitGroup group {
  INIT_RTN rtn(..) // initialization routine definition
  INIT_ORDER i1 i2 .. // ordered list of init groups and
                      // components that belong to this init group
  INIT_AFTER i2 i2 .. // Only needed for component releases.
}
```

2.9.2 Component Description File Conventions

Follow these conventions when working with component description files, where
**FOO** (or **Foo**) is the variable element of the naming convention:

- All bundles names are of the form `BUNDLE_FOO`.
- All component names are of the form `INCLUDE_FOO`.
- All folders names are of the form `FOLDER_FOO`.
- All selection names are of the form `SELECT_FOO`.
- Parameter names should not match the format of any other object type, but are
  otherwise unrestricted. For example, you can use `FOO_XXX`, but not
  `INCLUDE_FOO`.
- All initialization group names should be of the form `initFoo`. However, Wind
  River initialization groups use the form `usrFoo` for backwards compatibility
  reasons.
- All component description files have a `.cdf` suffix.
- All `.cdf` file names begin with two decimal digits; for example,
  `00comp_foo.cdf`. These first two digits control the order in which `.cdf` files are
  read within a directory. See *CDF Precedence*, p.85 for more information.

Note that more than one component can be defined in a single CDF.

New component description files should be independent of existing files, with two
exceptions:

- New component objects should be bound into an existing folder or selection
  for GUI display purposes.
- New component object initialization routines must be associated with an
  existing initialization group.
A new component object can be bound to an existing folder or selection, and to an existing initialization group, without modifying the existing elements. By prepending an underscore (“_”) to certain component properties, you can reverse the meaning of the property. For example, if there is already a folder FOLDER_EXISTING and an initialization group initExisting, you can bind a new component (which is defined in a different file) to it as follows:

```plaintext
Component INCLUDE_FOO

  _CHILDREN   FOLDER_EXISTING
  _INIT_ORDER initExisting
```

The property _CHILDREN has the opposite relationship to FOLDER_EXISTING as CHILDREN; that is, _CHILDREN identifies the parent. In other words, it produces the same effect as FOLDER_EXISTING having the component INCLUDE_FOO on its list of children—without any modifications being made to the CDF containing the FOLDER_EXISTING object.

Note that INIT BEFORE can be used _INIT_ORDER to define exactly where in the initialization group the component should be placed.

CDF Precedence

More than one CDF may define a given component and its properties. The precedence of multiple definitions is determined by a numbering scheme used with the CDF naming convention and by the order in which directories containing CDFs are read by the configuration facility.

CDF File Naming and Precedence

Wind River reserves the first 50 numbers, that is 00fileName.cdf through 49fileName.cdf. The remaining numbers, 50 through 99, may be used by third parties for their components.

Higher number files have greater precedence than lower number files (when the remainder of the name is the same); for example, the definition of the INCLUDE_FOO component in 66comp_foo.cdf override those in 55comp_foo.cdf.

This method of setting precedence allows project, BSP, and CPU architecture-specific overrides of generic components or parameters.

For example, if a BSP provider wanted to change the maximum number of files that can be open (NUM_FILES) to 75 from the default value of 50, it can be done in a BSP-specific CDF file with a higher file number that has the following entry in it:
Parameter NUM_FILES {
    DEFAULT 75
}

CDF Directories and Precedence

The component description files are read at two points by the configuration facility:

- When a project is created.
- After component description files are changed and the project build occurs.

The order in which CDFs are read is significant. If more than one file describes the same property of the same component, the one read last overrides all earlier ones. The intent is to allow component description files to have some precedence level. Files read later have higher precedence than those read earlier.

Precedence is established in two complementary ways:

- CDF files reside in certain directories, and those directories are read in a specified order.
- Within one of these directories, CDFs are read in alphanumeric order.

The configuration facility sources all .cdf files in any of the following directories. These directories are read in the order in which they are presented:

1. `installDir/vxworks-6.x/target/config/comps/vxWorks`
   
   Contains all generic VxWorks components.

2. `installDir/vxworks-6.x/target/config/comps/vxWorks/arch/arch`
   
   Contains all architecture-specific VxWorks components (or component overrides).

3. `installDir/vxworks-6.x/target/config/bspName`
   
   Contains all BSP-specific components.

4. the project directory
   
   Contains all other components.

Within a directory, to control alphanumeric order, a two digit number is prepended to each .cdf file to determine the order of precedence within a given directory. See 2.9.2 Component Description File Conventions, p. 84.
Adding New CDFs to the VxWorks Installation Tree

If you are creating a new CDF, you must place it in the appropriate path, based on
the nature of the contents and the desired level of precedence. Your choices of
paths are listed above in the discussion about precedence:

- `installDir/vxworks-6.x/target/config/comps/vxWorks` for generic VxWorks
  component descriptions only
- `installDir/vxworks-6.x/target/config/comps/vxWorks/arch/arch` for
  architecture-specific VxWorks component descriptions
- `installDir/vxworks-6.x/target/config/config/bspName` for board-specific
  component descriptions
- the project directory for all other files

CDF Contents Template

Bundle MY_BUNDLE
{
  COMPONENTS :
  HELP :
  NAME :
  SYNOPSIS :
  _CHILDREN :
}

Component MY_COMPONENT
{
  ARCHIVE :
  BSP_STUBS :
  CFG_PARAMS :
  CONFIGLETTERS :
  ENTRY_POINTS :
  EXCLUDES :
  EXCLUDE_WHEN :
  HDR_FILES :
  HELP :
  HIDE_UNLESS :
  INCLUDE_WHEN :
  INIT_AFTER :
  INIT_BEFORE :
  INIT_RTN :
  LINK_DATA_SYMS :
  LINK_SYMS :
  MACRO_NEST :
  MODULES :
  NAME :
  PREP_DOMAIN :
  PROJECT :
}
PROTOTYPE :
REQUIRES :
SHUTDOWN_RTN :
SYNOPSIS :
TERM_RTN :
USES :
_CHILDREN :
_COMPONENTS :
_DEFAULTS :
_EXCLUDES :
_EXCLUDE_WHEN :
_HIDE_UNLESS :
_INCLUDE_WHEN :
_INIT_AFTER :
_INIT_BEFORE :
_INIT_ORDER :
_LINK_DATASYMS :
_LINK_SYMS :
_REQUIRES :
_USES :
}

EntryPoint MY_ENTRYPOINT
{
NAME :
PRIVILEGED :
SYNOPSIS :
_TYPE :
_ENTRY_POINTS :
}

EntryPointType MY_ENTRYPOINT_TYPE
{
SYNOPSIS :
_TYPE :
}

Folder MY_FOLDER
{
_CHILDREN :
_DEFAULTS :
HELP :
NAME :
SYNOPSIS :
_CHILDREN :
_DEFAULTS :
}

InitGroup MY_INITGROUP
{
HELP :
_INIT_AFTER :
_INIT_BEFORE :
_INIT_ORDER :
_INIT_RTN :
NAME :
}
PROTOTYPE :
SHUTDOWN_RTN :
SYNOPSIS :
TERM_RTN :
_INIT_APTER :
_INIT_BEFORE :
_INIT_ORDER :
}

Module MY_MODULE
{
ENTRY_POINTS :
NAME :
SRC_PATH_NAME :
_MODULES :
}

Parameter MY_PARAMETER
{
DEFAULT :
HELP :
NAME :
STORAGE :
SYNOPSIS :
TYPE :
VALUE :
_CFG_PARAMS :
}

Profile MY_PROFILE
{
COMPONENTS :
HELP :
NAME :
SYNOPSIS :
_CHILDREN :
}

Selection MY_SELECTION
{
ARCHIVE :
BSP_STUBS :
CFG_PARAMS :
CHILDREN :
CONFIGLETTES :
COUNT :
DEFAULTS :
EXCLUDES :
HDR_FILES :
HELP :
HIDE_UNLESS :
_INIT_AFTER :
_INIT_BEFORE :
_INIT_RTN :
LINK_DATASYMS :
LINK_SYMS :
2.9.3 Creating and Modifying Components

This section provides instruction on how to define your own component or to modify an existing one. You must follow certain conventions when using the CDL. After describing your component in a component description file, you must place the file in the proper path, to ensure that the configuration facility reads the information and properly includes the component in the hierarchy.

Elements of Components in a VxWorks Installation Tree

Wind River delivers the parts of its components in the following locations:

- Source code modules are usually found in the
  \texttt{installDir/vxworks-6.x/target/src} or \texttt{target/config} directories.

- Headers are found in \texttt{installDir/vxworks-6.x/target/h}; object modules are delivered in \texttt{installDir/vxworks-6.x/target/lib/objARCH}.

- Component description files are in
  \texttt{installDir/vxworks-6.x/target/config/comps/vxWorks}. 

Symbol MY_SYMBOL

\{
\_LINK_DATASYMS :
\_LINK_SYMS :
\}
Component configlettes (source fragments) are in 
\textit{installDir/vxworks-6.x/target/config/comps/src}.

Third parties are not limited to this arrangement, and the location of component elements can be fully described in the component description file.

It is recommended that third parties place their component source code and object elements in a directory, such as \textit{installDir/vxworks-6.x/target/config/vendorName}. The location of the component description file (CDF) depends on where in the system the components should be integrated. See \textit{CDF Precedence}, p.85.

To be able to integrate a new general-purpose VxWorks component into the system, the CDF must be located in \textit{installDir/vxworks-6.x/target/config/comps/vxWorks}. If it is a BSP-specific component, the file should be located in the BSP directory. If it is specific to a single project, it should be located in the project directory \textit{(installDir/vxworks-6.x/target/proj/projectName)}.

Be sure to follow the proper naming and numbering conventions, which are described in \textit{2.9.2 Component Description File Conventions}, p.84.

Defining a Component

This section describes the process of defining your own component. To allow for the greatest flexibility, there is no convention governing the order in which properties describe a component or the sequence in which CDL objects are entered into a component description file. The following steps taken to create the component INCLUDE_FOO are a suggested order only; the sequence is not mandatory. Nor is there meant to be any suggestion that you use all of the properties and object classes described.

Step 1: Name and Provide a Synopsis

To create your new component, first name it and provide a synopsis of its utility.

\begin{verbatim}
Component INCLUDE_FOO { 
  NAME foo component 
  SYNOPSIS this is just an example component 
  ... 
}
\end{verbatim}

In the imaginary component INCLUDE_FOO, the NAME property is \texttt{foo component}. The SYNOPSIS element instructively informs the user that “this is just an example component.”

NAME and SYNOPSIS affect user presentation only; they have no bearing on initialization sequence or dependencies.
Step 2: Describe the Code-Related Elements

Next, describe your component’s code portions by defining any modules and source configlettes that should be configured in during the build.

If your imaginary component INCLUDE_FOO has an object module associated with it—and not source code—use the MODULES property to specify it. You can specify any number of modules this way. In the following example, fooLib.o and fooShow.o are listed:

```plaintext
... MODULES fooLib.o fooShow.o
HDR_FILES foo.h
ARCHIVE fooLib.a
CONFIGLETTES fooConfig.c
...
```

The host IDE offers visibility into component dependencies by graphically presenting component relationships.

The configuration facility (for both the GUI and CLI) automatically analyzes object module dependencies in order to calculate component dependencies. For example, if fooLib.o has an unresolved global for logMsg(), an automatic dependency upon the component INCLUDE_LOGGING is detected. For components not shipped in object module format, CDL supports the explicit listing of component dependencies.

If the source portion contains a call to logMsg(), the configuration facility does not detect the dependency; instead, an explicit dependency upon INCLUDE_LOGGING should be declared using the REQUIRES property. See Step 6.

If your module is not located in the standard path, use the ARCHIVE property to specify the archive name, for example, /somePath/fooLib.a. (For additional instructions concerning the use of ARCHIVE, see Step 10.)

NOTE: Developers should create their own archives for custom components. Do not modify Wind River archives.

Use the HDR_FILES property to specify any .h files associated with the component, like foo.h. These files are emitted in prjConfig.c, providing the initialization routine with a declaration.

If there is source code that should be compiled as part of the component, put it in a .c file and specify the file name in the CONFIGLETTES property, for example, fooConfig.c. Component parameters should be referenced in the configlette or in the initialization routine; otherwise they have no effect.
Step 3: **Set Up Initialization**

If your component must be initialized, use the `INIT_RTN` property of the component object class to specify the initialization routine and its arguments to be called, as in `fooInit(arg1, arg2)`. If your component needs more than a single line of initialization, create or add the initialization code to a `.c` file and use the `CONFIGLETTES` property instead. By associating a configlette with an initialization routine, you are securing the configlette’s place in the initialization sequence.

```c
... INIT_RTN fooInit(arg1, arg2);
...
```

If you are not using the `MODULES` property of the component object, use the `LINK_SYMS` property to include your object module from a linked archive. The system generates an unresolved reference to the symbol `fooRtn1` in this example, causing the linker to resolve it by extracting your module from the archive.

```c
... LINK_SYMS fooRtn1
...
```

Step 4: **Establish the Initialization Sequence**

Initialization order is important. You can control when in the initialization sequence your component is initialized with the `_INIT_ORDER` property. A component (or initialization group) that is bound to an existing initialization group using the `_INIT_ORDER` property is, by default, initialized last within that group. This is typically the desired effect; however, you can override this behavior by explicitly using the `INIT_BEFORE` property.

```c
... _INIT_ORDER usrRoot
  INIT_BEFORE INCLUDE_USER_APPL
...
```

In the example, `INCLUDE_FOO` is declared a member of the `usrRoot` initialization group. `INIT_BEFORE` has been used for fine control, and `INCLUDE_FOO` is initialized before `INCLUDE_USER_APPL`.

Alternatively, you can create a new initialization group and declare `INCLUDE_FOO` a member; however, you would have to declare the new initialization group a member of an existing initialization group. For more information on initialization groups, see *Initialization Groups*, p.82.

**NOTE:** `INIT_BEFORE` only affects ordering within the initialization group. Do not reference a component that is not in the initialization group; this has no effect at all.
Step 5: **Link Helpful Documentation**

Specify related reference entries (in HTML format) with the `HELP` property.

```
... HELP fooMan ...
```

By default, a build automatically includes reference entries related to values declared by the `MODULES` and `INIT_RTN` properties. In the case of `INCLUDE_FOO`, in addition to `fooMan.html`, which is specified by `HELP`, the build associates the `fooLib` and `fooShow` libraries and the `fooInit()` routine.

Step 6: **Define Dependencies**

Use the `REQUIRES`, `EXCLUDES`, and `INCLUDE_WHEN` properties to explicitly declare dependencies among components. (See Step 2 to learn how the configuration facility automatically configures object module-related dependencies.)

The configuration facility does not detect implicit dependencies when a component is not shipped in object module format. Likewise, no dependencies are detected when symbols are referenced by pointers at run-time. Both circumstances require you to declare dependencies explicitly.

```
... REQUIRES INCLUDE_LOGGING
EXCLUDES INCLUDE_SPY
INCLUDE_WHEN INCLUDE_POSIX_AIO INCLUDE_POSIX_MQ ...
```

In the example, `REQUIRES` declares that `INCLUDE_LOGGING` must be configured along with `INCLUDE_FOO`. `EXCLUDES` declares the `INCLUDE_SPY` cannot be configured with `INCLUDE_FOO`. And `INCLUDE_WHEN` tells the system that whenever the components `INCLUDE_POSIX_AIO` and `INCLUDE_POSIX_MQ` are included, then `INCLUDE_FOO` must also be included.

**NOTE:** In general, the configuration facility is designed to increase flexibility when selecting components, that is, to increase scalability; specifying a `REQUIRES` relationship reduces flexibility. Be sure that using `REQUIRES` is the best course of action in your situation before implementing it.

The `INIT_AFTER` property has no effect in this release.
**Step 7: List Associated Parameters**

In the component object, use the `CFG_PARAMS` property to declare all associated parameters, for example, `FOO_MAX_COUNT`.

```plaintext
...  
  CFG_PARAMS FOO_MAX_COUNT  
...  
```

**Step 8: Define Parameters**

For each parameter declared by `CFG_PARAMS`, create a parameter object to describe it. Provide a name using the `NAME` property.

Use the `TYPE` property to specify the data type, either `int`, `uint`, `bool`, `string`, `exists`, or `untyped`.

Use the `DEFAULT` property to specify a default value for each parameter.

```plaintext
Parameter FOO_MAX_COUNT {  
  NAME Foo maximum  
  TYPE uint  
  DEFAULT 50  
}
```

⚠️ **CAUTION:** A component is considered mis-configured if it contains a parameter without an assigned value. Be sure to assign default values, unless there is no reasonable default and you want to force the user to set it explicitly. (Other CDF files with higher precedence, may of course be used to set the parameter value; see *CDF File Naming and Precedence*, p.85.)

**Step 9: Define Group Membership**

A component must be associated with either a folder or selection, otherwise it is not visible in the IDE. Assign a component to a folder because of its logical inclusion in the group defined by the folder, based on similar or shared functionality, for example. By including a component in a folder, you make it possible for the user to load it simultaneously with other components in its group by declaring them as default values for the folder, using the folder object’s `DEFAULTS` property.

```plaintext
...  
  _CHILDREN FOLDER_ROOT  
...  
```

The `_CHILDREN` property declares that `INCLUDE_FOO` is a child component of folder `FOLDER_ROOT`. The prepended underscore (“_”) serves to reverse the relationship declared by the property `CHILDREN`, which means that `_CHILDREN`
identifies the parent. You can also use _DEFAULTS in conjunction with _CHILDREN to specify a component as a default component of a folder.

If you think a component is becoming too complex, you can divide it into a set of components assigned to a folder or selection object. In the following example, INCLUDE_FOO has been specified as part of a selection. You can add or remove the group from your configuration as a unit rather than by its individual components.

For folders, the DEFAULTS property specifies the base set of components that are included if the group is configured without any overrides.

For selections, the DEFAULTS property specifies the components that are included to satisfy the count (declared by the COUNT property), if you provide no alternative values.

In a selection group, the COUNT property specifies the minimum and maximum number of included components. If the user exceeds these limits the system flags the selection as mis-configured.

```plaintext
Selection SELECT_FOO {
  NAME Foo type
  SYNOPSIS Select the type of desired FOO support
  COUNT 0-1
  CHILDREN INCLUDE_FOO_TYPE1 \
        INCLUDE_FOO_TYPE2 \
        INCLUDE_FOO_TYPE3
  DEFAULTS INCLUDE_FOO_TYPE1
}
```

**Step 10: Create a Dummy Component**

The configuration facility analyzes archives only when they are associated with included components. This creates a chicken and egg problem: in order to know about a particular archive, the configuration facility would need to analyze components before they are actually added. In other words, if you add a component declared with an ARCHIVE property, the configuration analysis is done without knowing what the value of ARCHIVE is. So, if your component includes an archive with several object modules, you should create a dummy component that is always included, making it possible for the configuration facility to know that a new archive should be read. Call such a component INSTALL_FOO. It should contain only NAME, SYNOPSIS, and ARCHIVE properties. The user cannot add other components from the same archive until INSTALL_FOO is added.

⚠️ **CAUTION:** Do not alter Wind River-supplied CDFs directly. Use the naming convention to create a file whose higher precedence overrides the default properties of Wind River-supplied components.
Step 11: Generate the Project Files

Generate the project using the IDE or the CLI vxprj tool.

Note that the project configuration file (prjConfig.c) for each project is generated by the configuration facility based on two things:

- The set of components selected by the user from the component hierarchy.
- The information in the related component description files.

Modifying a Component

You should not modify any Wind River component description file. However, you can effectively modify the properties of existing components by re-specifying them in another, higher priority CDF file. Third-party CDF files are by convention read last and therefore have the highest priority. Use the naming convention to create a high-precedence CDF file that overrides the default properties of Wind River components. See CDF Directories and Precedence, p.86 for the file naming and precedence conventions.

In the following example, the default number of open file descriptors (NUM_FILES) in the standard Wind River component INCLUDE_IO_SYSTEM has been modified. The normal default value is 50.

```
Parameter NUM_FILES {
    DEFAULT 75
}
```

By adding these example lines of code to a third-party CDF file, by removing and adding the component if it is already in the configuration, and by re-building the project, the value of NUM_FILES is changed to 75. The original Wind River CDF file, 00vxWorks.cdf, is not changed; the default property value is changed because the third-party file has higher precedence. Other property values remain the same unless specifically redefined.

⚠️ CAUTION: Do not alter the Wind River-supplied source configlette files in installDir/vxworks-6.x/target/config/comps/src. If necessary, use a BSP- or project-specific CDF to cancel its declaration as CONFIGLETTES and to define BSP_STUB instead. A copy of the generic file is then placed into the BSP directory, and it can be altered without impacting other BSPs, or changing the original file.
Testing New Components

There are several tests that can run to verify that components have been written correctly:

- **Check Syntax and Semantics**
  
  This `vxprj` command provides the most basic test:
  ```
  vxprj component check [projectFile] [component ... ]
  
  For example:
  ```
  ```
  % vxprj component check MyProject.wpj
  ```
  
  If no project file is specified, `vxprj` looks for a `.wpj` file in the current directory. If no component is specified, `vxprj` checks every component in the project. This command invokes the `cmpTest` routine, which tests for syntactical and semantic errors.
  
  Based on test output, make any required modifications. Keep running the script until you have removed the errors.

- **Check Component Dependencies**
  
  You can test for scalability bugs in your component by running a second `vxprj` command, which has the following syntax:
  ```
  vxprj component dependencies [projectFile] component [component ... ]
  
  For example, the following command displays a list of components required by `INCLUDE_OBJ_LIB`, as well as those that require the component:
  ```
  ```
  % vxprj component dependencies INCLUDE_OBJ_LIB
  ```
  
  If no project file is specified, `vxprj` looks for a `.wpj` file in the current directory.

- **Check the Component Hierarchy in the IDE**
  
  Verify that selections, folders, and new components you have added are properly included by making a visual check of the IDE component hierarchy. Look at how your new elements appear in the folder tree. Check the parameters associated with a component and their parameter default values.

⚠️ **CAUTION:** Do not alter Wind River-supplied CDFs directly. Use the naming convention to create a file whose higher precedence overrides the default properties of Wind River-supplied components.
If you have added a folder containing components, and have included that folder in your configuration, the IDE component hierarchy should display in **boldface** all components listed as defaults for that folder (that is, values for the DEFAULTS property).

- **Build and Boot the System**

  Verify that the resulting image builds and boots.

### 2.10 Custom System Calls

The VxWorks system call interface provides kernel services for applications that are executed as processes in user space. The interface can be easily extended by developers who wish to add custom system calls to the operating system to support special needs of their applications. (See *VxWorks Application Programmer’s Guide: Applications and Processes* for information about user-space applications.)

Initially, the developer’s main tasks in extending the system call interface are designing the custom system call in accordance with the naming, numbering, and argument guidelines, and then writing the system call handler to support that design. See 2.10.2 System Call Requirements, p.100 and 2.10.3 System Call Handler Requirements, p.104.

The system call interface can then be extended either statically and dynamically. Static extension involves the use of configuration files and build system facilities to create a VxWorks system image that includes the new system call functionality. Dynamic extension involves using the host or kernel shell, and kernel object module loader, to download a development version of the system call handler to the kernel. See 2.10.4 Adding System Calls, p.106 and 2.10.5 Monitoring And Debugging System Calls, p.113.

#### 2.10.1 How System Calls Work

System calls are C-callable routines. They are implemented as short pieces of assembly code called system call stubs. The stubs execute a trap instruction, which switches execution mode from user mode to kernel mode. All stubs are identical to each other except for the unique system call number that they pass to the kernel to identify the system call.
In kernel mode, a trap handler copies any system call arguments from the user stack to the kernel stack, and then calls the system call handler.

Each system call handler is given only one argument—the address of its argument array. Handler routines interpret the argument area as a structure whose members are the arguments.

System call handlers may call other routines in the kernel to service the system call request. They must validate the parameters of the system call, and return errors if necessary.

The architecture of the system call dispatcher allows system call handlers to be installed at either compile time or runtime.

2.10.2 System Call Requirements

In order to be able to generate system calls automatically, as well as to ensure proper runtime operation, system calls must adhere strictly to naming, numbering, argument, and return value rules.

System Call Naming Rules

The names of various elements associated with a system call must derive their names from that of the system call itself. It is important to adhere to this convention in order to avoid compilation errors when using the automated mechanisms provided for adding system calls. See Table 2-6.

<table>
<thead>
<tr>
<th>Table 2-6</th>
<th>System Call Naming Conventions</th>
</tr>
</thead>
<tbody>
<tr>
<td>Element</td>
<td>Name Convention</td>
</tr>
<tr>
<td>system call</td>
<td>syscallName()</td>
</tr>
<tr>
<td>system call stub</td>
<td>SYSCALL_STUB_syscallName.s</td>
</tr>
<tr>
<td>system call handler routine</td>
<td>syscallNameSc()</td>
</tr>
<tr>
<td>system call argument structure</td>
<td>syscallNameScArgs</td>
</tr>
</tbody>
</table>

The system call name is used by developer in system call definition files. The system call stub is generated automatically from the information in the definition files. The developer must write the system call handler routine, which includes the system call argument structure.
2 Kernel

2.10 Custom System Calls

For example, if the name of the system call is \texttt{foo()}, then:

- The system call stub is named \texttt{SYSCALL\_STUB\_foo.s}.
  The stub implements the routine \texttt{foo()} in user mode.

- The system call handler routine for system call \texttt{foo} must be named \texttt{fooSc()}.
  Routine \texttt{fooSc()} is called when an application makes a call to \texttt{foo()} in user space. Writing a routine with this name is the kernel developer’s responsibility. Unless \texttt{fooSc()} exists, an error will be generated when the kernel is rebuilt.

- If the \texttt{foo} system call takes at least one argument, the argument structure for \texttt{foo} must be declared as \texttt{struct fooScArgs} in the system call handler.

For information about system call handler requirements, see 2.10.3 System Call Handler Requirements, p.104. For information about adding system calls to the operating system—both statically and dynamically—see 2.10.4 Adding System Calls, p.106.

System Call Numbering Rules

Each system call must have a unique system call number. The system call number is passed by the system call stub to the kernel, which then uses it to identify and execute the appropriate system call handler.

A system call number is a concatenation of two numbers:

- the system call group number
- the routine number within the system call group

Both numbers are implemented with 8-bit fields. This allows for up to 256 system call groups, each with up to 256 routines in it. The total system call number space can therefore accommodate 65,536 system calls.

Six system call groups (numbers 2 through 7) are reserved for customer use. (Customers may request a formal system call group allocation from Wind River.) All other system call groups are reserved for Wind River use.

Wind River system call group numbers and system call routine numbers are defined in the \texttt{syscallNum.def} file. It should not be modified by customers.

Customer system calls group numbers and system call routine numbers are defined in the \texttt{syscallUsrNum.def} file.
The Wind River system call number definition file, and a template for the customer system call definition file are located in `installDir/vxworks-6.0/target/share/h`.

A given system call group is simply a collection of related system calls offering complementary functionality. For example, the VxWorks SCG_STANDARD group includes system calls that are commonly found in UNIX-like (POSIX) systems, and the SCG_VXWORKS group includes system calls that are unique to VxWorks or that are otherwise dissimilar to UNIX-like system calls.

For information about using the system call definition files to generate system calls, see Adding System Calls Statically, p.106.

**System Call Argument Rules**

System calls may only take up to eight arguments. Special consideration must be given to 64-bit arguments on 32-bit systems. Floating point and vector-type arguments are not permitted.

Wind River system calls are defined in the `syscallApi.def` file. It should not be modified by customers.

Customer system calls are defined in the `syscallUsrApi.def` file. See Adding System Calls Statically, p.106 for information about editing this file.

**Number of Arguments**

System calls can take up to a maximum of eight arguments (the maximum that the trap handler can accommodate). Each argument is expected to be one native-word in size. The size of a native-word is 32 bits for a 32-bit architecture and 64 bits for 64-bit architectures. For the great majority of system calls (which use 32 bits), therefore, the number of words in the argument list is equal to the number of parameters the routine takes.

In cases where more than eight arguments are required the arguments should be packed into a structure whose address is the parameter to the system call.

**64-Bit Argument Issues**

64-bit arguments are permitted, but they may only be of the type `long long`. For 32-bit architectures, a 64-bit argument takes up two native-words on the argument list, although it is still only one parameter to the routine.

There are other complications associated with 64-bit arguments to routines. Some architectures require 64-bit arguments to be aligned to either even or odd numbered registers, while some architectures have no restrictions.
It is important for system call developers to take into account the subtleties of 64-bit argument passing on 32-bit systems. The definition of a system call for VxWorks requires identification of how many words are in the argument list, so that the trap handler can transfer the right amount of data from the user-stack to the kernel-stack. Alignment issues may make this less than straightforward.

Consider for example, the following routine prototypes:

\[
\begin{align*}
\text{int } \text{foo} \; (\text{int } a, \text{int } b, \text{long long } c, \text{int } d); \\
\text{int } \text{bar} \; (\text{int } p, \text{long long } q, \text{int } r);
\end{align*}
\]

The ARM and Intel x86 architectures have no alignment constraints for 64-bit arguments, so the size of the argument list for \text{foo( )} would be five words, while the size of the argument for \text{bar( )} would be four words.

PowerPC requires \textit{long long} arguments to be aligned on 8-byte boundaries. Parameter \textit{c} to routine \text{foo( )} is already at an 8-byte offset with respect to the start of the argument list and is hence aligned. So for PowerPC, the argument list size for \text{foo( )} is five words.

However, in the case of \text{bar( )} the \textit{long long} argument \textit{q} is at offset four from the first argument, and is therefore not aligned. When passing arguments to \text{bar( )}, the compiler will skip one argument register and place \textit{q} at offset eight so that it is aligned. This alignment pad is ignored by the called routine, though it still occupies space in the argument list. Hence for PowerPC, the argument list for \text{bar( )} is five words long.

Consult the architecture ABI documentation for more information. There are only a few routines that take 64-bit arguments.

**System Call Return Value Rules**

System calls may return only a native word as a return value (that is, integer values or pointers, etc.).

64-bit return values are not permitted directly, though they may be emulated by using private routines. To do so, a system call must have a name prefixed by an underscore, and it must a pointer to the return value as one of the parameters. For example the routine:

\[
\begin{align*}
\text{long long } \text{get64BitValue} \; (\text{void})
\end{align*}
\]

must have a companion routine:

\[
\begin{align*}
\text{void } _{\text{get64BitValue}} \; (\text{long long } *\text{pReturnValue})
\end{align*}
\]
Routine \_get64BitValue() is the actual system call that should be defined in the syscallUsrNum.def and syscallUsrApi.def files. The routine get64BitValue() can then be written as follows:

```c
long long get64BitValue (void)
{
    long long value;
    _get64BitValue (&value);
    return value;
}
```

(The get64BitValue() routine would be written by the user and placed in a user mode library, and the \_get64BitValue() routine would be generated automatically; see 2.10.4 Adding System Calls, p.106.)

The value -1 (ERROR) is the only permitted error return value from a system call. No system call should treat -1 as a valid return value. When a return value of -1 is generated, the operating system transfers the \errno value correctly across the trap boundary so that the user-mode code has access to it.

If NULL needs to be the error return value, then the system call itself must be implemented by another routine that returns -1 as an error return. The -1 value from the system call can then be translated to NULL by another routine in user mode.

### 2.10.3 System Call Handler Requirements

System call handlers must adhere to naming conventions, and to organizational requirements for the system call argument structure. They should validate arguments. If an error is encountered, they set \errno and return ERROR.

A system call handler typically calls one or more kernel routines that provide the functionality required. In some cases, the code will call the public kernel API directly; in other cases, it may do otherwise to skip the kernel level validation, and call the underlying functionality directly.

**System Call Handler Naming Rules**

System call handlers must be named in accordance with the system call naming conventions, which means that they must use the same name as the system call, but with an \Sc appended. For example, the foo() system call must be serviced by the fooSc() system call handler.
All system call handlers take a single parameter, which is a pointer to their argument structure. The argument structure must also be named in accordance with the system call naming conventions, which means that they must use the same name as the system call handler, but with Args appended. For example, the argument structure for fooSc must be declared as struct fooScArgs.

For example, the write() system call is declared as:

```c
int write (int fd, char * buf, int nbytes)
```

The system call handler routine for write is therefore named writeSc(), and it is declared as:

```c
int writeSc (struct writeScArgs * pArgs)
```

And the argument structure is writeScArgs, which is declared as:

```c
struct writeScArgs
{
  int    fd;
  char * buf;
  int    nbytes;
};
```

See System Call Naming Rules, p.100.

**System Call Handler Argument Validation**

A system call handler should validate all arguments. In particular, it should:

- Bounds-check numerical values.
- Validate any memory addresses to ensure they are accessible within the current memory context (that is memory within the process, and not within the kernel).

See the scMemValidate() API reference entry for information on pointer validation across system calls.

**System Call Handler Error Reporting**

At the end of the system call, in the case of failure, the system call handler should ensure errno is set appropriately, and then return -1 (ERROR). If the return value is -1 (ERROR) the kernel errno value is then copied into the calling process’ errno. If there is no error, simply return a value that will be copied to user mode. If the handlers set their errno before returning ERROR, user mode code sees the same errno value.
2.10.4 Adding System Calls

System calls can be added both statically and dynamically. This means that they can be either configured and built into the VxWorks operating system image, or they can be added interactively to the operating system while it is running on a target.

Dynamic addition is useful for rapid prototyping and debugging of system calls. Static configuration is useful for more stable development efforts, and production systems.

Adding System Calls Statically

The process of adding system calls statically is based on the use of the syscallUsrNum.def and syscallUsrApi.def system call definition files.

The files define the system call names and numbers, their prototypes, the system call groups to which they belong, and (optionally) the components with which they should be associated. The scgen utility program uses these files—along with comparable files for standard VxWorks system calls—to generate the system call apparatus required to work with the system call handler written by the developer. The scgen program is integrated into the build system, and is run automatically when the build system detects that changes have been to syscallUsrNum.def and syscallUsrApi.def.

The template files syscallUsrNum.def.template and syscallUsrApi.def.template are in installDir/vxworks-6.x/target/share/h. Make copies of files in the same directory without the .template extension, and create the appropriate entries in them, as described below.

After you have written a system handler, the basic steps required to add a new system call to VxWorks are:

1. If you are creating a new system call group, add an entry for the group to syscallUsrNum.def. See Defining a New System Call Group, p.108. Remember that only groups 2 through 7 are available to developers. (Contact Wind River if you need to have a group formally added to VxWorks.)

2. Add an entry to syscallUsrNum.def to assign the system call to a system call group, and to associate the system call name with a system call number. See Defining a New System Call, p.108.

3. Add an entry to syscallUsrApi.def to define the system call name and its arguments. See Defining a New System Call, p.108.
4. Write the system call handler routine. See 2.10.3 System Call Handler Requirements, p.104.

5. Rebuild both kernel and user mode trees to ensure that all derived file are generated. That is, rebuild the kernel with the system call handler linked into it, as well as the process-based application. See 2.7.7 Linking Kernel-Based Application Object Modules with VxWorks, p.65, 2.8 Custom Kernel Libraries, p.68, and the VxWorks Application Programmer's Guide: Applications and Processes.

The make utility automatically detects changes in syscallUsrNum.def and syscallUsrApi.def, and invokes the scgen utility.

What scgen Does

Using the system call definitions in the both Wind River and the customer system call definition files scgen generates the following:

1. The files installDir/vxworks-6.x/target/h/syscall.h and installDir/vxworks-6.x/target/usr/h/syscall.h. The contents of both files are identical. They define all system call numbers and group numbers in the system. These files provide information shared between kernel and user space code.

2. One system call assembly stub file for each system call. The stubs are placed into the appropriate architecture directory under installDir/vxworks-6.x/target/usr/src/arch for compilation into libvx.a or libc.so.

3. A file containing argument structures for all system calls in the system. This file is architecture/ABI specific, and is used by the system call handlers located in the kernel. This file is named syscallArgsArchAbi.h under installDir/vxworks-6.x/target/h/arch/archName (for example, installDir/vxworks-6.x/target/h/arch/ppc/syscallArgsppc.h).

4. A file containing a pre-initialized system call group table for all system call groups known at compile-time. This file is installDir/vxworks-6.x/target/h/syscallTbl.h.

All of this output is then used by the build system automatically; no user intervention is required to build the appropriated system call infrastructure into the system.

The scgen utility can also be run from the command line for debugging purposes.
Defining a New System Call Group

If you need to define a new system call group, add it to syscallUsrNum.def using the following syntax:

```
SYSCALL_GROUP       SCG_sgcGroupName   groupNum   componentNames
```

Six system call groups (numbers 2 through 7) are reserved for customer use. All other system call groups are reserved for Wind River use. (See System Call Numbering Rules, p.101.) Group names must be unique.

Identification of component names is optional, and provides the means of associating a system call group (all its calls) with specific operating system components for inclusion in a VxWorks configuration. It works as follows:

- If a component name is not defined, the system call group is always included in the system.
- If a component is defined, the system call group will either be included in the system or left out of it—depending on the presence or absence of the component. That is, if the component is included in a VxWorks configuration by the user, then the system call group is included automatically. But if the component is not included in the configuration, the group is likewise not included.

The fields must be separated by one or more space characters.

For example, a new group called SCG_MY_NEW_GROUP could be defined with the following entry:

```
SYSCALL_GROUP       SCG_MY_NEW_GROUP   5 INCLUDE_FOO
```

The system calls that are part of the system call group are identified below the SYSCALL_GROUP definition line. Up to 256 system calls can be identified within each group. See Defining a New System Call, p.108.

Defining a New System Call

To define a new system call, you need to create entries in two different files:

- One entry in syscallUsrNum.def, which assigns it to a system call group and associates the system call name and number.
- One entry in syscallUsrApi.def, which defines the system call name and its arguments.
System Call Definition Syntax

To add a system call to a call group, add an entry to `syscallUsrApi.def` under the appropriate system call group name, using the following syntax:

```
sysCallNum sysCallName
```

Note that it is important to add system calls to the end of a system call group; do not use numbers that have already been assigned. Reusing an existing number will break binary compatibility with existing binaries; and all existing applications will need to be recompiled. System call numbers need not be strictly sequential (that is there can be gaps in the series for future use).

To define a system call itself, add an entry to `syscallUsrApi.def` using the following syntax:

```
sysCallName numArgs [ argType arg1; argType arg2; argType argN; ] \nCompName INCLUDE headerFileName.h
```

System call definition lines can be split over multiple lines by using the backslash character as a connector.

The name of the system call used in `syscallUsrApi.def` must match the name used in `syscallUsrNum.def`.

When defining the number of arguments, take into consideration any 64-bit arguments and adjust the number accordingly (for issues related to 64-bit arguments, see System Call Argument Rules, p.102).

The arguments to the system call are described in the bracket-enclosed list. The opening bracket must be followed by a space; and the closing bracket preceded by one. Each argument must be followed by a semicolon and then at least one space. If the system call does not take any arguments, nothing should be listed—not even the bracket pair.

More than one component name can be listed. If any of the components is included in the operating system configuration, the system call will be included when the system is built. (For information about custom components, see 2.9 Custom Kernel Components, p.69.)
The following mistakes are commonly made when editing `syscallUsrApi.def` and `syscallUsrNum.def`, and can confuse the `scgen` utility:

- No space after the opening bracket of an argument list.
- No space before the closing bracket of an argument list.
- No backslash at the end of a line (if the argument list continues onto the next line).
- An empty pair of brackets that encloses no arguments at all. This will cause the generated temporary C file to have a compile error.

If the system call includes the definition of a new type in a header file, the header file must be identified with the INCLUDE statement. The `scgen` utility needs to resolve all types before generating the argument structures, and this is the mechanism by which it is informed of custom definitions.

For examples of how this syntax is used, see *System Call Definition Example*, p.110. Also consult the Wind River system call definitions files (`syscallNum.def` and `syscallApi.def`), but do not modify these files.

**System Call Definition Example**

Assume that we want to add the custom system call `myNewSyscall()` to a new system call group `SCG_USGR0` (which is defined in `syscallNum.def`).

First, create `syscallUsrNum.def` file by copying `syscallUsrNum.def.template`. Then edit the file `syscallUsrNum.def`, adding a system call group entry for the appropriate group, and the system call number and name under it. System call groups 2 through 7 are reserved for customer use.

For example:

```
SYSCALL_GROUP SCG_USGR0 2
1 myNewSyscall
```

Then we need to edit `syscallUsrApi.def` to define the system call itself.

The C prototype for `myNewSyscall()` is:

```
int myNewSyscall (MY_NEW_TYPE a, int b, char *c);
```

The call has three arguments, and a type defined in a custom header file. Assume that we also want to implement the system call conditionally, depending on whether or not the component `INCLUDE_FOO` is configured into the operating system.

The entry in `syscallUsrApi.def` would therefore look like this:

```
"INCLUDE <myNewType.h>"
```
Adding System Calls Dynamically

You can dynamically extend the system call interface on a target by downloading a kernel object module that includes code for installing system call handlers as well as the system call handler routines themselves. You do not need to modify the system call definition files, to run `scgen`, or to rebuild the kernel.

This approach is useful for rapid prototyping. It would rarely be useful or advisable with a deployed system.

System Call Installation Code

The code required to install your system call handlers in the kernel consists of:

- an initialized table for the system call handler routines
- a call to a system call registration routine

This code should be included in the same module with the system call handlers. You need to identify a system call group for the system calls, and it should be a group that is otherwise unused in the target system.

Routine Table

The system call handler routine table is used to register the system call handler routines with the system call infrastructure when the module is downloaded.

For example, if the system handler routines are `testFunc0()`, `testFunc1()`, `testFunc2()`, and `testFunc3()`, the table should be declared as follows:

```c
_WRS_DATA_ALIGN_BYTES(16) SYSCALL_RTN_TBL_ENTRY testScRtnTbl [] =
{
    { (FUNCPTR) testFunc0, 1, "testFunc0", 0 }, /* routine 0 */
    { (FUNCPTR) testFunc1, 2, "testFunc0", 1 }, /* routine 1 */
    { (FUNCPTR) testFunc2, 3, "testFunc0", 2 }, /* routine 2 */
    { (FUNCPTR) testFunc3, 4, "testFunc0", 3 }  /* routine 3 */
}
```

The `_WRS_DATA_ALIGN_BYTES(16)` directive instructs the compiler/linker to align the table on a 16-byte boundary. This directive is optional, but is likely to improve performance as it increases the chance of locating the table data on a cache line boundary.
Building the Object Module

Build the object module containing the stem call handlers and registration code as you would any module. See 2.7.5 Building Kernel-Based Application Modules, p.63.

Downloading the Module and Registering the System Calls

After you have built the module, download it, register it, and check that registration has been successful:

1. Download it to the target system with the debugger, host shell, or kernel shell. From the shell (using the C interpreter) the module foo.o could be loaded as follows:

   `ld < foo.o`

2. Register the new handlers with the system call infrastructure before any system calls are routed to your new handlers. This is done by calling `syscallGroupRegister()`.

   `syscallGroupRegister (2, "testGroup", 4, &testScRtnTbl, 0)`

   The first argument is a variable holding the group number (an integer); the second is the group name; the third is the number of system handler routines, as defined in the table; the fourth is the name of the table; and the last is set to that the registration does not forcibly overwrite an existing entry. (Note that you use the ampersand address operator with the third argument when you execute the call from the shell—which you would not do when executing it from a program.)

   It is important to check the return value from `syscallGroupRegister()` and print an error message if an error was returned. See the API reference for `syscallGroupRegister()` for more information.

3. Verify that the group is registered by running `syscallShow()` from the shell (host or kernel).

   The system call infrastructure is now ready to route system calls to the newly installed handlers.

Making System Calls from a Process

The quickest method of testing a new system call is to create and run a simple process-based application.

First, calculate the system call numbers for your newly introduced system calls. Each system call number is a concatenation of the group and routine numbers (both implemented as 8-bit fields).
For example, if you used group number 5 for your test group, then the system call number for `testFunc0()` is 0x500 and that for `testFunc1()` is 0x501, and so on.

To make the actual system calls, the application calls the `syscall()` routine. The first eight arguments (all integers) are the arguments passed to your system call, and the ninth argument is the system call number.

For example, to have your user-mode applications to call `testFunc0()` from process, you should implement `testFunc0()` like this:

```c
int testFunc0
{
    int arg1,
    int arg2,
    int arg3,
    int arg4,
    int arg5
}
{
    return syscall (arg1, arg2, arg3, arg4, arg5, 0, 0, 0, 0x500);
}
```

Note that you must use nine arguments with `syscall()`. The last argument is the system call number, and the preceding eight are for the system call arguments. If your routine takes less than eight arguments, you must use zeros as placeholders for the remainder.

### 2.10.5 Monitoring And Debugging System Calls

This section discusses using show routines, `syscallmonitor()`, and hooks for obtaining information about, and debugging, system calls.

If show routines are included in your VxWorks configuration (with the component `INCLUDE_SHOW_ROUTINES`), the set of system calls currently available can be displayed with the `syscallShow()` shell command with the shell’s C interpreter:

```sh
$ syscallShow
Group Name  GroupNo  NumRtms  Rtn  Tbl  Addr
----------  -------  -------  ------  -----  -----
TEMPGroup   7        6        0x001dea50
STANDARDGroup 8       48       0x001deab0
VXWORKSGroup 9       31       0x001dedb0
value = 55 = 0x37 = '7'
```
## System Call Monitor

The `syscallMonitor()` routine allows truss style monitoring of system calls from kernel mode, on a global, or per-process basis. It lists (on the console) every system call made, and their arguments. The routine synopsis is:

```c
syscallMonitor(level, RTP_ID)
```

If the `level` argument is set to 1, the system call monitor is turned on; if it is set to 0, it is turned off. If the `RTP_ID` is set to an RTP_ID, it will monitor only the system calls made from that process; if it is set to 0, it will monitor all system calls.

The `sysCallHookLib` library provides routines for adding extensions to the VxWorks system call library with hook routines. Hook routines can be added without modifying kernel code. The kernel provides call-outs whenever system calls are made.

### System Call Group

- **Group Name:** VXWORKSGroup
- **Group Number:** 9

#### Routines Provided

<table>
<thead>
<tr>
<th>Rtn#</th>
<th>Name</th>
<th>Address</th>
<th># Arguments</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>(null)</td>
<td>0x00000000</td>
<td>0</td>
</tr>
<tr>
<td>1</td>
<td>(null)</td>
<td>0x00000000</td>
<td>0</td>
</tr>
<tr>
<td>2</td>
<td>(null)</td>
<td>0x00000000</td>
<td>0</td>
</tr>
<tr>
<td>3</td>
<td>msgQSend</td>
<td>0x001d9464</td>
<td>5</td>
</tr>
<tr>
<td>4</td>
<td>msgQReceive</td>
<td>0x001d94ec</td>
<td>4</td>
</tr>
<tr>
<td>5</td>
<td>_msgQOpen</td>
<td>0x001d9540</td>
<td>5</td>
</tr>
<tr>
<td>6</td>
<td>objDelete</td>
<td>0x001d95b8</td>
<td>2</td>
</tr>
<tr>
<td>7</td>
<td>objInfoGet</td>
<td>0x001d9bf8</td>
<td>4</td>
</tr>
<tr>
<td>8</td>
<td>_semTake</td>
<td>0x001d9684</td>
<td>2</td>
</tr>
<tr>
<td>9</td>
<td>_semGive</td>
<td>0x001d96d0</td>
<td>1</td>
</tr>
<tr>
<td>10</td>
<td>_semOpen</td>
<td>0x001d970c</td>
<td>5</td>
</tr>
<tr>
<td>11</td>
<td>semCtl</td>
<td>0x001d9768</td>
<td>4</td>
</tr>
<tr>
<td>12</td>
<td>_taskOpen</td>
<td>0x001d98b8</td>
<td>1</td>
</tr>
<tr>
<td>13</td>
<td>taskCtl</td>
<td>0x001d99dc</td>
<td>4</td>
</tr>
<tr>
<td>14</td>
<td>taskDelay</td>
<td>0x001d99d4</td>
<td>1</td>
</tr>
<tr>
<td>15</td>
<td>rtpSpawn</td>
<td>0x001a2e14</td>
<td>6</td>
</tr>
<tr>
<td>16</td>
<td>rtpInfoGet</td>
<td>0x001a2e60</td>
<td>2</td>
</tr>
<tr>
<td>17</td>
<td>taskKill</td>
<td>0x001a2ec8</td>
<td>2</td>
</tr>
<tr>
<td>18</td>
<td>taskSigqueue</td>
<td>0x001a2f00</td>
<td>3</td>
</tr>
<tr>
<td>19</td>
<td>_timer_open</td>
<td>0x0018a860</td>
<td>4</td>
</tr>
<tr>
<td>20</td>
<td>timerCtl</td>
<td>0x0018a8c0</td>
<td>4</td>
</tr>
<tr>
<td>21</td>
<td>pxOpen</td>
<td>0x0018a960</td>
<td>4</td>
</tr>
<tr>
<td>22</td>
<td>pxClose</td>
<td>0x0018acf4</td>
<td>1</td>
</tr>
<tr>
<td>23</td>
<td>pxUnlink</td>
<td>0x0018ae44</td>
<td>2</td>
</tr>
<tr>
<td>24</td>
<td>pxCtl</td>
<td>0x0018b334</td>
<td>4</td>
</tr>
<tr>
<td>25</td>
<td>pxMsgReceive</td>
<td>0x0018aea0</td>
<td>6</td>
</tr>
<tr>
<td>26</td>
<td>pxMsgSend</td>
<td>0x0018afcc</td>
<td>6</td>
</tr>
<tr>
<td>27</td>
<td>pxSemWait</td>
<td>0x0018b1fc</td>
<td>3</td>
</tr>
<tr>
<td>28</td>
<td>pxSemPost</td>
<td>0x0018b0f8</td>
<td>1</td>
</tr>
<tr>
<td>29</td>
<td>pipeDevCreate</td>
<td>0x001971a8</td>
<td>3</td>
</tr>
<tr>
<td>30</td>
<td>pipeDevDelete</td>
<td>0x001971c4</td>
<td>2</td>
</tr>
</tbody>
</table>

Value = 50 = 0x32 = '2'
call groups are registered, and on entry and exit from system calls. Each hook type is represented as an array of function pointers. For each hook type, hook functions are called in the order they were added. For more information, see the syscallHookLib API reference.

2.10.6 Documenting Custom System Calls

Since system calls are not functions written in C, the apigen documentation generation utility cannot be used to generate API references from source code comments. You can, however, create a function header in a C file that can be read by apigen. The function header for system calls is no different from that for other C functions.

Here is a function header for getpid():

```c
/*****************************/
* * getpid - Get the process identifier for the calling process.
* * SYNOPSIS
* \cs
* int getpid
* ( * void
* )
* \ce
* *
* DESCRIPTION
* *
* This routine gets the process identifier for the calling process.
* The ID is guaranteed to be unique and is useful for constructing
* uniquely named entities such as temporary files etc.
* *
* RETURNS: Process identifier for the calling process.
* *
* ERROR: N/A.
* *
* SEE ALSO:
* .pG "Multitasking"
* /
```

No code or C declaration should follow the header. The compiler treats it as a comment block, but apigen uses it to generate API documentation. All fields in the header above (SYNOPSIS, DESCRIPTION, RETURNS, and so on) must to be present in the code.

You have two choices for the location of the comments:
You may add system call function headers to an existing C source file (one that has code for other functions). Be sure that this source file is part of the DOC_FILES list in the makefile for that directory. The apigen utility will not process it otherwise.

You may create a C file that contains only function headers and no C code. Such files must be part of the DOC_FILES list in the makefile, but not part of the OBJS list (because there is no code to compile).

For more information about the coding conventions that are required for API documentation generation, and the apigen tool, see the VxWorks BSP Developer’s Guide and the apigen entry in the Wind River Host Utilities API reference.

2.11 Kernel Schedulers

VxWorks provides the following kernel scheduler facilities:

- The VxWorks native scheduler, which provides options for preemptive priority-based scheduling or round-robin scheduling.
- A POSIX thread scheduler that provides POSIX thread scheduling support in user-space (processes) while keeping the VxWorks task scheduling.
- A kernel scheduler replacement framework that allows users to implement customized schedulers.

Only one such scheduler may be configured into a VxWorks image at any one time.

2.11.1 VxWorks Native Scheduler

The VxWorks native scheduler is the default scheduler. This scheduler is provided with the INCLUDE_VX_NATIVE_SCHEDULER component. This scheduler can be configured with either preemptive priority based scheduling or round robin scheduling, which is enabled with the kernelTimeSlice() routine. 3.2.2 Task Scheduling, p.134 for more information about task scheduling.
Kernel Scheduler Initialization

The kernel scheduler description structure is initialized in the `usrKernelInit()` routine. The following is an example for configuring the VxWorks native scheduler:

```c
#ifdef INCLUDE_VX_NATIVE_SCHEDULER
    /* install the native priority based preemptive scheduler */
#endif /* INCLUDE_VX_NATIVE_SCHEDULER */

#ifndef VX_NATIVE_SCHED_CONSTANT_RDY_Q
   vxKernelSchedDesc.readyQClassId = Q_PRI_BMAP;
   vxKernelSchedDesc.readyQInitArg1 = (void *) &readyQBMap;
   vxKernelSchedDesc.readyQInitArg2 = (void *) 256;
#else
   vxKernelSchedDesc.readyQClassId = Q_PRI_LIST;
#endif /* VX_NATIVE_SCHED_CONSTANT_RDY_Q == TRUE */

#endif /* INCLUDE_VX_NATIVE_SCHEDULER */
```

This code snippet of code is in `installDir/vxworks-6.x/target/config/comps/src/usrKernel.c`.

The `vxKernelSchedDesc` variable is the kernel scheduler description structure, which is defined in `installDir/vxworks-6.x/target/h/kernelLib.h` as follows:

```c
typedef struct wind_sched_desc
{
    Q_CLASS_ID readyQClassId; /* readyQ Id */
    void * readyQInitArg1; /* readyQ init arg 1 */
    void * readyQInitArg2; /* readyQ init arg 2 */
} WIND_SCHED_DESC;
```

The `readyQClassId` is a pointer to a ready queue class. The ready queue class is a structure with a set of pointers to functions that manage the tasks that are in the READY state. And the `readyQInitArg1` and `readyQInitArg2` are the input arguments for the `initRtn()` routine of the ready queue class.

`Q_PRI_BMAP` is the priority-based bit-mapped ready queue class ID, and it is used for the VxWorks native scheduler. See Kernel Scheduler Multi-way Queue Structure, p.118 for more information about the ready queue class and its associated members.

The `vxKernelSchedDesc` can be initialized with the user-specified ready queue class for customized kernel schedulers. See 2.11.3 Replacement Kernel Scheduler Framework, p.124 for more information how to install thirty-party kernel schedulers.
After the initialization of `vxKernelSchedDesc` variable, VxWorks invokes the `qInit()` function to initialize the ready queue class, as follows:

```c
/* kernel scheduler ready queue init */

qInit (&readyQHead, vxKernelSchedDesc.readyQClassId,
        (int)(vxKernelSchedDesc.readyQInitArg1),
        (int)(vxKernelSchedDesc.readyQInitArg2));
```

The `qInit()` function invokes `vxKernelSchedDesc.readyQClassId->initRtn()` to set up the ready queue and the `readyQHead` variable, which is of type `Q_HEAD`. It is described below in Kernel Scheduler Multi-way Queue Structure, p.118.

**Kernel Scheduler Multi-way Queue Structure**

The VxWorks scheduler data structure consists of `Q_HEAD`, `Q_NODE`, and `Q_CLASS`. The type definitions of `Q_HEAD` and `Q_NODE` structures are flexible so that they can be used for different types of ready queues.

The `readyQHead` variable is the head of a so-called *multi-way queue*, and the aforementioned `Q_PRI_BMAP` queue classes comply with the multi-way queue data structures. The multi-way queue *head* structure (`Q_HEAD`) is defined in `qLib.h` as follows:

```c
typedef struct          /* Q_HEAD */
{                         /* first node in queue based on key */
    Q_NODE  *pFirstNode; /* use is queue type dependent */
    UINT     qPriv1;     /* use is queue type dependent */
    UINT     qPriv2;     /* use is queue type dependent */
    Q_CLASS  *pQClass;   /* pointer to queue class */
} Q_HEAD;
```

The first field in the `Q_HEAD` must contain the highest priority node. The `qFirst()` routine and `Q_FIRST()` macro makes this assumption; that is, a queue-class specific routine is not invoked to determine which node is the head of the queue. Instead, the first four bytes of the `Q_HEAD` structure (the `pFirstNode` field) are simply read to determine the head of the queue. The kernel scheduler performs a `Q_FIRST()` on `readyQHead` to determine which task should be allocated to the CPU. For the `Q_PRI_BMAP` and `Q_PRI_LIST` queue classes, this represents the highest priority ready task.

The multi-way queue node structure (`Q_NODE`) is also defined in `qLib.h` as follows:

```c
typedef struct          /* Q_NODE */
{                         /* Q_NODE */
    /* Q_NODE */
```
Each task control block contains a `Q_NODE` structure for use by a multi-way queue class to manage the set of ready tasks. This same `Q_NODE` is used to manage a task when it is in a pend queue.

Note that an implementation of a multi-way queue class may choose to define class-specific `Q_HEAD` and `Q_NODE` structures. Clearly the size of the class specific structures must not exceed 16 bytes, which is the current size of both the `Q_HEAD` and `Q_NODE` structures. For example, the `Q_PRI_BMAP` queue class defines and uses a `Q_PRI_BMAP_HEAD` instead of `Q_HEAD`, and uses `Q_PRI_NODE` (defined by the `Q_PRI_LIST` queue class) instead of `Q_NODE`.

### Queue Class (Q_CLASS) Structure

The kernel interacts with a multi-way queue class through a `Q_CLASS` structure. A `Q_CLASS` structure contains function pointers to the class-specific operators; that is the address of the class specific `put` routine is stored in the `putRtn` field.

As described in *Kernel Scheduler Initialization*, p.117, the `qInit()` routine is used to initialize a multi-way queue head to a specified queue type. The second parameter specifies the `queue class` (that is, the type of queue), and is merely a pointer to a `Q_CLASS` structure. All kernel invocations of the queue class operators are performed indirectly through the `Q_CLASS` structure.

The `Q_CLASS` structure is defined in `qClass.h` as follows:

```c
typedef struct q_class              /* Q_CLASS */
{
    FUNCPTR createRtn;          /* create and initialize a queue */
    FUNCPTR initRtn;            /* initialize a queue */
    FUNCPTR deleteRtn;          /* delete and terminate a queue */
    FUNCPTR terminateRtn;       /* terminate a queue */
    FUNCPTR putRtn;             /* insert a node into q with insertion key */
    FUNCPTR getRtn;             /* return and remove lead node routine */
    FUNCPTR removeRtn;          /* remove routine */
    FUNCPTR resortRtn;          /* resort node to new priority */
    FUNCPTR advanceRtn;         /* advance queue by one tick routine */
    FUNCPTR getExpiredRtn;      /* return and remove an expired Q_NODE */
    FUNCPTR keyRtn;             /* return insertion key of node */
    FUNCPTR calibrateRtn;       /* calibrate every node in queue by an offset */
    FUNCPTR infoRtn;            /* return array of nodes in queue */
    FUNCPTR eachRtn;            /* call a user routine for each node in queue */
} Q_CLASS;
```

```c
UINT     qPriv1;                    /* use is queue type dependent */
UINT     qPriv2;                    /* use is queue type dependent */
UINT     qPriv3;                    /* use is queue type dependent */
UINT     qPriv4;                    /* use is queue type dependent */
} Q_NODE;
```
The following operators are not applicable to a queue class that is used to manage the set of ready tasks: `advanceRtn`, `getExpiredRtn`, and `calibrateRtn`.

**NOTE:** A `firstRtn` operator to return (but not de-queue) the first node in the queue, does not exist. The `qFirst()` routine and the `Q_FIRST()` macro make the assumption that the `pFirstNode` field of the `Q_HEAD` structure contains the first node in the queue. A queue class specific routine is not invoked to determine which node is the head of the queue.

The following are the expected signatures of the `Q_CLASS` operators:

```
Q_HEAD * createRtn (... /* optional arguments */);
STATUS initRtn (Q_HEAD *pQHead, ... /* optional arguments */);
STATUS deleteRtn (Q_HEAD *pQHead);
STATUS terminateRtn (Q_HEAD *pQHead);
void putRtn (Q_HEAD *pQHead, Q_NODE *pQNode, ULONG key);
Q_NODE * getRtn (Q_HEAD *pQHead);
STATUS removeRtn (Q_HEAD *pQHead, Q_NODE *pQNode);
void resortRtn (Q_HEAD *pQHead, Q_NODE *pQNode, ULONG newKey);
ULONG keyRtn (Q_HEAD *pQHead, Q_NODE *pQNode, int keyType);
int infoRtn (Q_HEAD *pQHead, Q_NODE *nodeArray [], int maxNodes);
Q_NODE * eachRtn (Q_HEAD *pQHead, FUNCPtr routine, int routineArg);
```

As noted above, an implementation may choose to define class specific `Q_HEAD` and `Q_NODE` structures.

The remainder of this section provides descriptions of each `Q_CLASS` operator that pertains to the management of ready tasks. Descriptions of the `advanceRtn`, `getExpiredRtn`, and `calibrateRtn` operators are not provided as they are not applicable to managing the set of ready tasks. Each description provides details about when the kernel invokes the operator with regard to management of ready tasks.

Some `Q_CLASS` operators are invoked within the *kernel context*. The operator description indicate whether the operator is invoked within kernel context or not. The operators that are invoked within kernel context do not have access to all VxWorks facilities. *Table 2-7* lists the routines that are available from within kernel context.

**WARNING:** The utilization of VxWorks APIs not listed *Table 2-7* from an operator that is invoked from kernel context results in unpredictable behavior. Typically the target will hang or reboot.
2 Kernel

2.11 Kernel Schedulers

Table 2-7 Kernel Context Routines

<table>
<thead>
<tr>
<th>VxWorks Library</th>
<th>Available Routines</th>
</tr>
</thead>
<tbody>
<tr>
<td>blib</td>
<td>All routines</td>
</tr>
<tr>
<td>fppArchLib</td>
<td>fppSave() and fppRestore()</td>
</tr>
<tr>
<td>intLib</td>
<td>intContext(), intCount(), intVecSet(), intVecGet()</td>
</tr>
<tr>
<td>lstLib, dllLib, sllLib</td>
<td>All routines except xxxCreate() and xxxDelete()</td>
</tr>
<tr>
<td>mathALib</td>
<td>All routines, if fppSave() and fppRestore() are used</td>
</tr>
<tr>
<td>rngLib</td>
<td>All routines except rngCreate()</td>
</tr>
<tr>
<td>taskLib</td>
<td>taskIdVerify(), taskIdDefault(),</td>
</tr>
<tr>
<td></td>
<td>taskIsReady(), taskIsSuspended(), and taskTcb()</td>
</tr>
<tr>
<td>vxLib</td>
<td>vxTas()</td>
</tr>
</tbody>
</table>

Q_CLASS createRtn Operator

This operator allocates a multi-way queue head structure from the system memory pool. The dynamically-allocated head structure is subsequently initialized. Currently the kernel does not utilize this operator. Instead, the ready task queue is initialized by statically allocating the head structure, and using the initRtn operator.

Q_CLASS initRtn Operator

This operator initializes a multi-way queue head. Up to ten optional arguments can be passed to the initRtn. The kernel initializes the ready task queue from the usrKernelInit() routine as described in Kernel Scheduler Initialization, p.117.

This operator is not called from within kernel context.

Q_CLASS deleteRtn Operator

This operator deallocates (frees) the multi-way queue head. All queued nodes are lost.

Currently the kernel does not utilize this operator.

Q_CLASS terminateRtn Operator

This operator terminates a multi-way queue head. All queued nodes will be lost.
Currently the kernel does not utilize this operator.

**Q_CLASS putRtn Operator**

This operator inserts a node into a multi-way queue. The insertion is based on the key and the underlying queue class. The second parameter is the `Q_NODE` structure pointer of the task to be inserted into the queue. Recall that each task control block contains a `Q_NODE` structure for use by a multi-way queue class to manage the set of ready tasks.

The third parameter, (the key), is the task's current priority. Note that a task’s current priority may be different than a task’s normal priority due to the mutex semaphore priority inheritance protocol.

The `pFirstNode` field of the `Q_HEAD` structure must be updated to contain the first node in the queue (if any change has occurred).

The `putRtn` operator is called whenever a task becomes ready; that is, a task is no longer suspended, pended, delayed, or stopped (or a combination thereof).

The VxWorks round-robin algorithm performs a `removeRtn` operation followed by a `putRtn` when a task has exceeded its time slice. In this case, the task does not change state. However, the expectation after performing a `removeRtn` operation followed by a `putRtn` operation is that the task appears as the last task in the list of tasks with the same priority, if there are any.

Performing a `taskDelay(0)` operation also results in a `removeRtn` operation followed by a `putRtn`. Again, in this case the task does not change state, and the expectation after performing a `removeRtn` operation followed by a `putRtn` operation is that the task appears as the last task in the list of tasks with the same priority, if there are any.

This operator is called from within kernel context.

**Q_CLASS getRtn Operator**

This operator removes and returns the first node in a multi-way queue.

Currently the kernel does not utilize this operator.

**Q_CLASS removeRtn Operator**

This operator removes the specified node from the specified multi-way queue.

The `removeRtn` operator is called whenever a task is no longer ready; that is, it is no longer eligible for execution, since it has become suspended, pended, delayed, or stopped (or a combination thereof).
See Q_CLASS putRtn Operator, p.122 above for more information about situations in which the kernel performs a removeRtn operation followed by a putRtn without the task’s state actually changing.

This operator is called from within kernel context.

Q_CLASS resortRtn Operator

This operator resorts a node to a new position based on a new key. The resortRtn operator is called whenever a task’s priority changes, either due to an explicit priority change with the taskPrioritySet() API, or an implicit priority change due to the mutex semaphore priority inheritance protocol.

The difference between invoking the resortRtn operator and a removeRtn/putRtn combination is that the former operator does not change the position of the task in the list of tasks with the same priority (if any) when the priority is the same as the old priority.

This operator is called from within kernel context.

Q_CLASS keyRtn Operator

This operator returns the key of a node currently in a multi-way queue. The keyType parameter determines key style on certain queue classes. Currently the kernel does not utilize this operator.

Q_CLASS infoRtn Operator

This routine gathers up information on a multi-way queue. The information consists of an array, supplied by the caller, filled with all the node pointers currently in the queue.

Currently the kernel does not utilize this operator.

Q_CLASS eachRtn Operator

This operator calls a user-supplied routine once for each node in the multi-way queue. The routine should be declared as follows:

```c
BOOL routine
{
    Q_NODE *pQNode, /* pointer to a queue node */
    int arg          /* arbitrary user-supplied argument */
};
```

The user-supplied routine should return TRUE if qEach() is to continue calling it for each entry, or FALSE if it is done, and qEach() can exit.
Currently the kernel does not utilize this operator.

2.11.2 POSIX Thread Scheduler

VxWorks also provides an alternative POSIX thread scheduler for POSIX threads in user space (processes). This scheduler is similar to the VxWorks native scheduler for VxWorks task scheduling. The major difference is in the support of scheduling decisions for threads that have different scheduling policies, SCHED_FIFO, SCHED_RR, or SCHED_OTHER policies. This scheduler supports, concurrently, threads with the different scheduling policies.

For more information, see 4.10 POSIX Scheduling, p.236 and the VxWorks Application Programmer’s Guide: POSIX Standard Interfaces.

2.11.3 Replacement Kernel Scheduler Framework

**WARNING:** The kernel scheduler is fundamental to a system’s behavior. The system’s behavior is not guarantee if a custom scheduler is used. You should ensure all VxWorks components behave as expected when a custom scheduler is used.

The replacement kernel scheduler framework allows users to customize, configure, and initialize a custom kernel scheduler for VxWorks. This section describes the necessary elements for custom schedulers.

**Class Specified Q_HEAD and Q_NODE**

User-specified ready queue class may need class-specific Q_NODE and Q_HEAD structures. As noted in Kernel Scheduler Multi-way Queue Structure, p.118, the size of these structures must not be more than 16 bytes.

**User Specified Q_CLASS**

User must define a ready-queue class for all READY tasks. A set of functions required by the Q_CLASS structure must be implemented. Kernel Scheduler Multi-way Queue Structure, p.118 includes the details about Q_CLASS.
There are several ways for users to link the definition and implementation of `Q_NODE`, `Q_HEAD`, and `Q_CLASS` to VxWorks. For example, the custom scheduler configuration file `installDir/vxworks-6.x/target/config/comps/src/usrCustomerScheduler.c` can be the placeholder for the `Q_NODE/Q_HEAD` type definitions and user specified `Q_CLASS` implementation.

Another way is to create a new header file for `Q_NODE/Q_HEAD` definition and a new source file for `Q_CLASS` implementation, and then link the new object file link to VxWorks by modifying the makefile in the BSP directory as follows:

```
MACH_EXTRA = qUserPriLib.o
```

Or, with the project facility, update the makefile in the project with the following:

```
EXTRA_MODULES = qUserPriLib.o
```

**INCLUDE_CUSTOM_SCHEDULER Component**

VxWorks must be configured with the `INCLUDE_CUSTOM_SCHEDULER` component to provide support for customized kernel schedulers.

Then the modification must be made to the function of `usrCustomSchedulerInit()` in `installDir/vxworks-6.x/target/config/comps/src/usrCustomerScheduler.c` to hook the user-specified ready queue structure and any hook routine to be executed at each tick interrupt. Here is an example for the `usrCustomSchedulerInit()` routine:

```c
void usrCustomSchedulerInit (void)
{
    vxKernelSchedDesc.readyQClassId    = qUserClassId;
    vxKernelSchedDesc.readyQInitArg1   = (void *) &usrReadyQBMap;
    vxKernelSchedDesc.readyQInitArg2   = (void *) 256;
    tickAnnounceHookAdd ((FUNCPTR)usrTickHook);
    kernelRoundRobinInstall();
}
```

See *Kernel Scheduler Initialization*, p. 117 for a description of the `vxKernelSchedDesc` variable. Users must initialize this variable for their own kernel scheduler.
User-Specified Data In Task Control Block

For a custom scheduler that needs to store user specific information in tasks, the `pSchedInfo` member of the Task Control Block (TCB) may be used. Currently, this member is of type `(void *)`. There are two ways to access `pSchedInfo`:

- If the `qNode` is given, the macro `TASK_QNODE_TO_PSCHEDINFO(qNode)` may be used to get the address of `pSchedInfo`. The file of `installDir/vxworks-6.x/target/h/taskLib.h` has the definition of this macro. This macro is typically used in the user-defined queue management functions. For example:

```c
void customQPut(
    CUSTOM_Q_HEAD     *pQHead,
    CUSTOM_NODE       *pQNode,
    ULONG              key
) {
    void          **ppSchedInfo = (void **) TASK_QNODE_TO_PSCHEDINFO (pQNode);
}
```

- If the task ID `tid` is given, the `TASK_SCHED_INFO_SET(tid, pSchedInfo)` macro can be used to set the `pSchedInfo` field in the TCB; and the macro `TASK_SCHED_INFO_GET(tid)` can be used for getting the value of `pSchedInfo`. Both macros are defined in `installDir/vxworks-6.x/target/h/taskUtilLib.h`.

User-defined scheduler may use `pSchedInfo` as the pointer to the user-specific data structure for tasks. If so, user is responsible for allocating the storage for the data structure. One way to allocate the memory is using a task hook function, which allocates the storage pointed to by `pSchedInfo`, with `taskCreateHookAdd()`. The storage can then be allocated by calling `malloc()` or `memalign()`. However, this approach makes the task creation process less deterministic.

Another way to allocate the memory is to statically allocate (with global variables) a chunk of memory dedicated for user-specific storage, and use them during task initialization.

User Specified Tick Hook Routine

For a custom scheduler that should perform operations at each tick interrupt, the `tickAnnounceHookAdd()` routine is provided to register a hook to be called at
each tick interrupt. Because they are run at interrupt context, the hook routines must obey the same rules as ISRs. Any VxWorks kernel services that should not be called in an interrupt context should not be called in the hooks. See 3.5 Interrupt Service Routines, p.209 for information about restrictions on ISRs.

The following is a pseudo code example illustrating the usage of the hook routine:

```c
void usrTickHook
{
    int tid

    update the statistics information if needed;
    update interrupted task's time slice if needed;
    resort the interrupted task location in the ready queue if needed.
}
```

VxWorks provides a round robin policy implementation, with the hook facility `kernelRoundRobinHook()`. This hook checks if:

1. The interrupted task has not lock preemption.
2. The interrupted task is still in the READY state.
3. The interrupted task has consumed its allowed time slice.

This task is then placed at the tail of the task list for its priority in the ready queue and its time slice is reset.

Another routine, `kernelTimeSlice()`, is used to dynamically enable or disable the round robin scheduling and to adjust the system time slice.

A custom scheduler may choose to use the `kernelRoundRobinHook()` to perform round robin scheduling if this implementation is appropriate for user specified scheduler. To take advantage of the VxWorks's implementation for round robin scheduling, the function `kernelRoundRobinInstall()` should be called in the `usrCustomSchedulerInit()` routine to install the `kernelRoundRobinHook()`. The routine `_func_kernelRoundRobinHook()` can then be called within the user defined hook for the round robin policy to take effect. The `_func_kernelRoundRobinHook()` takes a `tid` (the task ID) of the interrupted task, as the argument. The following is an example of code that takes advantage of the VxWorks round robin scheduling scheme:
void usrTickHook
    int tid
) {
    /* statistic information */
    userTick++;
    /* call kernelRoundRobinHook */
    if (_func_kernelRoundRobinHook != NULL)
        _func_kernelRoundRobinHook (tid);
    /* other work */
    ...
}

If the user specified scheduler does not implement the round robin policy use the
kernelRoundRobinHook() facility for the round robin implementation, the
routine kernelTimeSlice() must not be used to adjust system time slice nor to
enable or disable round robin policy.
3
Multitasking

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3.1 Introduction

Modern real-time systems are based on the complementary concepts of multitasking and intertask communications. A multitasking environment allows a real-time application to be constructed as a set of independent tasks, each with its own thread of execution and set of system resources.

Tasks are the basic unit of scheduling in VxWorks. All tasks, whether in the kernel or in processes, are subject to the same scheduler. VxWorks processes are not themselves scheduled.

Intertask communication facilities allow tasks to synchronize and communicate in order to coordinate their activity. In VxWorks, the intertask communication facilities include semaphores, message queues, message channels, pipes, network-transparent sockets, and signals.
For interprocess communication, VxWorks semaphores and message queues, pipes, and events (as well as POSIX semaphores and events) can be created as public objects to provide accessibility across memory boundaries (between the kernel and processes, and between different processes). In addition, message channels provide a socket-based inter-process communications mechanism.

Hardware interrupt handling is a key facility in real-time systems because interrupts are the usual mechanism to inform a system of external events. To get the fastest possible response to interrupts, interrupt service routines (ISRs) in VxWorks run in a special context of their own, outside any task’s context.

VxWorks includes a watchdog-timer mechanism that allows any C function to be connected to a specified time delay. Watchdog timers are maintained as part of the system clock ISR. For information about POSIX timers, see 4.6 POSIX Clocks and Timers, p. 225.

This chapter discusses the tasking, intertask communication, and interprocess communication facilities that are at the heart of the VxWorks run-time environment.

For information about VxWorks and POSIX, see 4. POSIX Standard Interfaces.

NOTE: This chapter provides information about facilities available in the VxWorks kernel. For information about facilities available to real-time processes, see the corresponding chapter in the VxWorks Application Programmer’s Guide.

### 3.2 Tasks and Multitasking

VxWorks tasks are the basic unit of code execution in the operating system itself, as well as in applications that it executes in processes. In other operating systems the term thread is used similarly.

Multitasking provides the fundamental mechanism for an application to control and react to multiple, discrete real-world events. The VxWorks real-time kernel provides the basic multitasking environment. Multitasking creates the appearance of many threads of execution running concurrently when, in fact, the kernel interleaves their execution on the basis of a scheduling algorithm.
Each task has its own context, which is the CPU environment and system resources that the task sees each time it is scheduled to run by the kernel. On a context switch, a task’s context is saved in the task control block (TCB).

A task’s context includes:

- a thread of execution; that is, the task’s program counter
- the tasks’ virtual memory context (if process support is included)
- the CPU registers and (optionally) coprocessor registers
- stacks for dynamic variables and function calls
- I/O assignments for standard input, output, and error
- a delay timer
- a time-slice timer
- kernel control structures
- signal handlers
- task variables
- task private environment (for environment variables)
- error status (errno)
- debugging and performance monitoring values

If VxWorks is configured without process support (the INCLUDE_RTP component), the context of a task does not include its virtual memory context. All tasks can only run in a single common address space (the kernel).

However, if VxWorks is configured with process support—regardless of whether or not any processes are active—the context of a kernel task does include its virtual memory context, because the system has the potential to operate with other virtual memory contexts besides the kernel. That is, the system could have tasks running in several different virtual memory contexts (the kernel and one or more processes).

For information about virtual memory contexts, see 5. Memory Management.

**NOTE:** The POSIX standard includes the concept of a thread, which is similar to a task, but with some additional features. For details, see 4.9 POSIX Threads, p.229.
3.2.1 Task State Transition

The kernel maintains the current state of each task in the system. A task changes from one state to another as a result of kernel function calls made by the application. When created, tasks enter the suspended state. Activation is necessary for a created task to enter the ready state. The activation phase is extremely fast, enabling applications to pre-create tasks and activate them in a timely manner. An alternative is the spawning primitive, which allows a task to be created and activated with a single function. Tasks can be deleted from any state.

Table 3-1 describes the state symbols that you see when working with development tools. Example 3-1 shows output from the i() and taskShow() shell commands containing task state information.

<table>
<thead>
<tr>
<th>State Symbol</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>READY</td>
<td>The task is not waiting for any resource other than the CPU.</td>
</tr>
<tr>
<td>PEND</td>
<td>The task is blocked due to the unavailability of some resource.</td>
</tr>
<tr>
<td>DELAY</td>
<td>The task is asleep for some duration.</td>
</tr>
<tr>
<td>SUSPEND</td>
<td>The task that is unavailable for execution (but not pended or delayed). This state is used primarily for debugging. Suspension does not inhibit state transition, only execution. Thus, pended-suspended tasks can still unblock and delayed-suspended tasks can still awaken.</td>
</tr>
<tr>
<td>STOP</td>
<td>The task is stopped by the debugger.</td>
</tr>
<tr>
<td>DELAY + S</td>
<td>The task is both delayed and suspended.</td>
</tr>
<tr>
<td>PEND + S</td>
<td>The task is both pended and suspended.</td>
</tr>
<tr>
<td>PEND + T</td>
<td>The a task is pended with a timeout value.</td>
</tr>
<tr>
<td>STOP + P</td>
<td>Task is pended and stopped by the debugger.</td>
</tr>
<tr>
<td>STOP + S</td>
<td>Task is stopped by the debugger and suspended.</td>
</tr>
<tr>
<td>STOP + T</td>
<td>Task is delayed and stopped by the debugger.</td>
</tr>
<tr>
<td>PEND + S + T</td>
<td>The task is pended with a timeout value and suspended.</td>
</tr>
<tr>
<td>STOP+P+S</td>
<td>Task is pended, suspended and stopped by the debugger.</td>
</tr>
</tbody>
</table>
The STOP state is used by the debugger facilities when a breakpoint is hit. It is also used by the error detection and reporting facilities when an error condition occurs (see 9. Error Detection and Reporting).

Example 3-1  Task States in Shell Command Output

-> taskShow

<table>
<thead>
<tr>
<th>NAME</th>
<th>ENTRY</th>
<th>TID</th>
<th>PRI</th>
<th>STATUS</th>
<th>PC</th>
<th>SP</th>
<th>ERRNO</th>
<th>DELAY</th>
</tr>
</thead>
<tbody>
<tr>
<td>tShell0</td>
<td>shellTask</td>
<td>455720</td>
<td>1</td>
<td>READY</td>
<td>214118</td>
<td>5db390</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>value = 0 = 0x0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>NAME</th>
<th>ENTRY</th>
<th>TID</th>
<th>PRI</th>
<th>STATUS</th>
<th>PC</th>
<th>SP</th>
<th>ERRNO</th>
<th>DELAY</th>
</tr>
</thead>
<tbody>
<tr>
<td>tExcTask</td>
<td>excTask</td>
<td>437460</td>
<td>0</td>
<td>PEND</td>
<td>209fac</td>
<td>484e40</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>tJobTask</td>
<td>jobTask</td>
<td>437910</td>
<td>0</td>
<td>PEND</td>
<td>20c6dc</td>
<td>487d30</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>tLogTask</td>
<td>logTask</td>
<td>437c80</td>
<td>0</td>
<td>PEND</td>
<td>209fac</td>
<td>48a190</td>
<td>3d0001</td>
<td>0</td>
</tr>
<tr>
<td>tShell0</td>
<td>shellTask</td>
<td>455720</td>
<td>1</td>
<td>READY</td>
<td>214118</td>
<td>5db390</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>tWdbTask</td>
<td>wdbTask</td>
<td>517a98</td>
<td>3</td>
<td>PEND</td>
<td>20c6dc</td>
<td>5c7560</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>tNetTask</td>
<td>netTask</td>
<td>43db90</td>
<td>50</td>
<td>PEND</td>
<td>20c6dc</td>
<td>48d920</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>value = 0 = 0x0</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Figure 3-1 illustrates task state transitions. The routines listed are examples of ones that would cause the associated transition. For example, a task that called taskDelay() would move from the ready state to the delayed state.
Multitasking requires a scheduling algorithm to allocate the CPU to ready tasks. The default algorithm is priority-based preemptive scheduling. You can also select to use round-robin scheduling for your applications (see *Round-Robin Scheduling*, p.136). Both algorithms rely on the task’s priority.

See 2.11 *Kernel Schedulers*, p.116 for information about implementing custom schedulers, and about using a POSIX threads scheduler for processes (RTPs).

The kernel has 256 priority levels, numbered 0 through 255. Priority 0 is the highest and priority 255 is the lowest.

All application tasks should be in the priority range from 100 to 255.
Tasks are assigned a priority when created. You can also change a task’s priority level while it is executing by calling `taskPrioritySet()`. The ability to change task priorities dynamically allows applications to track precedence changes in the real world.

The routines that control task scheduling are listed in Table 3-2.

Table 3-2  Task Scheduler Control Routines

<table>
<thead>
<tr>
<th>Routine</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>kernelTimeSlice()</td>
<td>Controls round-robin scheduling.</td>
</tr>
<tr>
<td>taskPrioritySet()</td>
<td>Changes the priority of a task.</td>
</tr>
<tr>
<td>taskLock( )</td>
<td>Disables task rescheduling.</td>
</tr>
<tr>
<td>taskUnlock( )</td>
<td>Enables task rescheduling.</td>
</tr>
</tbody>
</table>

POSIX also provides a scheduling interface. For more information, see 4.10 POSIX Scheduling, p.236.

Preemptive Priority Scheduling

A preemptive priority-based scheduler preempts the CPU when a task has a higher priority than the current task running. Thus, the kernel ensures that the CPU is always allocated to the highest priority task that is ready to run. This means that if a task—with a higher priority than that of the current task—becomes ready to run, the kernel immediately saves the current task’s context, and switches to the context of the higher priority task. For example, in Figure 3-2, task t1 is preempted by higher-priority task t2, which in turn is preempted by t3. When t3 completes, t2 continues executing. When t2 completes execution, t1 continues executing.

The disadvantage of this scheduling algorithm is that, when multiple tasks of equal priority must share the processor, if a single task is never blocked, it can usurp the processor. Thus, other equal-priority tasks are never given a chance to run. Round-robin scheduling solves this problem.
Round-Robin Scheduling

A round-robin scheduling algorithm attempts to share the CPU fairly among all ready tasks of the same priority. Round-robin scheduling uses time slicing to achieve fair allocation of the CPU to all tasks with the same priority. Each task, in a group of tasks with the same priority, executes for a defined interval or time slice.

Round-robin scheduling is enabled by calling `kernelTimeSlice()`, which takes a parameter for a time slice, or interval. This interval is the amount of time each task is allowed to run before relinquishing the processor to another equal-priority task. Thus, the tasks rotate, each executing for an equal interval of time. No task gets a second slice of time before all other tasks in the priority group have been allowed to run.

In most systems, it is not necessary to enable round-robin scheduling, the exception being when multiple copies of the same code are to be run, such as in a user interface task.

If round-robin scheduling is enabled, and preemption is enabled for the executing task, the system tick handler increments the task’s time-slice count. When the specified time-slice interval is completed, the system tick handler clears the counter and the task is placed at the tail of the list of tasks at its priority level. New tasks joining a given priority group are placed at the tail of the group with their run-time counter initialized to zero.

Enabling round-robin scheduling does not affect the performance of task context switches, nor is additional memory allocated.
If a task blocks or is preempted by a higher priority task during its interval, its
time-slice count is saved and then restored when the task becomes eligible for
execution. In the case of preemption, the task will resume execution once the
higher priority task completes, assuming that no other task of a higher priority is
ready to run. In the case where the task blocks, it is placed at the tail of the list of
tasks at its priority level. If preemption is disabled during round-robin scheduling,
the time-slice count of the executing task is not incremented.

Time-slice counts are accrued by the task that is executing when a system tick
occurs, regardless of whether or not the task has executed for the entire tick
interval. Due to preemption by higher priority tasks or ISRs stealing CPU time
from the task, it is possible for a task to effectively execute for either more or less
total CPU time than its allotted time slice.

Figure 3-3 shows round-robin scheduling for three tasks of the same priority: t1,
t2, and t3. Task t2 is preempted by a higher priority task t4 but resumes at the count
where it left off when t4 is finished.

![Round-Robin Scheduling](image)

Preemption Locks

The scheduler can be explicitly disabled and enabled on a per-task basis in the
kernel with the routines `taskLock()` and `taskUnlock()`. When a task disables the
scheduler by calling `taskLock()`, no priority-based preemption can take place
while that task is running.
If the task that has disabled the scheduler with taskLock() explicitly blocks or suspends, the scheduler selects the next highest-priority eligible task to execute. When the preemption-locked task unblocks and begins running again, preemption is again disabled.

Note that preemption locks prevent task context switching, but do not lock out interrupt handling.

Preemption locks can be used to achieve mutual exclusion; however, keep the duration of preemption locking to a minimum. For more information, see 3.3.3 Mutual Exclusion, p.160.

A Comparison of taskLock() and intLock()

When using taskLock(), consider that it will not achieve mutual exclusion. Generally, if interrupted by hardware, the system will eventually return to your task. However, if you block, you lose task lockout. Thus, before you return from the routine, taskUnlock() should be called.

When a task is accessing a variable or data structure that is also accessed by an ISR, you can use intLock() to achieve mutual exclusion. Using intLock() makes the operation atomic in a single processor environment. It is best if the operation is kept minimal, meaning a few lines of code and no function calls. If the call is too long, it can directly impact interrupt latency and cause the system to become far less deterministic.

Driver Support Task Priority

In contrast to application tasks, which should be in the task priority range from 100 to 255, driver support tasks (which are associated with an ISR) can be in the range of 51-99.

These tasks are crucial; for example, if a support task fails while copying data from a chip, the device loses that data. Examples of driver support tasks include tNetTask (the VxWorks network support task), an HDLC task, and so on.

The system tnetTask has a priority of 50, so user tasks should not be assigned priorities below that task; if they are, the network connection could die and prevent debugging capabilities with the host tools.
3.2.3 Task Control

The following sections give an overview of the basic VxWorks task routines, which are found in the VxWorks library `taskLib`. These routines provide the means for task creation and control, as well as for retrieving information about tasks. See the VxWorks API reference for `taskLib` for further information.

For interactive use, you can control VxWorks tasks with the host tools or the kernel shell; see the Wind River Workbench User’s Guide, the VxWorks Command-Line Tools User’s Guide, and VxWorks Kernel Programmer’s Guide: Target Tools.

Task Creation and Activation

The routines listed in Table 3-3 are used to create tasks.

The arguments to `taskSpawn()` are the new task’s name (an ASCII string), the task’s priority, an options word, the stack size, the main routine address, and 10 arguments to be passed to the main routine as startup parameters:

```
id = taskSpawn ( name, priority, options, stacksize, main, arg1, ...arg10 );
```

The `taskSpawn()` routine creates the new task context, which includes allocating the stack and setting up the task environment to call the main routine (an ordinary subroutine) with the specified arguments. The new task begins execution at the entry to the specified routine.

<table>
<thead>
<tr>
<th>Call</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>taskSpawn()</code></td>
<td>Spawns (creates and activates) a new task.</td>
</tr>
<tr>
<td><code>taskCreate()</code></td>
<td>Creates, but not activates a new task.</td>
</tr>
<tr>
<td><code>taskInit()</code></td>
<td>Initializes a new task.</td>
</tr>
<tr>
<td><code>taskInitExcStk()</code></td>
<td>Initializes a task with stacks at specified addresses.</td>
</tr>
<tr>
<td><code>taskOpen()</code></td>
<td>Open a task (or optionally create one, if it does not exist).</td>
</tr>
<tr>
<td><code>taskActivate()</code></td>
<td>Activates an initialized task.</td>
</tr>
</tbody>
</table>

Table 3-3 Task Creation Routines

The `taskOpen()` routine provides a POSIX-like API for creating a task (with optional activation) or obtaining a handle on existing task. It also provides for
creating a task as either a public or private object (see *Task Names and IDs*, p.141). The `taskOpen()` routine is the most general purpose task-creation routine.

The `taskSpawn()` routine embodies the lower-level steps of allocation, initialization, and activation. The initialization and activation functions are provided by the routines `taskCreate()` and `taskActivate()`; however, we recommend you use these routines only when you need greater control over allocation or activation.

The difference between `taskInit()` and `taskInitExcStk()` is that the `taskInit()` routine allows the specification of the execution stack address, while `taskInitExcStk()` allows the specification of both the execution and exception stacks.

### Task Stack

It can be difficult to know exactly how much stack space to allocate without reverse-engineering the configuration of the system. To help avoid a stack overflow, and task stack corruption, you can take the following approach: when initially allocating the stack, make it much larger than anticipated (for example, from 20KB to up to 100KB), depending upon the type of application; then periodically monitor the stack with `checkStack()`, and if it is safe to make it smaller, do so.

You can also use various components that provide task stack protection, such that an exception is generated when an overflow occurs, rather than memory corruption:

- If the `INCLUDE_RTP` component (which provides process support) is included in the system, all user tasks have overflow and underflow guard zones on the execution stack. Tasks in processes do not have guard zone on the exception stack by default. Kernel tasks also have no guard zones on the execution nor the exception stack by default, nor if `INCLUDE_RTP` is configured. The component `INCLUDE_PROTECT_TASK_STACK` must be configured to add overflow (no underflow) protection for user task exception stacks and to enable overflow and underflow protection for kernel task execution stacks. Note that kernel tasks have no guard zones on the exception stack.

- If the `INCLUDE_RTP` component is not included, but either the `INCLUDE_KERNEL_HARDENING` or the `INCLUDE_PROTECT_TASK_STACK` component is included, kernel tasks have overflow and underflow guard zones on the execution stack (but no guard zones on the exception stack). (If
the INCLUDE_RTP component is not included, there can be no process tasks in
the system.)

The protection provided by these components is, however, available only for tasks
that are created without the VX_NO_STACK_PROTECT task option. If tasks are
created with this option, no guard zones are created for their execution and
exception stacks.

Developers can also design and test their systems with the assistance of the
INCLUDE_PROTECT_TASK_STACK component. This component provides the
guard zone protection of stacks in the kernel, both for kernel task execution stacks
and for user task exception stacks. When this component is used, kernel tasks have
underflow and overflow protection on the execution stack (no protection on the
exception stacks for kernel tasks) and overflow protection (only) on the user task
exception stacks. Production systems can be shipped without the component to
save memory.

For more information about stack-protection facilities, see 5.10 Additional Memory
Protection Features, p.310.

Task Names and IDs

When a task is spawned, you can specify an ASCII string of any length to be the
task name, and a task ID is returned.

Most VxWorks task routines take a task ID as the argument specifying a task.
VxWorks uses a convention that a task ID of 0 (zero) always implies the calling
task. In the kernel, the task ID is a 4-byte handle to the task’s data structures.

The following rules and guidelines should be followed when naming tasks:

- The names of public tasks must be unique and must begin with a forward
  slash; for example /tMyTask. Note that public tasks are visible throughout
  the entire system—in the kernel and any processes.

- The names of private tasks should be unique. VxWorks does not require that
  private task names be unique, but it is preferable to use unique names to avoid
  confusing the user. (Note that private tasks are visible only within the entity in
  which they were created—either the kernel or a process.)

To use the host development tools to their best advantage, task names should not
conflict with globally visible routine or variable names. To avoid name conflicts,
VxWorks uses a convention of prefixing any kernel task name started from the
target with the letter t, and any task name started from the host with the letter u.
In addition, the name of the initial task of a real-time process is the executable file name (less the extension) prefixed with the letter i.

Creating a task as a public object allows other tasks from outside of its process to send signals or events to it (with the taskKill() or the eventSend() routine, respectively).

For more information, see 3.3.1 Public and Private Objects, p.157.

You do not have to explicitly name tasks. If a NULL pointer is supplied for the name argument of taskSpawn(), then VxWorks assigns a unique name. The name is of the form tN, where N is a decimal integer that is incremented by one for each unnamed task that is spawned.

The taskLib routines listed in Table 3-4 manage task IDs and names.

### Table 3-4 Task Name and ID Routines

<table>
<thead>
<tr>
<th>Call</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>taskName()</td>
<td>Gets the task name associated with a task ID (restricted to the context—process or kernel—in which it is called).</td>
</tr>
<tr>
<td>taskNameToId()</td>
<td>Looks up the task ID associated with a task name.</td>
</tr>
<tr>
<td>taskIdSelf()</td>
<td>Gets the calling task’s ID.</td>
</tr>
<tr>
<td>taskIdVerify()</td>
<td>Verifies the existence of a specified task.</td>
</tr>
</tbody>
</table>

### Task Options

When a task is spawned, you can pass in one or more option parameters, which are listed in Table 3-5. The result is determined by performing a logical OR operation on the specified options.

### Table 3-5 Task Options

<table>
<thead>
<tr>
<th>Name</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>VX_ALTIVEC_TASK</td>
<td>Execute with Altivec coprocessor support.</td>
</tr>
<tr>
<td>VX DEALLOC_EXC_STACK</td>
<td>Deallocates the exception stack.</td>
</tr>
<tr>
<td>VX DEALLOC_STACK</td>
<td>Deallocate stack.</td>
</tr>
<tr>
<td>VX_DSP_TASK</td>
<td>Execute with DSP coprocessor support.</td>
</tr>
</tbody>
</table>
You must include the `VX_FP_TASK` option when creating a task that:

- Performs floating-point operations.
- Calls any function that returns a floating-point value.
- Calls any function that takes a floating-point value as an argument.

For example:

```c
int tid = taskSpawn("tMyTask", 90, VX_FP_TASK, 20000, myFunc, 2387, 0, 0, 0, 0, 0, 0, 0, 0);
```

Some routines perform floating-point operations internally. The VxWorks documentation for each of these routines clearly states the need to use the `VX_FP_TASK` option.

After a task is spawned, you can examine or alter task options by using the routines listed in Table 3-6. Currently, only the `VX_UNBREAKABLE` option can be altered.

### Table 3-5 Task Options (cont'd)

<table>
<thead>
<tr>
<th>Name</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>VX_FP_TASK</td>
<td>Executes with the floating-point coprocessor.</td>
</tr>
<tr>
<td>VX_NO_STACK_FILL</td>
<td>Does not fill the stack with 0xee (for debugging)</td>
</tr>
<tr>
<td>VX_NO_STACK_PROTECT</td>
<td>Create without stack overflow or underflow guard zones.</td>
</tr>
<tr>
<td>VX_PRIVATE_ENV</td>
<td>Executes a task with a private environment.</td>
</tr>
<tr>
<td>VX_TASK_NOACTIVATE</td>
<td>Used with <code>taskOpen()</code> so that the task is not activated.</td>
</tr>
<tr>
<td>VX_UNBREAKABLE</td>
<td>Disables breakpoints for the task.</td>
</tr>
</tbody>
</table>

### Table 3-6 Task Option Routines

<table>
<thead>
<tr>
<th>Call</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>taskOptionsGet()</code></td>
<td>Examines task options.</td>
</tr>
<tr>
<td><code>taskOptionsSet()</code></td>
<td>Sets task options.</td>
</tr>
</tbody>
</table>
Task Information

The routines listed in Table 3-7 get information about a task by taking a snapshot of a task’s context when the routine is called. Because the task state is dynamic, the information may not be current unless the task is known to be dormant (that is, suspended).

Table 3-7  Task Information Routines

<table>
<thead>
<tr>
<th>Call</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>taskIdListGet()</td>
<td>Fills an array with the IDs of all active tasks.</td>
</tr>
<tr>
<td>taskInfoGet()</td>
<td>Gets information about a task.</td>
</tr>
<tr>
<td>taskPriorityGet()</td>
<td>Examines the priority of a task.</td>
</tr>
<tr>
<td>taskRegsGet()</td>
<td>Examines a task’s registers (cannot use with current task).</td>
</tr>
<tr>
<td>taskRegsSet()</td>
<td>Sets a task’s registers (cannot be used with the current task).</td>
</tr>
<tr>
<td>taskIsSuspended()</td>
<td>Checks whether a task is suspended.</td>
</tr>
<tr>
<td>taskIsReady()</td>
<td>Checks whether a task is ready to run.</td>
</tr>
<tr>
<td>taskIsPended()</td>
<td>Checks whether a task is pended.</td>
</tr>
<tr>
<td>taskTcb()</td>
<td>Gets a pointer to a task’s control block.</td>
</tr>
</tbody>
</table>

Also note that each task has a VxWorks events register, which receives events sent from other tasks, ISRs, semaphores, or message queues. See 3.3.7 VxWorks Events, p.179 for more information about this register, and the routines used to interact with it.

Task Deletion and Deletion Safety

Tasks can be dynamically deleted from the system. VxWorks includes the routines listed in Table 3-8 to delete tasks and to protect tasks from unexpected deletion.
3 Multitasking

3.2 Tasks and Multitasking

Tasks implicitly call `exit()` if the entry routine specified during task creation returns.

When a task is deleted, no other task is notified of this deletion. The routines `taskSafe()` and `taskUnsafe()` address problems that stem from unexpected deletion of tasks. The routine `taskSafe()` protects a task from deletion by other tasks. This protection is often needed when a task executes in a critical region or engages a critical resource.

For example, a task might take a semaphore for exclusive access to some data structure. While executing inside the critical region, the task might be deleted by another task. Because the task is unable to complete the critical region, the data structure might be left in a corrupt or inconsistent state. Furthermore, because the semaphore can never be released by the task, the critical resource is now unavailable for use by any other task and is essentially frozen.

Using `taskSafe()` to protect the task that took the semaphore prevents such an outcome. Any task that tries to delete a task protected with `taskSafe()` is blocked. When finished with its critical resource, the protected task can make itself available for deletion by calling `taskUnsafe()`, which readies any deleting task. To support nested deletion-safe regions, a count is kept of the number of times `taskSafe()` and `taskUnsafe()` are called. Deletion is allowed only when the count is zero, that is,

<table>
<thead>
<tr>
<th>Call</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>exit()</code></td>
<td>Terminates the calling task and frees memory (task stacks and task control blocks only).&lt;sup&gt;a&lt;/sup&gt;</td>
</tr>
<tr>
<td><code>taskDelete()</code></td>
<td>Terminates a specified task and frees memory (task stacks and task control blocks only).&lt;sup&gt;a&lt;/sup&gt; The calling task may terminate itself with this routine.</td>
</tr>
<tr>
<td><code>taskSafe()</code></td>
<td>Protects the calling task from deletion.</td>
</tr>
<tr>
<td><code>taskUnsafe()</code></td>
<td>Undoes a <code>taskSafe()</code>, which makes calling task available for deletion.</td>
</tr>
</tbody>
</table>

<sup>a</sup> Memory that is allocated by the task during its execution is not freed when the task is terminated.

⚠️ WARNING: Make sure that tasks are not deleted at inappropriate times. Before an application deletes a task, the task should release all shared resources that it holds.
there are as many *unsafes* as *safes*. Only the calling task is protected. A task cannot make another task safe or unsafe from deletion.

The following code fragment shows how to use `taskSafe()` and `taskUnsafe()` to protect a critical region of code:

```c
  taskSafe();
  semTake (semId, WAIT_FOREVER); /* Block until semaphore available */
  /* critical region code */
  semGive (semId); /* Release semaphore */
  taskUnsafe();
```

Deletion safety is often coupled closely with mutual exclusion, as in this example. For convenience and efficiency, a special kind of semaphore, the *mutual-exclusion semaphore*, offers an option for deletion safety. For more information, see *Mutual-Exclusion Semaphores*, p.167.

### Task Execution Control

The routines listed in Table 3-9 provide direct control over a task’s execution.

<table>
<thead>
<tr>
<th>Call</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>taskSuspend()</code></td>
<td>Suspends a task.</td>
</tr>
<tr>
<td><code>taskResume()</code></td>
<td>Resumes a task.</td>
</tr>
<tr>
<td><code>taskRestart()</code></td>
<td>Restarts a task.</td>
</tr>
<tr>
<td><code>taskDelay()</code></td>
<td>Delays a task; delay units are ticks, resolution in ticks.</td>
</tr>
<tr>
<td><code>nanosleep()</code></td>
<td>Delays a task; delay units are nanoseconds, resolution in ticks.</td>
</tr>
</tbody>
</table>

Tasks may require restarting during execution in response to some catastrophic error. The restart mechanism, `taskRestart()`, recreates a task with the original creation arguments.

Delay operations provide a simple mechanism for a task to sleep for a fixed duration. Task delays are often used for polling applications. For example, to delay a task for half a second without making assumptions about the clock rate, call:

```c
  taskDelay (sysClkRateGet () / 2);
```
The routine `sysClkRateGet()` returns the speed of the system clock in ticks per second. Instead of `taskDelay()`, you can use the POSIX routine `nanosleep()` to specify a delay directly in time units. Only the units are different; the resolution of both delay routines is the same, and depends on the system clock. For details, see 4.6 POSIX Clocks and Timers, p.225.

As a side effect, `taskDelay()` moves the calling task to the end of the ready queue for tasks of the same priority. In particular, you can yield the CPU to any other tasks of the same priority by delaying for zero clock ticks:

```c
    taskDelay(NO_WAIT); /* allow other tasks of same priority to run */
```

A delay of zero duration is only possible with `taskDelay()`, `nanosleep()` considers it an error.

**NOTE:** ANSI and POSIX APIs are similar.

System clock resolution is typically 60Hz (60 times per second). This is a relatively long time for one clock tick, and would be even at 100Hz or 120Hz. Thus, since periodic delaying is effectively polling, you may want to consider using event-driven techniques as an alternative.

### 3.2.4 Tasking Extensions

To allow additional task-related facilities to be added to the system, VxWorks provides hook routines that allow additional routines to be invoked whenever a task is created, a task context switch occurs, or a task is deleted. There are spare fields in the task control block (TCB) available for application extension of a task’s context.
These hook routines are listed in Table 3-10; for more information, see the VxWorks API reference for taskHookLib.

<table>
<thead>
<tr>
<th>Call</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>taskCreateHookAdd()</td>
<td>Adds a routine to be called at every task create.</td>
</tr>
<tr>
<td>taskCreateHookDelete()</td>
<td>Deletes a previously added task create routine.</td>
</tr>
<tr>
<td>taskSwitchHookAdd()</td>
<td>Adds a routine to be called at every task switch.</td>
</tr>
<tr>
<td>taskSwitchHookDelete()</td>
<td>Deletes a previously added task switch routine.</td>
</tr>
<tr>
<td>taskDeleteHookAdd()</td>
<td>Adds a routine to be called at every task delete.</td>
</tr>
<tr>
<td>taskDeleteHookDelete()</td>
<td>Deletes a previously added task delete routine.</td>
</tr>
</tbody>
</table>

When using switch hook routines, be aware of the following restrictions:

- Do not assume any virtual memory (VM) context is current other than the kernel context (as with ISRs).
- Do not rely on knowledge of the current task or invoke any function that relies on this information, for example taskIdSelf().
- Do not rely on taskIdVerify (pOldTcb) to determine if a delete hook has executed for the self-destructing task case. Instead, other state information needs to be changed in the delete hook to be detected by the switch hook (for example by setting a pointer to NULL).

Task create hook routines execute in the context of the creator task. Task create hooks need to consider the ownership of any kernel objects (such as watchdog timers, semaphores, and so on) created in the hook routine. Since create hook routines execute in the context of the creator task, new kernel objects will be owned by the creator task’s process. It may be necessary to assign the ownership of these objects to the new task’s process. This will prevent unexpected object reclamation from occurring if and when the process of the creator task terminates.

When the creator task is a kernel task, the kernel will own any kernel objects that are created. Thus there is no concern about unexpected object reclamation for this case.

User-installed switch hooks are called within the kernel context and therefore do not have access to all VxWorks facilities. Table 3-11 summarizes the routines that
can be called from a task switch hook; in general, any routine that does not involve the kernel can be called.

### Table 3-11 Routines Callable by Task Switch Hooks

<table>
<thead>
<tr>
<th>Library</th>
<th>Routines</th>
</tr>
</thead>
<tbody>
<tr>
<td>bLib</td>
<td>All routines</td>
</tr>
<tr>
<td>fppArchLib</td>
<td>fppSave(), fppRestore()</td>
</tr>
<tr>
<td>intLib</td>
<td>intContext(), intCount(), intVecSet(), intVecGet(), intLock(), intUnlock()</td>
</tr>
<tr>
<td>lstLib</td>
<td>All routines except lstFree()</td>
</tr>
<tr>
<td>mathALib</td>
<td>All are callable if fppSave()/fppRestore() are used</td>
</tr>
<tr>
<td>rngLib</td>
<td>All routines except rngCreate()</td>
</tr>
<tr>
<td>taskLib</td>
<td>taskIdVerify(), taskIdDefault(), taskIdReady(), taskIdSuspended(), taskIdTcb()</td>
</tr>
<tr>
<td>vxLib</td>
<td>vxTas()</td>
</tr>
</tbody>
</table>

**NOTE:** For information about POSIX extensions, see 4. POSIX Standard Interfaces.

### 3.2.5 Task Error Status: errno

By convention, C library functions set a single global integer variable `errno` to an appropriate error number whenever the function encounters an error. This convention is specified as part of the ANSI C standard.

#### Layered Definitions of errno

In VxWorks, `errno` is simultaneously defined in two different ways. There is, as in ANSI C, an underlying global variable called `errno`, which you can display by name using host development tools.

However, `errno` is also defined as a macro in `errno.h`; this is the definition visible to all of VxWorks except for one function. The macro is defined as a call to a function `__errno()` that returns the address of the global variable, `errno` (as you
might guess, this is the single function that does not itself use the macro definition for `errno). This subterfuge yields a useful feature: because `__errno()` is a function, you can place breakpoints on it while debugging, to determine where a particular error occurs.

Nevertheless, because the result of the macro `errno` is the address of the global variable `errno`, C programs can set the value of `errno` in the standard way:

```
erro = someErrorNumber;
```

As with any other `errno` implementation, take care not to have a local variable of the same name.

### A Separate `errno` Value for Each Task

In VxWorks, the underlying global `errno` is a single predefined global variable that can be referenced directly by application code that is linked with VxWorks (either statically on the host or dynamically at load time).

However, for `errno` to be useful in the multitasking environment of VxWorks, each task must see its own version of `errno`. Therefore `errno` is saved and restored by the kernel as part of each task’s context every time a context switch occurs.

Similarly, interrupt service routines (ISRs) see their own versions of `errno`. This is accomplished by saving and restoring `errno` on the interrupt stack as part of the interrupt enter and exit code provided automatically by the kernel (see 3.5.1 Connecting Routines to Interrupts, p.209).

Thus, regardless of the VxWorks context, an error code can be stored or consulted with direct manipulation of the global variable `errno`.

### Error Return Convention

Almost all VxWorks functions follow a convention that indicates simple success or failure of their operation by the actual return value of the function. Many functions return only the status values `OK` (0) or `ERROR` (-1). Some functions that normally return a nonnegative number (for example, `open()` returns a file descriptor) also return `ERROR` to indicate an error. Functions that return a pointer usually return `NULL` (0) to indicate an error. In most cases, a function returning such an error indication also sets `errno` to the specific error code.

The global variable `errno` is never cleared by VxWorks routines. Thus, its value always indicates the last error status set. When a VxWorks subroutine gets an error...
indication from a call to another routine, it usually returns its own error indication without modifying \texttt{errno}. Thus, the value of \texttt{errno} that is set in the lower-level routine remains available as the indication of error type.

For example, the VxWorks routine \texttt{intConnect()}, which connects a user routine to a hardware interrupt, allocates memory by calling \texttt{malloc()} and builds the interrupt driver in this allocated memory. If \texttt{malloc()} fails because insufficient memory remains in the pool, it sets \texttt{errno} to a code indicating an insufficient-memory error was encountered in the memory allocation library, \texttt{memLib}. The \texttt{malloc()} routine then returns \texttt{NULL} to indicate the failure. The \texttt{intConnect()} routine, receiving the \texttt{NULL} from \texttt{malloc()}, then returns its own error indication of \texttt{ERROR}. However, it does not alter \texttt{errno} leaving it at the insufficient memory code set by \texttt{malloc()}. For example:

\begin{verbatim}
if ((pNew = malloc (CHUNK_SIZE)) == NULL)
  return (ERROR);
\end{verbatim}

It is recommended that you use this mechanism in your own subroutines, setting and examining \texttt{errno} as a debugging technique. A string constant associated with \texttt{errno} can be displayed using \texttt{printErrno()} if the \texttt{errno} value has a corresponding string entered in the error-status symbol table, \texttt{statSymTbl}. See the VxWorks API reference for \texttt{errnoLib} for details on error-status values and building \texttt{statSymTbl}.

Assignment of Error Status Values

VxWorks \texttt{errno} values encode the module that issues the error, in the most significant two bytes, and uses the least significant two bytes for individual error numbers. All VxWorks module numbers are in the range 1–500; \texttt{errno} values with a module number of zero are used for source compatibility.

All other \texttt{errno} values (that is, positive values greater than or equal to $501\times 16$, and all negative values) are available for application use.

See the VxWorks API reference on \texttt{errnoLib} for more information about defining and decoding \texttt{errno} values with this convention.

3.2.6 Task Exception Handling

Errors in program code or data can cause hardware exception conditions such as illegal instructions, bus or address errors, divide by zero, and so forth. The VxWorks exception handling package takes care of all such exceptions (see 9. Error Detection and Reporting).
Tasks can also attach their own handlers for certain hardware exceptions through the `signal` facility. If a task has supplied a signal handler for an exception, the default exception handling described above is not performed. A user-defined signal handler is useful for recovering from catastrophic events. Typically, `setjmp()` is called to define the point in the program where control will be restored, and `longjmp()` is called in the signal handler to restore that context. Note that `longjmp()` restores the state of the task’s signal mask.

Signals are also used for signaling software exceptions as well as hardware exceptions. They are described in more detail in 3.3.10 Signals, p.202 and in the VxWorks API reference for `sigLib`.

### 3.2.7 Shared Code and Reentrancy

In VxWorks, it is common for a single copy of a subroutine or subroutine library to be invoked by many different tasks. For example, many tasks may call `printf()`, but there is only a single copy of the subroutine in the system. A single copy of code executed by multiple tasks is called shared code. VxWorks dynamic linking facilities make this especially easy. Shared code makes a system more efficient and easier to maintain; see Figure 3-4.

![Figure 3-4 Shared Code](image)

Shared code must be reentrant. A subroutine is reentrant if a single copy of the routine can be called from several task contexts simultaneously without conflict. Such conflict typically occurs when a subroutine modifies global or static variables, because there is only a single copy of the data and code. A routine’s references to such variables can overlap and interfere in invocations from different task contexts.
Most routines in VxWorks are reentrant. However, you should assume that any routine `someName()` is not reentrant if there is a corresponding routine named `someName_r()` — the latter is provided as a reentrant version of the routine. For example, because `ldiv()` has a corresponding routine `ldiv_r()`, you can assume that `ldiv()` is not reentrant.

VxWorks I/O and driver routines are reentrant, but require careful application design. For buffered I/O, we recommend using file-pointer buffers on a per-task basis. At the driver level, it is possible to load buffers with streams from different tasks, due to the global file descriptor table in VxWorks.

This may or may not be desirable, depending on the nature of the application. For example, a packet driver can mix streams from different tasks because the packet header identifies the destination of each packet.

The majority of VxWorks routines use the following reentrancy techniques:

- dynamic stack variables
- global and static variables guarded by semaphores
- task variables

We recommend applying these same techniques when writing application code that can be called from several task contexts simultaneously.

**NOTE:** In some cases reentrant code is not preferable. A critical section should use a binary semaphore to guard it, or use `intLock()` or `intUnlock()` if called from by an ISR.

**NOTE:** Initialization routines should be callable multiple times, even if logically they should only be called once. As a rule, routines should avoid static variables that keep state information. Initialization routines are an exception; using a static variable that returns the success or failure of the original initialization routine call is appropriate.

### Dynamic Stack Variables

Many subroutines are pure code, having no data of their own except dynamic stack variables. They work exclusively on data provided by the caller as parameters. The linked-list library, `lstLib`, is a good example of this. Its routines operate on lists and nodes provided by the caller in each subroutine call.
Subroutines of this kind are inherently reentrant. Multiple tasks can use such routines simultaneously, without interfering with each other, because each task does indeed have its own stack. See Figure 3-5.

Guarded Global and Static Variables

Some libraries encapsulate access to common data. This kind of library requires some caution because the routines are not inherently reentrant. Multiple tasks simultaneously invoking the routines in the library might interfere with access to common variables. Such libraries must be made explicitly reentrant by providing a mutual-exclusion mechanism to prohibit tasks from simultaneously executing critical sections of code. The usual mutual-exclusion mechanism is the mutex semaphore facility provided by *semMLib* and described in *Mutual-Exclusion Semaphores*, p.167.

Task Variables

Some routines that can be called by multiple tasks simultaneously may require global or static variables with a distinct value for each calling task. For example, several tasks may reference a private buffer of memory and yet refer to it with the same global variable.
To accommodate this, VxWorks provides a facility called *task variables* that allows 4-byte variables to be added to a task’s context, so that the value of such a variable is switched every time a task switch occurs to or from its owner task. Typically, several tasks declare the same variable (4-byte memory location) as a task variable. Each of those tasks can then treat that single memory location as its own private variable; see Figure 3-6. This facility is provided by the routines `taskVarAdd()`, `taskVarDelete()`, `taskVarSet()`, and `taskVarGet()`, which are described in the VxWorks API reference for `taskVarLib`.

![Figure 3-6 Task Variables and Context Switches](image)

Use this mechanism sparingly. Each task variable adds a few microseconds to the context switching time for its task, because the value of the variable must be saved and restored as part of the task’s context. Consider collecting all of a module’s task variables into a single dynamically allocated structure, and then making all accesses to that structure indirectly through a single pointer. This pointer can then be the task variable for all tasks using that module.

**Multiple Tasks with the Same Main Routine**

With VxWorks, it is possible to spawn several tasks with the same main routine. Each spawn creates a new task with its own stack and context. Each spawn can also pass the main routine different parameters to the new task. In this case, the same rules of reentrancy described in *Task Variables*, p.154 apply to the entire task.
This is useful when the same function needs to be performed concurrently with different sets of parameters. For example, a routine that monitors a particular kind of equipment might be spawned several times to monitor several different pieces of that equipment. The arguments to the main routine could indicate which particular piece of equipment the task is to monitor.

In Figure 3-7, multiple joints of the mechanical arm use the same code. The tasks manipulating the joints invoke `joint()`. The joint number (jointNum) is used to indicate which joint on the arm to manipulate.

```
joint(int jointNum)
{
    /* joint code here */
}
```
3.3 Intertask and Interprocess Communications

The complement to the multitasking routines described in 3.2 Tasks and Multitasking, p.130 is the intertask communication facilities. These facilities permit independent tasks to coordinate their actions.

VxWorks supplies a rich set of intertask and interprocess communication mechanisms, including:

- **Shared memory**, for simple sharing of data.
- **Semaphores**, for basic mutual exclusion and synchronization.
- **Mutexes and condition variables** for mutual exclusion and synchronization using POSIX interfaces.
- **Message queues and pipes**, for intertask message passing within a CPU.
- **VxWorks events**, for communication and synchronization.
- **Message channels**, for socket-based interprocess communication.
- **Sockets and remote procedure calls**, for network-transparent intertask communication.
- **Signals**, for exception handling, interprocess communication, and process management.

In addition, the VxMP component provides for intertask communication between multiple CPUs that share memory. See 12. Shared-Memory Objects: VxMP.

3.3.1 Public and Private Objects

Kernel objects such as semaphores and message queues can be created as either private or public objects. This provides control over the scope of their accessibility—which can be limited to a virtual memory context by defining them as private, or extended to the entire system (the kernel and any processes) by defining them as public. There is no difference in performance between a public and a private object.

An object can only be defined as public or private when it is created—the designation cannot be changed thereafter. Public objects must be named when they are created, and the name must begin with a forward slash; for example, `/foo`. Private objects do not need to be named.

For information about naming tasks in addition to that provided in this section, see Task Names and IDs, p.141.
Creating and Naming Public and Private Objects

Public objects are always named, and the name must begin with a forward-slash. Private objects can be named or unnamed. If they are named, the name must not begin with a forward-slash.

Only one public object of a given class and name can be created. That is, there can be only one public semaphore with the name /foo. But there may be a public semaphore named /foo and a public message queue named /foo. Obviously, more distinctive naming is preferable (such as /fooSem and /fooMQ).

The system allows creation of only one private object of a given class and name in any given memory context; that is, in any given process or in the kernel. For example:

- If process A has created a private semaphore named bar, it cannot create a second semaphore named bar.
- However, process B could create a private semaphore named bar, as long as it did not already own one with that same name.

Note that private tasks are an exception to this rule—duplicate names are permitted for private tasks; see Task Names and IDs, p.141.

To create a named object, the appropriate xyzOpen() API must be used, such as semOpen(). When the routine specifies a name that starts with a forward slash, the object will be public.

To delete public objects, the xyzDelete() API cannot be used (it can only be used with private objects). Instead, the xyzClose() and xyzUnlink() APIs must be used in accordance with the POSIX standard. That is, they must be unlinked from the name space, and then the last close operation will delete the object (for example, using the semUnlink() and semClose() APIs for a public semaphore). Alternatively, all close operations can be performed first, and then the unlink operation, after which the object is deleted. Note that if an object is created with the OM_DELETE_ON_LAST_CLOSE flag, it is be deleted with the last close operation, regardless of whether or not it was unlinked.

Object Ownership and Resource Reclamation

All objects are owned by the process to which the creator task belongs, or by the kernel if the creator task is a kernel task. When ownership must be changed, for example on a process creation hook, the objOwnerSet() can be used. However, its use is restricted—the new owner must be a process or the kernel.
All objects that are owned by a process are automatically destroyed when the process dies.

All objects that are children of another object are automatically destroyed when the parent object is destroyed.

Processes can share public objects through an object lookup-by-name capability (with the `xyzOpen()` set of routines). Sharing objects between processes can only be done by name.

When a process terminates, all the private objects that it owns are deleted, regardless of whether or not they are named. All references to public objects in the process are closed (an `xyzClose()` operation is performed). Therefore, any public object is deleted during resource reclamation, regardless of which process created them, if there are no more outstanding `xyzOpen()` calls against it (that is, no other process or the kernel has a reference to it), and the object was already unlinked or was created with the `OM_DELETE_ON_LAST_CLOSE` option. The exception to this rule is tasks, which are always reclaimed when its creator process dies.

When the creator process of a public object dies, but the object survives because it hasn’t been unlinked or because another process has a reference to it, ownership of the object is assigned to the kernel.

The `objShowAll()` show routine can be used to display information about ownership relations between objects.

### 3.3.2 Shared Data Structures

The most obvious way for tasks executing in the same memory space (either a process or the kernel) to communicate is by accessing shared data structures. Because all the tasks in a single process or in the kernel exist in a single linear address space, sharing data structures between tasks is trivial; see Figure 3-8.

Global variables, linear buffers, ring buffers, linked lists, and pointers can be referenced directly by code running in different contexts.

For information about using shared data regions to communicate between processes, see *VxWorks Application Programmer’s Guide: Applications and Processes*. 

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3.3.3 Mutual Exclusion

While a shared address space simplifies exchange of data, interlocking access to memory is crucial to avoid contention. Many methods exist for obtaining exclusive access to resources, and vary only in the scope of the exclusion. Such methods include disabling interrupts, disabling preemption, and resource locking with semaphores.

For information about POSIX mutexes, see 4.12 POSIX Mutexes and Condition Variables, p.254.

Interrupt Locks and Latency

The most powerful method available for mutual exclusion is the disabling of interrupts. Such a lock guarantees exclusive access to the CPU:

```c
funcA ()
{
    int lock = intLock();
    /* critical region of code that cannot be interrupted */
    intUnlock (lock);
}
```

While this solves problems involving mutual exclusion with ISRs, it is inappropriate as a general-purpose mutual-exclusion method for most real-time systems, because it prevents the system from responding to external events for the duration of these locks. Interrupt latency is unacceptable whenever an immediate
response to an external event is required. However, interrupt locking can sometimes be necessary where mutual exclusion involves ISRs. In any situation, keep the duration of interrupt lockouts short.

![WARNING:](image1) Do not call VxWorks system routines with interrupts locked. Violating this rule may re-enable interrupts unpredictably.

### Preemptive Locks and Latency

Disabling preemption offers a somewhat less restrictive form of mutual exclusion. While no other task is allowed to preempt the current executing task, ISRs are able to execute:

```c
funcA ()
{
    taskLock ();
    // /* critical region of code that cannot be interrupted */
    taskUnlock ();
}
```

However, this method can lead to unacceptable real-time response. Tasks of higher priority are unable to execute until the locking task leaves the critical region, even though the higher-priority task is not itself involved with the critical region. While this kind of mutual exclusion is simple, if you use it, be sure to keep the duration short. Semaphores provide a better mechanism; see 3.3.4 Semaphores, p.162.

![WARNING:](image2) The critical region code should not block. If it does, preemption could be re-enabled.
3.3.4 Semaphores

VxWorks semaphores are highly optimized and provide the fastest intertask communication mechanism in VxWorks. Semaphores are the primary means for addressing the requirements of both mutual exclusion and task synchronization, as described below:

- For mutual exclusion, semaphores interlock access to shared resources. They provide mutual exclusion with finer granularity than either interrupt disabling or preemptive locks, discussed in 3.3.3 Mutual Exclusion, p.160.
- For synchronization, semaphores coordinate a task’s execution with external events.

There are three types of VxWorks semaphores, optimized to address different classes of problems:

- **binary**  
  The fastest, most general-purpose semaphore. Optimized for synchronization or mutual exclusion.

- **mutual exclusion**  
  A special binary semaphore optimized for problems inherent in mutual exclusion: priority inheritance, deletion safety, and recursion.

- **counting**  
  Like the binary semaphore, but keeps track of the number of times a semaphore is given. Optimized for guarding multiple instances of a resource.

VxWorks semaphores can be created as private objects, which are accessible only within the memory space in which they were created (kernel or process); or as public objects, which accessible throughout the system. For more information, see 3.3.1 Public and Private Objects, p.157.

VxWorks provides not only the semaphores designed expressly for VxWorks, but also POSIX semaphores, designed for portability. An alternate semaphore library provides the POSIX-compatible semaphore interface; see 4.11 POSIX Semaphores, p.245.

The semaphores described here are for use on a single CPU. The optional product VxMP provides semaphores that can be used across processors; see 12. Shared-Memory Objects: VxMP.
Semaphore Control

Instead of defining a full set of semaphore control routines for each type of semaphore, the VxWorks semaphores provide a single uniform interface for semaphore control. Only the creation routines are specific to the semaphore type. Table 3-12 lists the semaphore control routines.

<table>
<thead>
<tr>
<th>Call</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>semBCreate()</td>
<td>Allocates and initializes a binary semaphore.</td>
</tr>
<tr>
<td>semMCreate()</td>
<td>Allocates and initializes a mutual-exclusion semaphore.</td>
</tr>
<tr>
<td>semCCreate()</td>
<td>Allocates and initializes a counting semaphore.</td>
</tr>
<tr>
<td>semDelete()</td>
<td>Terminates and frees a semaphore.</td>
</tr>
<tr>
<td>semTake()</td>
<td>Takes a semaphore.</td>
</tr>
<tr>
<td>semGive()</td>
<td>Gives a semaphore.</td>
</tr>
<tr>
<td>semFlush()</td>
<td>Unblocks all tasks that are waiting for a semaphore.</td>
</tr>
</tbody>
</table>

The semBCreate(), semMCreate(), and semCCreate() routines return a semaphore ID that serves as a handle on the semaphore during subsequent use by the other semaphore-control routines. When a semaphore is created, the queue type is specified. Tasks pending on a semaphore can be queued in priority order (SEM_Q_PRIORITY) or in first-in first-out order (SEM_Q_FIFO).

⚠️ WARNING: The semDelete() call terminates a semaphore and deallocates all associated memory. Take care when deleting semaphores, particularly those used for mutual exclusion, to avoid deleting a semaphore that another task still requires. Do not delete a semaphore unless the same task first succeeds in taking it.

Binary Semaphores

The general-purpose binary semaphore is capable of addressing the requirements of both forms of task coordination: mutual exclusion and synchronization. The binary semaphore has the least overhead associated with it, making it particularly applicable to high-performance requirements. The mutual-exclusion semaphore described in Mutual-Exclusion Semaphores, p. 167 is also a binary semaphore, but it
has been optimized to address problems inherent to mutual exclusion. Alternatively, the binary semaphore can be used for mutual exclusion if the advanced features of the mutual-exclusion semaphore are deemed unnecessary.

A binary semaphore can be viewed as a flag that is available (full) or unavailable (empty). When a task takes a binary semaphore, with \texttt{semTake()}, the outcome depends on whether the semaphore is available (full) or unavailable (empty) at the time of the call; see Figure 3-9. If the semaphore is available (full), the semaphore becomes unavailable (empty) and the task continues executing immediately. If the semaphore is unavailable (empty), the task is put on a queue of blocked tasks and enters a state of pending on the availability of the semaphore.

\textbf{Figure 3-9} \hspace{1em} \textbf{Taking a Semaphore}

When a task gives a binary semaphore, using \texttt{semGive()}, the outcome also depends on whether the semaphore is available (full) or unavailable (empty) at the time of the call; see Figure 3-10. If the semaphore is already available (full), giving the semaphore has no effect at all. If the semaphore is unavailable (empty) and no task is waiting to take it, then the semaphore becomes available (full). If the semaphore is unavailable (empty) and one or more tasks are pending on its availability, then the first task in the queue of blocked tasks is unblocked, and the semaphore is left unavailable (empty).
Binary semaphores interlock access to a shared resource efficiently. Unlike disabling interrupts or preemptive locks, binary semaphores limit the scope of the mutual exclusion to only the associated resource. In this technique, a semaphore is created to guard the resource. Initially the semaphore is available (full).

```c
/* includes */
#include <vxWorks.h>
#include <semLib.h>

SEM_ID semMutex;

/* Create a binary semaphore that is initially full. Tasks *
 * blocked on semaphore wait in priority order. */
semMutex = semBCreate (SEM_Q_PRIORITY, SEM_FULL);
```

When a task wants to access the resource, it must first take that semaphore. As long as the task keeps the semaphore, all other tasks seeking access to the resource are blocked from execution. When the task is finished with the resource, it gives back the semaphore, allowing another task to use the resource.

Thus, all accesses to a resource requiring mutual exclusion are bracketed with `semTake()` and `semGive()` pairs:

```c
semTake (semMutex, WAIT_FOREVER);

// /* critical region, only accessible by a single task at a time */
semGive (semMutex);
```
Synchronization

When used for task synchronization, a semaphore can represent a condition or event that a task is waiting for. Initially, the semaphore is unavailable (empty). A task or ISR signals the occurrence of the event by giving the semaphore. Another task waits for the semaphore by calling \texttt{semTake()}. The waiting task blocks until the event occurs and the semaphore is given.

(See \textit{3.5 Interrupt Service Routines}, p.209 for a complete discussion of ISRs)

Note the difference in sequence between semaphores used for mutual exclusion and those used for synchronization. For mutual exclusion, the semaphore is initially full, and each task first takes, then gives back the semaphore. For synchronization, the semaphore is initially empty, and one task waits to take the semaphore given by another task.

In Example 3-2, the \texttt{init()} routine creates the binary semaphore, attaches an ISR to an event, and spawns a task to process the event. The routine \texttt{task1()} runs until it calls \texttt{semTake()}. It remains blocked at that point until an event causes the ISR to call \texttt{semGive()}. When the ISR completes, \texttt{task1()} executes to process the event. There is an advantage of handling event processing within the context of a dedicated task: less processing takes place at interrupt level, thereby reducing interrupt latency. This model of event processing is recommended for real-time applications.

\begin{example}{Using Semaphores for Task Synchronization}
/* This example shows the use of semaphores for task synchronization. */

/* includes */
#include <vxWorks.h>
#include <semLib.h>
#include <arch/ivarch.h> /* replace arch with architecture type */

SEM_ID syncSem;  /* ID of sync semaphore */

init (int someIntNum)
{
    /* connect interrupt service routine */
    intConnect (INUM_TO_IVEC (someIntNum), eventInterruptSvcRout, 0);

    /* create semaphore */
    syncSem = semBCreate (SEM_Q_FIFO, SEM_EMPTY);

    /* spawn task used for synchronization. */
    taskSpawn ("sample", 100, 0, 20000, task1, 0,0,0,0,0,0,0,0,0,0);
}
\end{example}
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```c
3

3

task1 (void)
{

   semTake (syncSem, WAIT_FOREVER); /* wait for event to occur */
   printf("task 1 got the semaphore\n");
   ... /* process event */
}

eventInterruptSvcRout (void)
{

   semGive (syncSem); /* let task 1 process event */
   ...
}

Broadcast synchronization allows all processes that are blocked on the same semaphore to be unblocked atomically. Correct application behavior often requires a set of tasks to process an event before any task of the set has the opportunity to process further events. The routine semFlush() addresses this class of synchronization problem by unblocking all tasks pended on a semaphore.

Mutual-Exclusion Semaphores

The mutual-exclusion semaphore is a specialized binary semaphore designed to address issues inherent in mutual exclusion, including priority inversion, deletion safety, and recursive access to resources.

The fundamental behavior of the mutual-exclusion semaphore is identical to the binary semaphore, with the following exceptions:

- It can be used only for mutual exclusion.
- It can be given only by the task that took it.
- It cannot be given from an ISR.
- The semFlush() operation is illegal.

Priority Inversion

Figure 3-11 illustrates a situation called priority inversion.
```
Priority inversion arises when a higher-priority task is forced to wait an indefinite period of time for a lower-priority task to complete. Consider the scenario in Figure 3-11: t1, t2, and t3 are tasks of high, medium, and low priority, respectively. t3 has acquired some resource by taking its associated binary guard semaphore. When t1 preempts t3 and contends for the resource by taking the same semaphore, it becomes blocked. If we could be assured that t1 would be blocked no longer than the time it normally takes t3 to finish with the resource, there would be no problem because the resource cannot be preempted. However, the low-priority task is vulnerable to preemption by medium-priority tasks (like t2), which could inhibit t3 from relinquishing the resource. This condition could persist, blocking t1 for an indefinite period of time.

The mutual-exclusion semaphore has the option SEM_INVERSION_SAFE, which enables a priority-inheritance algorithm. The priority-inheritance protocol assures that a task that holds a resource executes at the priority of the highest-priority task blocked on that resource. Once the task priority has been elevated, it remains at the higher level until all mutual-exclusion semaphores that have contributed to the tasks elevated priority are released. Hence, the inheriting task is protected from preemption by any intermediate-priority tasks. This option must be used in conjunction with a priority queue (SEM_Q_PRIORITY).
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In Figure 3-12, priority inheritance solves the problem of priority inversion by elevating the priority of \( t_3 \) to the priority of \( t_1 \) during the time \( t_1 \) is blocked on the semaphore. This protects \( t_3 \), and indirectly \( t_1 \), from preemption by \( t_2 \).

The following example creates a mutual-exclusion semaphore that uses the priority inheritance algorithm:

\[
\text{semId} = \text{semMCreate} (\text{SEM\_Q\_PRIORITY} \mid \text{SEM\_INVERSION\_SAFE});
\]

Deletion Safety

Another problem of mutual exclusion involves task deletion. Within a critical region guarded by semaphores, it is often desirable to protect the executing task from unexpected deletion. Deleting a task executing in a critical region can be catastrophic. The resource might be left in a corrupted state and the semaphore guarding the resource left unavailable, effectively preventing all access to the resource.

The primitives \text{taskSafe()} and \text{taskUnsafe()} provide one solution to task deletion. However, the mutual-exclusion semaphore offers the option \text{SEM\_DELETE\_SAFE}, which enables an implicit \text{taskSafe()} with each \text{semTake()}, and a \text{taskUnsafe()} with each \text{semGive()}. In this way, a task can be protected from deletion while it
has the semaphore. This option is more efficient than the primitives \texttt{taskSafe()}
and \texttt{taskUnsafe()}, as the resulting code requires fewer entrances to the kernel.

\begin{verbatim}
semId = semMCreate (SEM_Q_FIFO | SEM_DELETE_SAFE);
\end{verbatim}

\section*{Recursive Resource Access}

Mutual-exclusion semaphores can be taken \textit{recursively}. This means that the
semaphore can be taken more than once by the task that holds it before finally
being released. Recursion is useful for a set of routines that must call each other but
that also require mutually exclusive access to a resource. This is possible because
the system keeps track of which task currently holds the mutual-exclusion
semaphore.

Before being released, a mutual-exclusion semaphore taken recursively must be
given the same number of times it is \textit{taken}. This is tracked by a count that
increments with each \texttt{semTake()} and decrements with each \texttt{semGive()}.

\begin{example}
\textbf{Recursive Use of a Mutual-Exclusion Semaphore}

\begin{verbatim}
/* Function A requires access to a resource which it acquires by taking
 * mySem;
 * Function A may also need to call function B, which also requires mySem:
 */

/* includes */
#include <vxWorks.h>
#include <semLib.h>
SEM_ID mySem;

/* Create a mutual-exclusion semaphore. */

init ()
{
    mySem = semMCreate (SEM_Q_PRIORITY);
}

funcA ()
{
    semTake (mySem, WAIT_FOREVER);
    printf ("funcA: Got mutual-exclusion semaphore\n");
    ...
    funcB ();
    ...
    semGive (mySem);
    printf ("funcA: Released mutual-exclusion semaphore\n");
}
\end{verbatim}
\end{example}
funcB ()
{
    semTake (mySem, WAIT_FOREVER);
    printf ("funcB: Got mutual-exclusion semaphore\n");
    ...
    semGive (mySem);
    printf ("funcB: Releases mutual-exclusion semaphore\n");
}

Counting Semaphores

Counting semaphores are another means to implement task synchronization and mutual exclusion. The counting semaphore works like the binary semaphore except that it keeps track of the number of times a semaphore is given. Every time a semaphore is given, the count is incremented; every time a semaphore is taken, the count is decremented. When the count reaches zero, a task that tries to take the semaphore is blocked. As with the binary semaphore, if a semaphore is given and a task is blocked, it becomes unblocked. However, unlike the binary semaphore, if a semaphore is given and no tasks are blocked, then the count is incremented. This means that a semaphore that is given twice can be taken twice without blocking. Table 3-13 shows an example time sequence of tasks taking and giving a counting semaphore that was initialized to a count of 3.

Table 3-13  Counting Semaphore Example

<table>
<thead>
<tr>
<th>Semaphore Call</th>
<th>Count after Call</th>
<th>Resulting Behavior</th>
</tr>
</thead>
<tbody>
<tr>
<td>semCCreate()</td>
<td>3</td>
<td>Semaphore initialized with an initial count of 3.</td>
</tr>
<tr>
<td>semTake()</td>
<td>2</td>
<td>Semaphore taken.</td>
</tr>
<tr>
<td>semTake()</td>
<td>1</td>
<td>Semaphore taken.</td>
</tr>
<tr>
<td>semTake()</td>
<td>0</td>
<td>Semaphore taken.</td>
</tr>
<tr>
<td>semTake()</td>
<td>0</td>
<td>Task blocks waiting for semaphore to be available.</td>
</tr>
<tr>
<td>semGive()</td>
<td>0</td>
<td>Task waiting is given semaphore.</td>
</tr>
<tr>
<td>semGive()</td>
<td>1</td>
<td>No task waiting for semaphore; count incremented.</td>
</tr>
</tbody>
</table>

Counting semaphores are useful for guarding multiple copies of resources. For example, the use of five tape drives might be coordinated using a counting semaphore with an initial count of 5, or a ring buffer with 256 entries might be
implemented using a counting semaphore with an initial count of 256. The initial count is specified as an argument to the `semCCreate()` routine.

**Special Semaphore Options**

The uniform VxWorks semaphore interface includes three special options. These options are not available for the POSIX-compatible semaphores described in 4.11 POSIX Semaphores, p.245.

**Timeouts**

As an alternative to blocking until a semaphore becomes available, semaphore take operations can be restricted to a specified period of time. If the semaphore is not taken within that period, the take operation fails.

This behavior is controlled by a parameter to `semTake()` that specifies the amount of time in ticks that the task is willing to wait in the pended state. If the task succeeds in taking the semaphore within the allotted time, `semTake()` returns OK. The `errno` set when a `semTake()` returns ERROR due to timing out before successfully taking the semaphore depends upon the timeout value passed.

A `semTake()` with NO_WAIT (0), which means do not wait at all, sets `errno` to `S_objLib_OBJ_UNAVAILABLE`. A `semTake()` with a positive timeout value returns `S_objLib_OBJ_TIMEOUT`. A timeout value of WAIT_FOREVER (-1) means wait indefinitely.

**Queues**

VxWorks semaphores include the ability to select the queuing mechanism employed for tasks blocked on a semaphore. They can be queued based on either of two criteria: first-in first-out (FIFO) order, or priority order; see Figure 3-13.
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Priority ordering better preserves the intended priority structure of the system at the expense of some overhead in `semTake()` in sorting the tasks by priority. A FIFO queue requires no priority sorting overhead and leads to constant-time performance. The selection of queue type is specified during semaphore creation with `semBCreate()`, `semMCreate()`, or `semCCreate()`. Semaphores using the priority inheritance option (`SEM_INVERSION_SAFE`) must select priority-order queuing.

### Semaphores and VxWorks Events

Semaphores can send VxWorks events to a specified task when they becomes free. For more information, see 3.3.7 VxWorks Events, p.179.

### 3.3.5 Message Queues

Modern real-time applications are constructed as a set of independent but cooperating tasks. While semaphores provide a high-speed mechanism for the synchronization and interlocking of tasks, often a higher-level mechanism is necessary to allow cooperating tasks to communicate with each other. In VxWorks, the primary intertask communication mechanism within a single CPU is message queues.
For information about socket-based message communication across memory spaces (kernel and processes), and between multiple nodes, see 3.3.8 Message Channels, p. 186.

Message queues allow a variable number of messages, each of variable length, to be queued. Tasks and ISRs can send messages to a message queue, and tasks can receive messages from a message queue.

Multiple tasks can send to and receive from the same message queue. Full-duplex communication between two tasks generally requires two message queues, one for each direction; see Figure 3-14.

VxWorks message queues can be created as private objects, which accessible only within the memory space in which they were created (process or kernel); or as public objects, which accessible throughout the system. For more information, see 3.3.1 Public and Private Objects, p. 157.

There are two message-queue subroutine libraries in VxWorks. The first of these, msgQLib, provides VxWorks message queues, designed expressly for VxWorks; the second, mqPxLib, is compatible with the POSIX standard (1003.1b) for real-time extensions. See 4.10.1 Comparison of POSIX and VxWorks Scheduling, p. 236 for a discussion of the differences between the two message-queue designs.

**VxWorks Message Queues**

VxWorks message queues are created, used, and deleted with the routines shown in Table 3-14. This library provides messages that are queued in FIFO order, with
a single exception: there are two priority levels, and messages marked as high priority are attached to the head of the queue.

Table 3-14  VxWorks Message Queue Control

<table>
<thead>
<tr>
<th>Call</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>msgQCreate()</td>
<td>Allocates and initializes a message queue.</td>
</tr>
<tr>
<td>msgQDelete()</td>
<td>Terminates and frees a message queue.</td>
</tr>
<tr>
<td>msgQSend()</td>
<td>Sends a message to a message queue.</td>
</tr>
<tr>
<td>msgQReceive()</td>
<td>Receives a message from a message queue.</td>
</tr>
</tbody>
</table>

A message queue is created with msgQCreate(). Its parameters specify the maximum number of messages that can be queued in the message queue and the maximum length in bytes of each message. Enough buffer space is allocated for the specified number and length of messages.

A task or ISR sends a message to a message queue with msgQSend(). If no tasks are waiting for messages on that queue, the message is added to the queue’s buffer of messages. If any tasks are already waiting for a message from that message queue, the message is immediately delivered to the first waiting task.

A task receives a message from a message queue with msgQReceive(). If messages are already available in the message queue’s buffer, the first message is immediately dequeued and returned to the caller. If no messages are available, then the calling task blocks and is added to a queue of tasks waiting for messages. This queue of waiting tasks can be ordered either by task priority or FIFO, as specified in an option parameter when the queue is created.

Timeouts

Both msgQSend() and msgQReceive() take timeout parameters. When sending a message, the timeout specifies how many ticks to wait for buffer space to become available, if no space is available to queue the message. When receiving a message, the timeout specifies how many ticks to wait for a message to become available, if no message is immediately available. As with semaphores, the value of the timeout parameter can have the special values of NO_WAIT (0), meaning always return immediately, or WAIT_FOREVER (-1), meaning never time out the routine.
Urgent Messages

The `msgQSend()` function allows specification of the priority of the message as either normal (`MSG_PRI_NORMAL`) or urgent (`MSG_PRI_URGENT`). Normal priority messages are added to the tail of the list of queued messages, while urgent priority messages are added to the head of the list.

Example 3-4  VxWorks Message Queues

```c
/* In this example, task t1 creates the message queue and sends a message * to task t2. Task t2 receives the message from the queue and simply * displays the message. */

/* includes */
#include <vxWorks.h>
#include <msgQLib.h>

/* defines */
#define MAX_MSGS (10)
#define MAX_MSG_LEN (100)

MSG_Q_ID myMsgQId;

task2 (void)
{
    char msgBuf[MAX_MSG_LEN];

    /* get message from queue; if necessary wait until msg is available */
    if (msgQReceive(myMsgQId, msgBuf, MAX_MSG_LEN, WAIT_FOREVER) == ERROR)
        return (ERROR);

    /* display message */
    printf ("Message from task 1: \n%s\n", msgBuf);
}

#define MESSAGE "Greetings from Task 1"
task1 (void)
{
    /* create message queue */
    if ((myMsgQId = msgQCreate (MAX_MSGS, MAX_MSG_LEN, MSG_Q_PRIORITY))
        == NULL)
        return (ERROR);

    /* send a normal priority message, blocking if queue is full */
    if (msgQSend (myMsgQId, MESSAGE, sizeof (MESSAGE), WAIT_FOREVER,
        MSG_PRI_NORMAL) == ERROR)
        return (ERROR);
}
```

```c
/* In this example, task t1 creates the message queue and sends a message *
* to task t2. Task t2 receives the message from the queue and simply *
* displays the message. */

/* includes */
#include <vxWorks.h>
#include <msgQLib.h>

/* defines */
#define MAX_MSGS (10)
#define MAX_MSG_LEN (100)

MSG_Q_ID myMsgQId;

task2 (void)
{
    char msgBuf[MAX_MSG_LEN];

    /* get message from queue; if necessary wait until msg is available */
    if (msgQReceive(myMsgQId, msgBuf, MAX_MSG_LEN, WAIT_FOREVER) == ERROR)
        return (ERROR);

    /* display message */
    printf ("Message from task 1: \n%s\n", msgBuf);
}

#define MESSAGE "Greetings from Task 1"
task1 (void)
{
    /* create message queue */
    if ((myMsgQId = msgQCreate (MAX_MSGS, MAX_MSG_LEN, MSG_Q_PRIORITY))
        == NULL)
        return (ERROR);

    /* send a normal priority message, blocking if queue is full */
    if (msgQSend (myMsgQId, MESSAGE, sizeof (MESSAGE), WAIT_FOREVER,
        MSG_PRI_NORMAL) == ERROR)
        return (ERROR);
}
```
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Queuing

VxWorks message queues include the ability to select the queuing mechanism employed for tasks blocked on a message queue. The MSG_Q_FIFO and MSG_Q_PRIORITY options are provided to specify (to the msgQCreate() and msgQOpen() routines) the queuing mechanism that should be used for tasks that pend on msgQSend() and msgQReceive().

Displaying Message Queue Attributes

The VxWorks show() command produces a display of the key message queue attributes, for either kind of message queue. For example, if myMsgQId is a VxWorks message queue, the output is sent to the standard output device, and looks like the following from the shell (using the C interpreter):

```
-> show myMsgQId
Message Queue Id : 0x3adaf0
Task Queuing : FIFO
Message Byte Len : 4
Messages Max : 30
Messages Queued : 14
Send timeouts : 0
Receive timeouts : 0
```

Servers and Clients with Message Queues

Real-time systems are often structured using a client-server model of tasks. In this model, server tasks accept requests from client tasks to perform some service, and usually return a reply. The requests and replies are usually made in the form of intertask messages. In VxWorks, message queues or pipes (see 3.3.6 Pipes, p.178) are a natural way to implement this functionality.

For example, client-server communications might be implemented as shown in Figure 3-15. Each server task creates a message queue to receive request messages from clients. Each client task creates a message queue to receive reply messages from servers. Each request message includes a field containing the msgQId of the client’s reply message queue. A server task’s main loop consists of reading request messages from its request message queue, performing the request, and sending a reply to the client’s reply message queue.
The same architecture can be achieved with pipes instead of message queues, or by other means that are tailored to the needs of the particular application.

**Message Queues and VxWorks Events**

Message queues can send VxWorks events to a specified task when a message arrives on the queue and no task is waiting on it. For more information, see 3.3.7 *VxWorks Events*, p.179.

### 3.3.6 Pipes

Pipes provide an alternative interface to the message queue facility that goes through the VxWorks I/O system. Pipes are virtual I/O devices managed by the driver `pipeDrv`. The routine `pipeDevCreate()` creates a pipe device and the underlying message queue associated with that pipe. The call specifies the name of the created pipe, the maximum number of messages that can be queued to it, and the maximum length of each message:
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```
status = pipeDevCreate("/pipe/name", max_msgs, max_length);
```

The created pipe is a normally named I/O device. Tasks can use the standard I/O routines to open, read, and write pipes, and invoke `ioctl` routines. As they do with other I/O devices, tasks block when they read from an empty pipe until data is available, and block when they write to a full pipe until there is space available.

Like message queues, ISRs can write to a pipe, but cannot read from a pipe.

As I/O devices, pipes provide one important feature that message queues cannot—the ability to be used with `select()`. This routine allows a task to wait for data to be available on any of a set of I/O devices. The `select()` routine also works with other asynchronous I/O devices including network sockets and serial devices. Thus, by using `select()`, a task can wait for data on a combination of several pipes, sockets, and serial devices; see 6.3.9 Pending on Multiple File Descriptors: The Select Facility, p.331.

Pipes allow you to implement a client-server model of intertask communications; see Servers and Clients with Message Queues, p.177.

3.3.7 VxWorks Events

VxWorks events provide a means of communication and synchronization between tasks and other tasks, interrupt service routines (ISRs) and tasks, semaphores and tasks, and message queues and tasks.

Events can be used as a lighter-weight alternative to binary semaphores for task-to-task and ISR-to-task synchronization (because no object needs to be created). They can also be used to notify a task that a semaphore has become available, or that a message has arrived on a message queue.

The events facility provides a mechanism for coordinating the activity of a task using up to thirty-two `events` that can be sent to it by other tasks, ISRs, semaphores, and message queues. A task can wait on multiple events from multiple sources. Events thereby provide a means for coordination of complex matrix of activity without allocation of additional system resources.

Each task has 32 event flags, bit-wise encoded in a 32-bit word (bits 25 to 32 are reserved for Wind River use). These flags are stored in the task’s `event register`. Note that an event flag itself has no intrinsic meaning. The significance of each of the 32 event flags depends entirely on how any given task is coded to respond to their

---

1. VxWorks events are based on pSOS operating system events. VxWorks introduced functionality similar to pSOS events (but with enhancements) with the VxWorks 5.5 release.
being set. There is no mechanism for recording how many times any given event has been received by a task. Once a flag has been set, its being set again by the same or a different sender is essentially an invisible operation.

Events are similar to signals in that they are sent to a task asynchronously; but differ in that receipt is synchronous. That is, the receiving task must call a routine to receive at will, and can choose to pend while waiting for events to arrive. Unlike signals, therefore, events do not require a handler.

For a code example of how events can be used, see the eventLib API reference.

NOTE: VxWorks events, which are also simply referred to as events in this section, should not be confused with System Viewer events.

Configuring VxWorks for Events

To provide events facilities, VxWorks must be configured with the INCLUDE_VXEVENTS component.

Preparing a Task to Receive Events

A task can pend on one or more events, or simply check on which events have been received, with a call to eventReceive(). The routine specifies which events to wait for, and provides options for waiting for one or all of those events. It also provides various options for how to manage unsolicited events.

In order for a task to receive events from a semaphore or a message queue, however, it must first register with the specific object, using semEvStart() for a semaphore or msgQEvStart() for a message queue. Only one task can be registered with any given semaphore or message queue at a time.

The semEvStart() routine identifies the semaphore and the events that it should send to the task when the semaphore is free. It also provides a set of options to specify whether the events are sent only the first time the semaphore is free, or each time; whether to send events if the semaphore is free at the time of registration; and whether a subsequent semEvStart() call from another task is allowed to take effect (and to unregister the previously registered task).

Once a task has registered with a semaphore, every time the semaphore is released with semGive(), and as long as no other tasks are pending on it, the semaphore sends events to the registered task.

To request that the semaphore stop sending events to it, the registered task calls semEvStop().
Registration with a message queue is similar to registration with a semaphore. The `msgQEvStart()` routine identifies the message queue and the events that it should send to the task when a message arrives and no tasks are pending on it. It provides a set of options to specify whether the events are sent only the first time a message is available, or each time; whether a subsequent call to `msgQEvStart()` from another task is allowed to take effect (and to unregister the previously registered task).

Once a task has registered with a message queue, every time the message queue receives a message and there are no tasks pending on it, the message queue sends events to the registered task.

To request that the message queue stop sending events to it, the registered task calls `msgQEvStop()`.

### Sending Events to a Task

Tasks and ISRs can send specific events to a task using `eventSend()`, whether or not the receiving task is prepared to make use of them.

Semaphores and message queues send events automatically to tasks that have registered for notification with `semEvStart()` or `msgQEvStart()`, respectively. These objects send events when they are free. The conditions under which objects are free are as follows:

**Mutex Semaphore**

A mutex semaphore is considered free when it no longer has an owner and no task is pending on it. For example, following a call to `semGive()`, the semaphore will not send events if another task is pending on a `semTake()` for the same semaphore.

**Binary Semaphore**

A binary semaphore is considered free when no task owns it and no task is waiting for it.

**Counting Semaphore**

A counting semaphore is considered free when its count is nonzero and no task is pending on it. Events cannot, therefore, be used as a mechanism to compute the number of times a semaphore is released or given.

**Message Queue**

A message queue is considered free when a message is present in the queue and no task is pending for the arrival of a message in that queue. Events
cannot, therefore, be used as a mechanism to compute the number of messages sent to a message queue.

Note that just because an object has been released does not mean that it is free. For example, if a semaphore is given, it is released; but it is not free if another task is waiting for it at the time it is released. When two or more tasks are constantly exchanging ownership of an object, it is therefore possible that the object never becomes free, and never sends events.

Also note that when a semaphore or message queue sends events to a task to indicate that it is free, it does not mean that the object is in any way reserved for the task. A task waiting for events from an object unpends when the resource becomes free, but the object may be taken in the interval between notification and unpending. The object could be taken by a higher priority task if the task receiving the event was pended in `eventReceive()`. Or a lower priority task might steal the object: if the task receiving the event was pended in some routine other than `eventReceive()`, a low priority task could execute and (for example) perform a `semTake()` after the event is sent, but before the receiving task unpends from the blocking call. There is, therefore, no guarantee that the resource will still be available when the task subsequently attempts to take ownership of it.

**WARNING:** Because events cannot be reserved for an application in any way, care should be taken to ensure that events are used uniquely and unambiguously. Note that events 25 to 32 (VXEV25 to VXEV32) are reserved for Wind River’s use, and should not be used by customers. Third parties should be sure to document their use of events so that their customers do not use the same ones for their applications.

### Events and Object Deletion

If a semaphore or message queue is deleted while a task is waiting for events from it, the task is automatically unpended by the `semDelete()` or `msgQDelete()` implementation. This prevents the task from pending indefinitely while waiting for events from an object that has been deleted. The pending task then returns to the ready state (just as if it were pending on the semaphore itself) and receives an ERROR return value from the `eventReceive()` call that caused it to pend initially.

If, however, the object is deleted between a tasks’ registration call and its `eventReceive()` call, the task pends anyway. For example, if a semaphore is deleted while the task is between the `semEvStart()` and `eventReceive()` calls, the task pends in `eventReceive()`, but the event is never sent. It is important, therefore, to use a timeout other than `WAIT_FOREVER` when object deletion is expected.
Events and Task Deletion

If a task is deleted before a semaphore or message queue sends events to it, the events can still be sent, but are obviously not received. By default, VxWorks handles this event-delivery failure silently.

It can, however, be useful for an application that created an object to be informed when events were not received by the (now absent) task that registered for them. In this case, semaphores and message queues can be created with an option that causes an error to be returned if event delivery fails (the SEM_EVENTSEND_ERROR_NOTIFY and MSG_Q_EVENTSEND_ERROR_NOTIFY options, respectively). The semGive() or msgQSend() call then returns ERROR when the object becomes free.

The error does not mean the semaphore was not given or that the message was not properly delivered. It simply means the resource could not send events to the registered task. Note that a failure to send a message or give a semaphore takes precedence over an events failure.

Accessing Event Flags

When events are sent to a task, they are stored in the task’s events register (see Task Events Register, p.184), which is not directly accessible to the task itself.

When the events specified with an eventReceive() call have been received and the task unpends, the contents of the events register is copied to a variable that is accessible to the task.

When eventReceive() is used with the EVENTS_WAIT_ANY option—which means that the task unpends for the first of any of the specified events that it receives—the contents of the events variable can be checked to determine which event caused the task to unpend.

The eventReceive() routine also provides an option that allows for checking which events have been received prior to the full set being received.
Events Routines

The routines used for working with events are listed in Table 3-15.

Table 3-15  Events Routines

<table>
<thead>
<tr>
<th>Routine</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>eventSend()</td>
<td>Sends specified events to a task.</td>
</tr>
<tr>
<td>eventReceive()</td>
<td>Pends a task until the specified events have been received. Can also be used to check what events have been received in the interim.</td>
</tr>
<tr>
<td>eventClear()</td>
<td>Clears the calling task’s event register.</td>
</tr>
<tr>
<td>semEvStart()</td>
<td>Registers a task to be notified of semaphore availability.</td>
</tr>
<tr>
<td>semEvStop()</td>
<td>Unregisters a task that had previously registered for notification of semaphore availability.</td>
</tr>
<tr>
<td>msgQEvStart()</td>
<td>Registers a task to be notified of message arrival on a message queue when no recipients are pending.</td>
</tr>
<tr>
<td>msgQEvStop()</td>
<td>Unregisters a task that had previously registered for notification of message arrival on a message queue.</td>
</tr>
</tbody>
</table>

For more information about these routines, see the VxWorks API references for eventLib, semEvLib, and msgQEvLib.

Task Events Register

Each task has its own task events register. The task events register is a 32-bit field used to store the events that the task receives from other tasks (or itself), ISRs, semaphores, and message queues.

Events 25 to 32 (VXE25 or 0x01000000 to VXEV32 or 0x80000000) are reserved for Wind River use only, and should not be used by customers.

As noted above (Accessing Event Flags, p.183), a task cannot access the contents of its events registry directly.

Table 3-16 describes the routines that affect the contents of the events register.
Show Routines and Events

For the purpose of debugging systems that make use of events, the taskShow, semShow, and msgQShow libraries display event information.

The taskShow library displays the following information:
- the contents of the event register
- the desired events
- the options specified when eventReceive() was called

The semShow and msgQShow libraries display the following information:
- the task registered to receive events
- the events the resource is meant to send to that task
- the options passed to semEvStart() or msgQEvStart()
3.3.8 Message Channels

Message channels are a socket-based facility that provides for inter-process communication across memory boundaries on a single node (processor), as well as for inter-process communication between multiple nodes (multi-processor). That is, message channel communications can take place between tasks running in the kernel and tasks running in processes (RTPs) on a single node, as well as between multiple nodes, regardless of the memory context in which the tasks are running. For example, message channels can be used to communicate between:

- a task in the kernel and a task in a process on a single node
- a task in one process and a task in another process on a single node
- a task in the kernel of one node and a task in a process on another node
- a task in a process on one node and a task in a process on another node

and so on.

The scope of message channel communication can be configured to limit server access to:

- one memory space on a node (either the kernel or one process)
- all memory spaces on a node (the kernel and all processes)
- a cluster of nodes in a system (including all memory spaces in each node)

Message channels provide a connection-oriented messaging mechanism. Tasks exchange information in the form of messages that can be of variable size and format. They can be passed back and forth in full duplex mode once the connection is established. Message channels can also provide a connection-oriented messaging mechanism between separate nodes in a cluster.

Message Channel Facilities

The message channel technology consists of the following basic facilities:

- The Transparent Inter-Process Communication (TIPC) infrastructure for multi-node communication. See Multi-Node Communication with TIPC, p.190.
- The Socket Name Service (SNS), which provides location and interface transparency for message channel communication between tasks on a single node, and maintains communication between nodes for multi-node message channel communications. In addition, it controls the scope of message channel communications.
communication (to two memory spaces, a node, or a cluster of nodes). See *Socket Name Service*, p. 191.

- The Socket Application Libraries (SAL), which provide APIs for using message channels in applications, as well as the mechanism for registering the tasks that are using a message channel with a Socket Name Service. See *Socket Application Libraries*, p. 195.

Also see *Comparing Message Channels and Message Queues*, p. 200.

### Single-Node Communication with COMP

The underlying transport mechanism for single-node message channels is based on the Connection-Oriented Message Passing protocol (COMP), which provides a fast method for transferring messages across memory boundaries on a single node.

COMP is designed for use with the standard socket API. Because it provides connection-oriented messaging, the socket type associated with message channels is the `SOCK_SEQPACKET`. The protocol is connection-based, like other stream-based protocols such as TCP, but it carries variable-sized messages, like datagram-based protocols such as UDP.

While COMP provides for standard socket support, it has no dependency on TCP/IP networking facilities, which can be left out of a system if the facilities are not otherwise needed.

In providing single-node local communications, COMP sockets are available as part of the `AF_LOCAL` domain. Although this domain is traditionally related to the UNIX file system, in VxWorks the addressing is completely independent of any file system. Like UNIX sockets, COMP uses a string to define the address, and it has a structure similar to a file path name, but this is the extent of the similarity in this regard. The address is simply a logical representation of the end-point.

The transfer of data in message channels is based on an internal buffer management implementation that allows for deterministic memory allocation, which reduces the amount of copies needed to transfer the data whenever possible. Only one copy is needed for the internal transfer; the data coming from the user is directly moved into the receiver buffer space. Another copy is required to submit and retrieve the data to and from the channel.

COMP supports the standard socket options, such as `SO_SNDBUF` or `SO_RECVBUF` and `SO_SNDTIMEO` and `SO_RCVTIME`. For information about the socket options, refer to the socket API references. For information about how
COMP uses them, see
installDir/vxworks-6.x/target/src/dsi/backend/dsiSockLib.c.

Express Messaging

Express messaging is also available for sending and receiving a message. An express message is placed on a special queue on the sending side and placed at the front of the normal queue at the receiving end. This allows for urgent messages to be sent and received with a higher priority than the normal messages. In order to send an express message, the flags parameter of the standard send() routine must have the MSG_EXP bit set. (Also see the socket send() API reference).

Show Routines

Because COMP is based on the standard socket API, traditional network show routines can be used, such as netstat(). In addition, information on local sockets can be retrieved with the unstatShow() routine.

COMP Socket Support with DSI

The COMP socket functional interface is provided by the DSI back end. The back end provides the set of implementations of the standard socket functions for the COMP protocol specific calls. The traditional network protocols in VxWorks, such as TCP and UDP, use the BSD Internet Domain Socket back end and are described in the Wind River Network Stack for VxWorks 6 Programmer’s Guide. The DSI back end is a simplified version of the BSD back-end. It is designed for optimized communications when both end points are in a single node (which is true for COMP).

The DSI back end requires its own system and data memory pools, which are used to handle the creation of sockets and the data transfers between two endpoints. The pools are similar to those required for the network stack. In addition, the pools are configured so as to enhance performance for the local transfers. The system pool provides COMP with the memory it needs for its internal structures and data types. The data pool provides COMP with the memory it needs for receiving data. Because COMP is local, data transfer has been optimized so that data are put directly in the receiver’s packet queue.

Both the DSI back end and DSI memory pools complement the BSD equivalent. Therefore, both BSD and DSI sockets can coexist in the system. They do not depend on each other, so that they can be added or removed, as needed.

COMP uses netBufLib to manage its internal system and data memory pools. For detailed information on how buffers are configured, see the coverage of the similar

These pools are created automatically by the INCLUDE_DSI_POOL component. The DSI parameters listed in Table 3-17 are used for memory pool configuration. These parameters are used when `usrNetDsiPoolConfig()` routine is called, which happens automatically when the system boots. The `dsiSysPoolShow()` and `dsiDataPoolShow()` can be used to display related information (see the VxWorks API reference for `dsiSockLib`).

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Default Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>DSI_NUM_SOCKETS</td>
<td>200</td>
</tr>
<tr>
<td>DSI_DATA_32</td>
<td>50</td>
</tr>
<tr>
<td>DSI_DATA_64</td>
<td>100</td>
</tr>
<tr>
<td>DSI_DATA_128</td>
<td>200</td>
</tr>
<tr>
<td>DSI_DATA_256</td>
<td>40</td>
</tr>
<tr>
<td>DSI_DATA_512</td>
<td>40</td>
</tr>
<tr>
<td>DSI_DATA_1K</td>
<td>10</td>
</tr>
<tr>
<td>DSI_DATA_2K</td>
<td>10</td>
</tr>
<tr>
<td>DSI_DATA_4K</td>
<td>10</td>
</tr>
<tr>
<td>DSI_DATA_8K</td>
<td>10</td>
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<tr>
<td>DSI_DATA_16K</td>
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</tr>
<tr>
<td>DSI_DATA_32K</td>
<td>0</td>
</tr>
<tr>
<td>DSI_DATA_64K</td>
<td>0</td>
</tr>
</tbody>
</table>

The DSI pool is configured more strictly and more efficiently than the core network pool since it is more contained, fewer scenarios are possible, and everything is known in advance (as there is only the one node involved). The `DSI_NUM_SOCKETS` parameter controls the size of the system pool. It controls the number of clusters needed to fit a socket, for each family and each protocol supported by the back end. Currently, only the *AF_LOCAL* address family is supported by COMP.
The clusters allocated in the back end are of these sizes:

- aligned sizeof (struct socket)
- aligned sizeof (struct uncompcb)
- aligned sizeof (struct sockaddr_un)

One cluster of size 328 and of size 36 are needed for each socket that is created since currently, the COMP protocol is always linked to a DSI socket. Only one cluster of sizeof (struct sockaddr_un) is required, therefore the size of the system pool is basically determined by: \( (\text{DSI_NUM_SOCKETS} \times (328 + 36) + 108) \). Using these sizes prevents any loss of space since they are the actual sizes needed.

All other parameters for the DSI pool are used to calculate the size of clusters in the data pool, and at the same time, the size of the pool itself. The data pool is used as packet holders during the transmissions between two sockets, between the time the data is copied from the sender’s buffer to the receiver’s buffer. Each of them represent a cluster size from 32 bytes to 64 kilobytes and the number of allocated clusters of that specific size.

To set reasonable values for the parameters in this component, you need to know how much memory your deployed application will require. There is no simple formula that you can use to anticipate memory usage. Your only real option is to determine memory usage empirically. This means running your application under control of the debugger, pausing the application at critical points in its execution, and monitoring the state of the memory pool. You will need to perform these tests under both stressed and unstressed conditions.

Multi-Node Communication with TIPC

The underlying transport mechanism for multi-node message channels is based on the Transparent Inter-Process Communication (TIPC) protocol, which provides a fast method for transferring messages across node boundaries in a cluster environment. TIPC can also be used within a single node.

TIPC is designed for use with the standard socket API. For connection-oriented messaging, the socket type associated with message channels is the \texttt{SOCK_SEQPACKET}.

The TIPC protocol is connection-based, like other stream-based protocols such as TCP, but it carries variable-sized messages, like datagram-based protocols such as UDP. In providing cluster and node based communications, TIPC sockets are available in the \texttt{AF_TIPC} domain. TIPC provides several means of identifying end points that are handled transparently through the SNS name server. In this release,
TIPC has a dependency on the TCP/IP stack. For more information about TIPC, see the Wind River TIPC for VxWorks 6 Programmer’s Guide.

TIPC Socket Support With BSD

The TIPC socket functionality is provided by the BSD socket back end. In a future VxWorks release, the socket functionality will be provided by a new back end.

Socket Name Service

A Socket Name Service (SNS) allows a server application to associate a service name with a collection of listening sockets, as well as to limit the visibility of the service name to a restricted (but not arbitrary) set of clients.

Both Socket Application Library (SAL) client and server routines make use of an SNS server to establish a connection to a specified service without the client having to be aware of the address of the server’s listening sockets, or the exact interface type being utilized (see Socket Application Libraries, p.195). This provides both location transparency and interface transparency. Such transparency makes it possible to design client and server applications that can operate efficiently without requiring any knowledge of the system’s topology.

An SNS server is a simple database that provides an easy mapping of service names and their associated sockets. The service name has this URL format:

[SNS: service_name [ @scope ]]

The [SNS] prefix is the only prefix accepted, and it can be omitted. The scope can have the following values: private, node, or cluster. These values designate an access scope for limiting access to the same single memory space (the kernel or a process), the same node (the kernel and all processes on that node), and a set of nodes, respectively. A server can be accessed by clients within the scope that is defined when the server is created with the salCreate() routine (see SAL Server Library, p.196).

SNS provides a resource reclamation mechanism for servers created within processes. If a process dies before salDelete() has been called on a SAL server, SNS will be notified and will remove the entry from the database. Note, however, that this mechanism is not available for tasks in the kernel. If a task in the kernel terminates before salDelete() is called, the service name is not automatically removed from SNS. In order to avoid stale entries that may prevent new services with the same name from being created, the salRemove() routine should be used.
The SNS server can be configured to run in either kernel or user space. A node should not be configured with more than one SNS server. The server starts at boot time, and is named \texttt{tSnsServer} if it is running in the kernel, or \texttt{iSnsServer} if it is running as a process. For a multi-node system, a monitoring task is automatically spawned to maintain a list of all the SNS servers in the cluster. The monitoring task is named \texttt{tDsalMonitor}, and it runs in the kernel.

The \texttt{snsShow()} command allows a user to verify that SAL-based services are correctly registered with the SNS server from the shell (see \texttt{snsShow()} Example, p.193).

For more information, see the VxWorks API reference for \texttt{snsLib}.

Multi-Node Socket Name Service

For a multi-node system, a Socket Name Service (SNS) runs on each node that is configured to use SAL. Note that VxWorks SNS components for multi-node use are different from those used on single node systems (see \textit{Configuring VxWorks for Message Channels}, p.198).

When a distributed SNS server starts on a node at boot time, it uses a TIPC bind operation to publish a TIPC port name. This is visible to all other nodes in the cluster. The other existing SNS servers then register the node in their tables of SNS servers. A separate monitoring task (called \texttt{tDsalMonitor}) is started on each node at boot time, which uses the TIPC subscription feature to detect topology-change events such as a new SNS server coming online, or an existing SNS server leaving the cluster.

Note that if the TIPC networking layer does not start up properly at boot time, the distributed SAL system will not initialize itself correctly with TIPC, and the SNS server will work strictly in local mode. The SNS server does not check for a working TIPC layer after the system boots, so that it will not detect the layer if it is subsequently started manually, and the SNS server will continue to run in local mode.

When a new node appears, each SNS server sends a command to that node requesting a full listing of all sockets that are remotely accessible. The SNS server on the new node sends a list of sockets that can be reached remotely. Each time a new socket is created with \texttt{salCreate()} on a node that has a server scope greater than \texttt{node}, this information is sent to all known SNS servers in the cluster. All SNS servers are thereby kept up to date with relevant information. Similarly, when a socket is deleted using the \texttt{salRemove()} function, this information is sent to all known SNS servers in the cluster. The addition and
removal of sockets is an infrequent occurrence in most anticipated uses and should be of minimal impact on network traffic and on the performance of the node.

When the tDsalMonitor task detects that an SNS server has been withdrawn from the system, the local SNS server purges all entries related to the node that is no longer a part of the distributed SNS cluster.

Note that only information on accessible sockets is transmitted to remote SNS servers. While it is acceptable to create an AF_LOCAL socket with cluster scope, this socket will use the COMP protocol which can only be accessed locally. SNS servers on remote nodes will not be informed of the existence of this socket.

On a local node, if a socket name exists in the SNS database in both the AF_LOCAL and AF_TIPC families, when a connection is made to that name using salOpen(), the AF_LOCAL socket will be used.

**snsShow( ) Example**

The snsShow() shell command provides information about all sockets that are accessible from the local node, whether the sockets are local or remote. The command is provided by the VxWorks INCLUDE_SNS_SHOW component.
The following examples illustrate **snsShow()** output from three different nodes in a system.

### From Node <1.1.22>

<table>
<thead>
<tr>
<th>NAME</th>
<th>SCOPE</th>
<th>FAMILY</th>
<th>TYPE</th>
<th>PROTO</th>
<th>ADDR</th>
</tr>
</thead>
<tbody>
<tr>
<td>astronaut_display</td>
<td>clust</td>
<td>LOCAL</td>
<td>SEQPKT</td>
<td>0</td>
<td>/comp/socket/0x5</td>
</tr>
<tr>
<td>ground_control_timestamp</td>
<td>clust</td>
<td>TIPC</td>
<td>SEQPKT</td>
<td>0</td>
<td>* &lt;1.1.25&gt;, 1086717967</td>
</tr>
<tr>
<td>ground_control_weblog</td>
<td>clust</td>
<td>TIPC</td>
<td>SEQPKT</td>
<td>0</td>
<td>* &lt;1.1.25&gt;, 1086717961</td>
</tr>
<tr>
<td>heartbeat_private</td>
<td>priv</td>
<td>LOCAL</td>
<td>SEQPKT</td>
<td>0</td>
<td>/comp/socket/0x4</td>
</tr>
<tr>
<td>local_temperature</td>
<td>node</td>
<td>LOCAL</td>
<td>SEQPKT</td>
<td>0</td>
<td>/comp/socket/0x2</td>
</tr>
<tr>
<td>newsfeed</td>
<td>clust</td>
<td>TIPC</td>
<td>SEQPKT</td>
<td>0</td>
<td>* &lt;1.1.50&gt;, 1086717962</td>
</tr>
<tr>
<td>rocket_diagnostic_port</td>
<td>clust</td>
<td>TIPC</td>
<td>SEQPKT</td>
<td>0</td>
<td>&lt;1.1.22&gt;, 1086717964</td>
</tr>
<tr>
<td>rocket_propellant_fuel_level_interface</td>
<td>clust</td>
<td>TIPC</td>
<td>SEQPKT</td>
<td>0</td>
<td>* &lt;1.1.22&gt;, 1086717960</td>
</tr>
<tr>
<td>spacestation_docking_port</td>
<td>clust</td>
<td>TIPC</td>
<td>SEQPKT</td>
<td>0</td>
<td>* &lt;1.1.55&gt;, 1086717963</td>
</tr>
</tbody>
</table>

### From Node <1.1.25>

<table>
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<th>NAME</th>
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<th>FAMILY</th>
<th>TYPE</th>
<th>PROTO</th>
<th>ADDR</th>
</tr>
</thead>
<tbody>
<tr>
<td>astronaut_display</td>
<td>clust</td>
<td>TIPC</td>
<td>SEQPKT</td>
<td>0</td>
<td>* &lt;1.1.22&gt;, 1086717967</td>
</tr>
<tr>
<td>ground_control_timestamp</td>
<td>clust</td>
<td>LOCAL</td>
<td>SEQPKT</td>
<td>0</td>
<td>/comp/socket/0x3</td>
</tr>
<tr>
<td>ground_control_weblog</td>
<td>clust</td>
<td>TIPC</td>
<td>SEQPKT</td>
<td>0</td>
<td>* &lt;1.1.25&gt;, 1086717965</td>
</tr>
<tr>
<td>local_billboard</td>
<td>node</td>
<td>LOCAL</td>
<td>SEQPKT</td>
<td>0</td>
<td>/comp/socket/0x2</td>
</tr>
<tr>
<td>newsfeed</td>
<td>clust</td>
<td>TIPC</td>
<td>SEQPKT</td>
<td>0</td>
<td>* &lt;1.1.50&gt;, 1086717962</td>
</tr>
<tr>
<td>rocket_diagnostic_port</td>
<td>clust</td>
<td>TIPC</td>
<td>SEQPKT</td>
<td>0</td>
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</tr>
<tr>
<td>rocket_propellant_fuel_level_interface</td>
<td>clust</td>
<td>TIPC</td>
<td>SEQPKT</td>
<td>0</td>
<td>* &lt;1.1.22&gt;, 1086717960</td>
</tr>
<tr>
<td>spacestation_docking_port</td>
<td>clust</td>
<td>TIPC</td>
<td>SEQPKT</td>
<td>0</td>
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</tbody>
</table>

### From Node <1.1.55>

<table>
<thead>
<tr>
<th>NAME</th>
<th>SCOPE</th>
<th>FAMILY</th>
<th>TYPE</th>
<th>PROTO</th>
<th>ADDR</th>
</tr>
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<tr>
<td>astronaut_display</td>
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<td>TIPC</td>
<td>SEQPKT</td>
<td>0</td>
<td>* &lt;1.1.25&gt;, 1086717961</td>
</tr>
<tr>
<td>ground_control_weblog</td>
<td>clust</td>
<td>TIPC</td>
<td>SEQPKT</td>
<td>0</td>
<td>* &lt;1.1.50&gt;, 1086717962</td>
</tr>
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<td>SEQPKT</td>
<td>0</td>
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<td>TIPC</td>
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<td>0</td>
<td>* &lt;1.1.22&gt;, 1086717960</td>
</tr>
<tr>
<td>spacestation_docking_port</td>
<td>clust</td>
<td>LOCAL</td>
<td>SEQPKT</td>
<td>0</td>
<td>/comp/socket/0x2</td>
</tr>
</tbody>
</table>

The output of the **snsShow()** command is fairly self-explanatory. The first field is the name of the socket. If the name is longer than the space allocated in the output,
the entire name is printed and the other information is presented on the next line with the name field containing several dashes.

The scope values are priv for private, node for node, and clust for cluster.

The family types can be TIPC for AF_TIPC or LOCAL for AF_LOCAL.

The protocol type can be SEQPCT for SOCK_SEQPACKET, RDM.

The scope values are priv for private, node for node, and clust for cluster.

The family types can be TIPC for AF_TIPC or LOCAL for AF_LOCAL.

The protocol can be SEQPCT for SOCK_SEQPACKET, RDM.

The address field indicates the address of the socket. All addresses of the form /comp/socket belong to the AF_LOCAL family. All addresses of the form <x.y.z>,refID belong to the AF_TIPC family. The TIPC address gives the TIPC portID which consists of the nodeID and the unique reference number.

Socket Application Libraries

The Socket Application Libraries (SAL) simplify creation of both server and client applications by providing routines to facilitate use of the sockets API.

SAL also provides an infrastructure for the development of location-transparent and interface-transparent applications. By allowing SAL to handle the basic housekeeping associated with a socket-based application, developers can focus on the application-specific portions of their designs. Developers are free to use the complete range of SAL capabilities in their applications, or just the subset that suits their needs; they can even bypass SAL entirely and develop a socket-based application using nothing but custom software. The SAL client and server APIs can be used in both kernel and user space.

Several VxWorks components are available to provide SAL support in different memory spaces, for single or multi-node systems, and so on (see Configuring VxWorks for Message Channels, p. 198).

SAL-based applications can also utilize the Socket Name Service (SNS), which allows a client application to establish communication with a server application without having to know the socket addresses used by the server (see Socket Name Service, p. 191).
SAL Server Library

The SAL server routines provide the infrastructure for implementing a
socket-based server application. The SAL server allows a server application to
provide service to any number of client applications. A server application
normally utilizes a single SAL server in its main task, but is free to spawn
additional tasks to handle the processing for individual clients if parallel
processing of client requests is required. The SAL server library is made of the
following routines:

salCreate( )
Creates a named socket-based server.

salDelete( )
Deletes a named socket-based server.

salServerRtnSet( )
Configures the processing routine with the SAL server.

salRun( )
Activates a socket-based server.

salRemove( )
Removes a service from the SNS by name.

A server application typically calls salCreate( ) to configure a SAL server with one
or more sockets that are then automatically registered with SNS under a specified
service identifier. The number of sockets created depends on which address
families, socket types, and socket protocols are specified by the server application.
AF_LOCAL and AF_TIPC sockets are supported.

If the address family specified is AF_UNSPEC, the system attempts to create sockets
in all of the supported address families (AF_LOCAL and AF_TIPC). The socket
addresses used for the server’s sockets are selected automatically, and cannot be
specified by the server application with salCreate( ).

A server can be accessed by clients within the scope that is defined when the server
is created with the salCreate( ) routine. The scope can have the following values:
private, node, or cluster. These values designate an access scope for limiting access
to the same task (kernel or process), the same node (the kernel and all processes on
that node), and a set of nodes, respectively. For example, the following call would
create a socket named foo with cluster scope:

salCreate("foo@cluster",1,5)
Once created, a SAL server must be configured with one or more processing routines before it is activated. These routines can be configured by calling `salServerRtnSet()`.

Once the server is ready, `salRun()` is called to start the server activities. The `salRun()` routine never returns unless there is an error or one of the server processing routines requests it. You must call `salDelete()` to delete the server and its sockets regardless of whether or not the routine has terminated. This is accomplished with `salDelete()`. This routine can be called only by tasks in the process (or the kernel) where the server was created. In order for tasks outside the process to remove a service name from SNS, `salRemove()` must be used. The `salRemove()` routine does not close sockets, nor does it delete the server. It only deletes the SNS entry, and therefore access to any potential clients.

For more information, including sample service code, see the VxWorks API reference for the `salServer` library.

**SAL Client Library**

The SAL client library provides a simple means for implementing a socket-based client application. The data structures and routines provided by SAL allow the application to easily communicate with socket-based server applications that are registered with the Socket Name Service (see *Socket Name Service*, p.191). Additional routines can be used to communicate with server applications that are not registered with the SNS. The SAL client library is made of the following routines:

`salOpen()`
- Establishes communication with a named socket-based server.

`salSocketFind()`
- Finds sockets for a named socket-based server.

`salNameFind()`
- Finds services with the specified name.

`salCall()`
- Invokes a socket-based server.

A client application typically calls `salOpen()` to create a client socket and connect it to the named server application. The client application can then communicate with the server by passing the socket descriptor to standard socket API routines, such as `send()` and `recv()`.

As an alternative, the client application can perform a `send()` and `recv()` as a single operation using `salCall()`. When the client application no longer needs to
communicate with a server it calls the standard socket `close()` routine to close the socket to the server.

A client socket can be shared between two or more tasks. In this case, however, special care must be taken to ensure that a reply returned by the server application is handled by the correct task.

The `salNameFind()` and `salSocketFind()` routines facilitate the search of the server and provide more flexibility for the client application.

The `salNameFind()` routine provides a lookup mechanism for services based on pattern matching, which can be used with (multiple) wild cards to locate similar names. For example, if the names are `foo`, `foo2`, and `foobar`, then a search using `foo*` would return them all. The scope of the search can also be specified. For example, a client might want to find any server up to a given scope, or only within a given scope. In the former case the `upto_` prefix can be added to the scope specification. For example, `upto_node` defines a search that look for services in all processes and in the kernel in a node.

Once a service is found, the `salSocketFind()` routine can be used to return the proper socket ID. This can be useful if the service has multiple sockets, and the client requires use of a specific one. This routine can also be used with wild cards, in which case the first matching server socket is returned.

For more information, including sample client code, see the VxWorks API reference for the `salClient` library.

---

**Configuring VxWorks for Message Channels**

To provide the full set of message channel facilities in a system, configure VxWorks with the following components:

- `INCLUDE_UN_COMP`
- `INCLUDE_DSI_POOL`
- `INCLUDE_DSI_SOCKET`
- `INCLUDE_SAL_SERVER`
- `INCLUDE_SAL_CLIENT`

Note that `INCLUDE_UN_COMP` is required for both single and multi-node systems, as it provides support for communication between SAL and SNS.

While COMP provides for standard socket support, it has no dependency on TCP/IP networking facilities, which can be left out of a system if they are not otherwise needed.
For multi-node systems, TIPC components must also be included. See the *Wind River TIPC Programmer's Guide* for more information.

**SNS Configuration**

In addition to the COMP, DSI, and SAL components, one of the four following components is required for SNS:

- **INCLUDE_SNS** to run SNS as a kernel daemon.
- **INCLUDE_SNS_RTP** to start SNS as a process automatically at boot time.
- **INCLUDE_SNS_MP** to run SNS as a kernel daemon supporting distributed named sockets.
- **INCLUDE_SNS_MP_RTP** to start SNS as a process automatically at boot time supporting distributed named sockets.

Note that including a distributed SNS server will cause the inclusion of TIPC which in turn will force the inclusion of other networking components.

**Running SNS as a Process**

In order to run SNS as a process (RTP), the developer must also build the server, add it to ROMFS, configure VxWorks with ROMFS support, and then rebuild the entire system:

a. Build `installDir/vxworks-6.x/target/usr/apps/dsi/snsd/snsd.c` (using the makefile in the same directory) to create `snsServer.vxe`.

b. Copy `snsServer.vxe` to the ROMFS directory (creating the directory first, if necessary).

The **INCLUDE_SNS_RTP** and **INCLUDE_SNS_MP_RTP** components need to know the location of the server in order to start it at boot time. They expect to find the server in the ROMFS directory. If you wish to store the server somewhere else (in another file system to reduce the VxWorks image size, for example) use the **SNS_PATHNAME** parameter to identify the location.

c. Configure VxWorks with the ROMFS component.

d. Rebuild VxWorks.

These steps can also be performed with Wind River Workbench (see the *Wind River Workbench User’s Guide*). For information about ROMFS, see 7.7 Read-Only Memory File System: ROMFS, p.455.
SNS Configuration

The following SNS component parameters can usually be used without modification:

**SNS_LISTEN_BACKLOG**
This parameter defines the number of outstanding service requests that the SNS server can track on the socket that it uses to service SNS requests from SAL routines. The default value is 5. The value may be increased if some SAL requests are not processed on a busy system.

**SNS_DISTRIBUTED_SERVER_TYPE and SNS_DISTRIBUTED_SERVER_INSTANCE**
These parameters are used in the multi-node configuration of SNS servers to define the TIPC port name that all SNS servers use. The default is type 51 and instance 51 in the TIPC name tables. If this type and instance conflict with other usages in the network, they can be changed to values that are unique for the network. Note that it is recommended to use a type of 50 or above (types 0 through 7 are reserved by TIPC).

The SNS server creates a COMP socket for local communication with the socket address of 0x0405. All of the SAL routines send messages to the SNS server at this socket address.

⚠️ **CAUTION:** It is recommended that you do not change the default values of the SNS_PRIORITY and SNS_STACK_SIZE parameters. The default for SNS_PRIORITY is 50 and the default for SNS_STACK_SIZE is 20000.

Show Routines

The show routines related to COMP can be included by adding the INCLUDE_UN_COMP_SHOW component. The snsShow() routine is included with the INCLUDE_SNS_SHOW component. In order to use netstat() the network show routines need to be included. Note that this will force the inclusion of networking components.

For information about processes and applications, see *VxWorks Application Programmer’s Guide: Applications and Processes*.

Comparing Message Channels and Message Queues

Message channels can be used similarly to message queues, to exchange data between two tasks. Both methods allow multiple tasks to send and receive from the same channel. The main differences between these two mechanisms are:
- Message channels can be used to communicate between nodes, but message queues cannot.

- Message channels are connection-oriented while message queues are not. There is no way to establish a connection between two tasks with message queues. In a connection-oriented communication, the two end-points are aware of each other, and if one leaves the other eventually finds out. By way of analogy, a connection-oriented communication is like a telephone call, whereas a connection-less communication is like sending a letter. Both models are valid, and the requirements of the application should determine their use.

Each message queue is unidirectional. In order to establish a bidirectional communication, two queues are needed, one for each end-point (see Figure 3-14). Each message channel is bidirectional and data can be sent from both end-points at any time. That is, each message channel provides connection-oriented full-duplex communication.

- The messages communicated by message channels can be of variable size, whereas those communicated by message queues have a maximum size that is defined when the queue is created. Message channels therefore allow for a better utilization of system resources by using exactly what is needed for the message, and nothing more.

- Message queues have a fixed capacity. Only a pre-defined number of messages can be in a queue at any one time. Message channels, on the other hand, have a flexible capacity. There is no limit to the number of messages that a message channel can handle.

- Message channels provide location transparency. An endpoint can be referred to by a name, that is by a simple string of characters (but a specific address can also be used). Message queues only provide location transparency for interprocess communication when they are created as public objects.

- Message channels provide a simple interface for implementing a client/server paradigm. A location transparent connection can be established by using two simple calls, one for the client and one for the server. Message queues do not provide support for client/server applications.

- Message channels use the standard socket interface and support the `select()` routine; message queues do not.

- Message channels cannot be used with VxWorks events; message queues can.

- Message queues can be used within an ISR, albeit only the `msgQsend()` routine. No message channel routines can be used within an ISR.
Message queues are based entirely on a proprietary API and are therefore more difficult to port to a different operating system than message channels, which are based primarily on the standard socket API.

Message channels are better suited to applications that are based on a client/server paradigm and for which location transparency is important.

### 3.3.9 Network Communication

To communicate peer on a remote networked system, you can use an Internet domain socket or RPC. For information on working with Internet domain sockets under VxWorks, see the *Wind River Network Stack for VxWorks 6 Programmer's Guide: Sockets under the Wind River Network Stack*. For information on RPC, see *Wind River Network Stack for VxWorks 6 Programmer's Guide: RPC Components* and the VxWorks API reference for `rpcLib`.

### 3.3.10 Signals

VxWorks provides a software signal facility. Signals are the means by which tasks are notified of the occurrence of significant events in the system. Examples of significant events include hardware exceptions, signals to kill processes, and so on. Each signal has a unique number, and there are 31 signals in all. The value 0 is reserved for use as the null signal. Each signal has a default action associated with itself. Developers can change the default. Signals can either be disabled, so that they will not interrupt the task, or enabled, which allows signals to be received.

Signals asynchronously alter the control flow of a task. Any task or ISR can raise a signal for a particular task. The task being signaled immediately suspends its current thread of execution and executes the task-specified signal handler routine the next time it is scheduled to run. The signal handler executes in the receiving task’s context and makes use of that task’s stack. The signal handler is invoked even if the task is blocked.

VxWorks supports two types of signal interface: UNIX BSD-style signals and POSIX-compatible signals. The POSIX-compatible signal interface, in turn, includes both the fundamental signaling interface specified in the POSIX standard 1003.1, and the queued-signals extension from POSIX 1003.1b. For more information, see *4.14 POSIX Queued Signals*, p.268. For the sake of simplicity, we recommend that you use only one interface type in a given application, rather than mixing routines from different interfaces.
For information about using signals in the kernel, see VxWorks Kernel Programmer’s Guide: Multitasking.

**NOTE:** The VxWorks implementation of sigLib does not impose any special restrictions on operations on SIGKILL, SIGCONT, and SIGSTOP signals such as those imposed by UNIX. For example, the UNIX implementation of signal( ) cannot be called on SIGKILL and SIGSTOP.

### Configuring VxWorks for Signals

By default, VxWorks includes the basic signal facility component INCLUDE_SIGNALS. This component automatically initializes signals with sigInit().

### Basic Signal Routines

Signals are in many ways analogous to hardware interrupts. The basic signal facility provides a set of 31 distinct signals. A signal handler binds to a particular signal with sigvec() or sigaction() in much the same way that an ISR is connected to an interrupt vector with intConnect(). A signal can be asserted by calling kill(). This is analogous to the occurrence of an interrupt. The routines sigsetmask() and sigblock() or sigprocmask() let signals be selectively inhibited. Certain signals are associated with hardware exceptions. For example, bus errors, illegal instructions, and floating-point exceptions raise specific signals.

For a list and description of basic signal routines provided by VxWorks in the kernel, see Table 3-18.
VxWorks also provides a POSIX and BSD-like `kill()` routine, which sends a signal to a task.

VxWorks also provides additional routines that serve as aliases for POSIX routines, such as `rtpKill()`, that provide for sending signals from the kernel to processes.

For more information about signal routines, see the VxWorks API reference for `sigLib` and `rtpSigLib`.

### Signal Handlers

Signals are more appropriate for error and exception handling than as a general-purpose intertask communication mechanism. And in general, signal handlers should be treated like ISRs; no routine should be called from a signal handler that might cause the handler to block. Because signals are asynchronous,
it is difficult to predict which resources might be unavailable when a particular signal is raised.

To be perfectly safe, call only those routines listed in Table 3-19. Deviate from this practice only if you are certain that your signal handler cannot create a deadlock situation.

<table>
<thead>
<tr>
<th>Library</th>
<th>Routines</th>
</tr>
</thead>
<tbody>
<tr>
<td>bLib</td>
<td>All routines</td>
</tr>
<tr>
<td>errnoLib</td>
<td>errnoGet(), errnoSet()</td>
</tr>
<tr>
<td>eventLib</td>
<td>eventSend()</td>
</tr>
<tr>
<td>fppArchLib</td>
<td>fppSave(), fppRestore()</td>
</tr>
<tr>
<td>intLib</td>
<td>intContext(), intCount(), intVecSet(), intVecGet()</td>
</tr>
<tr>
<td>intArchLib</td>
<td>intLock(), intUnlock()</td>
</tr>
<tr>
<td>logLib</td>
<td>logMsg()</td>
</tr>
<tr>
<td>lstLib</td>
<td>All routines except lstFree()</td>
</tr>
<tr>
<td>mathALib</td>
<td>All routines, if fppSave()/fppRestore() are used</td>
</tr>
<tr>
<td>msgQLib</td>
<td>msgQSend()</td>
</tr>
<tr>
<td>rngLib</td>
<td>All routines except rngCreate() and rngDelete()</td>
</tr>
<tr>
<td>semLib</td>
<td>semGive() except mutual-exclusion semaphores, semFlush()</td>
</tr>
<tr>
<td>sigLib</td>
<td>kill()</td>
</tr>
<tr>
<td>taskLib</td>
<td>taskSuspend(), taskResume(), taskPrioritySet(), taskPriorityGet(), taskIdVerify(), taskIdDefault(), taskIsReady(), taskIsSuspended(), taskTcb()</td>
</tr>
<tr>
<td>tickLib</td>
<td>tickAnnounce(), tickSet(), tickGet()</td>
</tr>
<tr>
<td>tyLib</td>
<td>tyIRd(), tyITx()</td>
</tr>
<tr>
<td>vxLib</td>
<td>vxTas(), vxMemProbe()</td>
</tr>
<tr>
<td>wdLib</td>
<td>wdStart(), wdCancel()</td>
</tr>
</tbody>
</table>
Most signals are delivered asynchronously to the execution of a program. Therefore programs must be written to account for the unexpected occurrence of signals, and handle them gracefully. Unlike ISR’s, signal handlers execute in the context of the interrupted task or process. And the VxWorks kernel does not distinguish between normal task execution and a signal context, as it distinguishes between a task context and an ISR. Therefore the system has no way of distinguishing between a task execution context and a task executing a signal handler. To the system, they are the same.

When you write signal handlers make sure that they:

- Release resources prior to exiting:
  - Free any allocated memory.
  - Close any open files.
  - Release any mutual exclusion resources such as semaphores.
- Leave any modified data structures in a sane state.
- Notify the kernel with an appropriate error return value.

Mutual exclusion between signal handlers and tasks must be managed with care. In general, users should completely avoid the following activity in signal handlers:

- Taking mutual exclusion (such as semaphores) resources that can also be taken by any other element of the application code. This can lead to deadlock.
- Modifying any shared data memory that may have been in the process of modification by any other element of the application code when the signal was delivered. This compromises mutual exclusion and leads to data corruption.

Both scenarios are very difficult to debug, and should be avoided. One safe way to synchronize other elements of the application code and a signal handler is to set up dedicated flags and data structures that are set from signal handlers and read from the other elements. This ensures a consistency in usage of the data structure. In addition, the other elements of the application code must check for the occurrence of signals at any time by periodically checking to see if the synchronizing data structure or flag has been modified in the background by a signal handler, and then acting accordingly. The use of the `volatile` keyword is useful for memory locations that are accessed from both a signal handler and other elements of the application.

Taking a mutex semaphore in a signal handler is an especially bad idea. Mutex semaphores can be taken recursively. A signal handler can therefore easily re-acquire a mutex that was taken by any other element of the application. Since the signal handler is an asynchronously executing entity, it has thereby broken the mutual exclusion that the mutex was supposed to provide.
Taking a binary semaphore in a signal handler is an equally bad idea. If any other element has already taken it, the signal handler will cause the task to block on itself. This is a deadlock from which no recovery is possible. Counting semaphores, if available, suffer from the same issue as mutexes, and if unavailable, are equivalent to the binary semaphore situation that causes an unrecoverable deadlock.

On a general note, the signal facility should be used only for notifying/handling exceptional or error conditions. Usage of signals as a general purpose IPC mechanism or in the data flow path of an application can cause some of the pitfalls described above.

### 3.4 Watchdog Timers

VxWorks includes a watchdog-timer mechanism that allows any C function to be connected to a specified time delay. Watchdog timers are maintained as part of the system clock ISR. Functions invoked by watchdog timers execute as interrupt service code at the interrupt level of the system clock. Restrictions on ISRs apply to routines connected to watchdog timers. The functions in Table 3-20 are provided by the wdLib library.

<table>
<thead>
<tr>
<th>Call</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>wdCreate()</td>
<td>Allocates and initializes a watchdog timer.</td>
</tr>
<tr>
<td>wdDelete()</td>
<td>Terminates and deallocates a watchdog timer.</td>
</tr>
<tr>
<td>wdStart()</td>
<td>Starts a watchdog timer.</td>
</tr>
<tr>
<td>wdCancel()</td>
<td>Cancels a currently counting watchdog timer.</td>
</tr>
</tbody>
</table>

A watchdog timer is first created by calling `wdCreate()`. Then the timer can be started by calling `wdStart()`, which takes as arguments the number of ticks to delay, the C function to call, and an argument to be passed to that function. After the specified number of ticks have elapsed, the function is called with the specified argument. The watchdog timer can be canceled any time before the delay has elapsed by calling `wdCancel()`.
Example 3-5  Watchdog Timers

/* Creates a watchdog timer and sets it to go off in 3 seconds.*/

/* includes */
#include <vxWorks.h>
#include <logLib.h>
#include <wdLib.h>

/* defines */
#define SECONDS (3)

WDOG_ID myWatchDogId;

task (void)
{
    /* Create watchdog */
    if ((myWatchDogId = wdCreate( )) == NULL)
        return (ERROR);

    /* Set timer to go off in SECONDS - printing a message to stdout */
    if (wdStart (myWatchDogId, sysClkRateGet( ) * SECONDS, logMsg,
                 "Watchdog timer just expired\n") == ERROR)
        return (ERROR);
    /* ... */
}

For information about POSIX timers, see 4.6 POSIX Clocks and Timers, p. 225.
3.5 Interrupt Service Routines

Hardware interrupt handling is of key significance in real-time systems, because it is usually through interrupts that the system is informed of external events. For the fastest possible response to interrupts, VxWorks runs interrupt service routines (ISRs) in a special context outside of any task's context. Thus, interrupt handling involves no task context switch. Table 3-21 lists the interrupt routines provided in intLib and intArchLib.

<table>
<thead>
<tr>
<th>Call</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>intConnect()</td>
<td>Connects a C routine to an interrupt vector.</td>
</tr>
<tr>
<td>intContext()</td>
<td>Returns TRUE if called from interrupt level.</td>
</tr>
<tr>
<td>intCount()</td>
<td>Gets the current interrupt nesting depth.</td>
</tr>
<tr>
<td>intLevelSet()</td>
<td>Sets the processor interrupt mask level.</td>
</tr>
<tr>
<td>intLock()</td>
<td>Disables interrupts.</td>
</tr>
<tr>
<td>intUnlock()</td>
<td>Re-enables interrupts.</td>
</tr>
<tr>
<td>intVecBaseSet()</td>
<td>Sets the vector base address.</td>
</tr>
<tr>
<td>intVecBaseGet()</td>
<td>Gets the vector base address.</td>
</tr>
<tr>
<td>intVecSet()</td>
<td>Sets an exception vector.</td>
</tr>
<tr>
<td>intVecGet()</td>
<td>Gets an exception vector.</td>
</tr>
</tbody>
</table>

3.5.1 Connecting Routines to Interrupts

You can use system hardware interrupts other than those used by VxWorks. VxWorks provides the routine intConnect(), which allows C functions to be connected to any interrupt. The arguments to this routine are the byte offset of the interrupt vector to connect to, the address of the C function to be connected, and an argument to pass to the function. When an interrupt occurs with a vector established in this way, the connected C function is called at interrupt level with the specified argument. When the interrupt handling is finished, the connected function returns. A routine connected to an interrupt in this way is called an interrupt service routine (ISR).
Interrupts cannot actually vector directly to C functions. Instead, \texttt{intConnect()} builds a small amount of code that saves the necessary registers, sets up a stack entry (either on a special interrupt stack, or on the current task’s stack) with the argument to be passed, and calls the connected function. On return from the function it restores the registers and stack, and exits the interrupt; see Figure 3-16.

**Figure 3-16 Routine Built by intConnect()**

<table>
<thead>
<tr>
<th>Wrapper built by intConnect()</th>
<th>Interrupt Service Routine</th>
</tr>
</thead>
<tbody>
<tr>
<td>save registers</td>
<td>myISR</td>
</tr>
<tr>
<td>set up stack</td>
<td>{</td>
</tr>
<tr>
<td>invoke routine</td>
<td>\  \  int val;</td>
</tr>
<tr>
<td>restore registers and stack</td>
<td>\  \  \  /* deal with hardware*/</td>
</tr>
<tr>
<td>exit</td>
<td>\  \  \  \  \  \  \  ...</td>
</tr>
</tbody>
</table>

\[
\text{intConnect (INUM\_TO\_IVEC (someIntNum), myISR, someVal);}\
\]

For target boards with VME backplanes, the BSP provides two standard routines for controlling VME bus interrupts, \texttt{sysIntEnable()} and \texttt{sysIntDisable()}.

### 3.5.2 Interrupt Stack

All ISRs use the same \textit{interrupt stack}. This stack is allocated and initialized by the system at start-up according to specified configuration parameters. It must be large enough to handle the worst possible combination of nested interrupts.

Some architectures, however, do not permit using a separate interrupt stack. On such architectures, ISRs use the stack of the interrupted task. If you have such an architecture, you must create tasks with enough stack space to handle the worst possible combination of nested interrupts \textit{and} the worst possible combination of ordinary nested calls. See the VxWorks reference for your BSP to determine whether your architecture supports a separate interrupt stack.

Use the \texttt{checkStack()} facility during development to see how close your tasks and ISRs have come to exhausting the available stack space.

You can also design and test their systems with the assistance of the \texttt{INCLUDE\_PROTECT\_INTERRUPT\_STACK} component, which helps detect interrupt stack overflows. Production systems can be shipped without the
component to save memory. For more information about stack-protection facilities, see 5.10 Additional Memory Protection Features, p.310.

3.5.3 Writing and Debugging ISRs

There are some restrictions on the routines you can call from an ISR. For example, you cannot use routines like printf(), malloc(), and semTake() in your ISR. You can, however, use semGive(), logMsg(), msgQSend(), and bcopy(). For more information, see 3.5.4 Special Limitations of ISRs, p.211.

ISRs and the Kernel Work Queue

The VxWorks kernel reduces interrupt latency to a minimum by protecting portions of its critical sections using a work deferral mechanism as opposed to locking interrupts. Work deferral consists of storing kernel work requests performed by ISRs that interrupt the kernel while it is in one of its critical sections. For example, an ISR that performs a semGive() after having interrupted the kernel in a critical section would cause work to be stored in the work queue. This work is processed after the ISR returns and immediately after the kernel exits its critical section. This process involves a static buffer, also known as work queue, used to store work requests until they are processed. This is all internal to the VxWorks kernel and users must never make use of the work queue. However, it is possible for the work queue to overflow in situations where a large number of interrupts cause work deferral without allowing the kernel to complete execution of the critical section that was originally interrupted. These situations are uncommon and are often symptoms of ill-behaved interrupt service routines. A work queue overflow is also known as a work queue panic in reference to the message the kernel displays as a result of an overflow:

workQPanic: Kernel work queue overflow

In order to help reduce the occurrences of work queue overflows, system architects can use the WIND_JOBS_MAX kernel configuration parameter to increase the size of the kernel work queue. However in most cases this is simply hiding the root cause of the overflow.

3.5.4 Special Limitations of ISRs

Many VxWorks facilities are available to ISRs, but there are some important limitations. These limitations stem from the fact that an ISR does not run in a regular task context and has no task control block, so all ISRs share a single stack.
For this reason, the basic restriction on ISRs is that they must not invoke routines that might cause the caller to block. For example, they must not try to take a semaphore, because if the semaphore is unavailable, the kernel tries to switch the

<table>
<thead>
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</tr>
</thead>
<tbody>
<tr>
<td>bLib</td>
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</tr>
<tr>
<td>errnoLib</td>
<td>errnoGet(), errnoSet()</td>
</tr>
<tr>
<td>eventLib</td>
<td>eventSend()</td>
</tr>
<tr>
<td>fppArchLib</td>
<td>fppSave(), fppRestore()</td>
</tr>
<tr>
<td>intLib</td>
<td>intContext(), intCount(), intVecSet(), intVecGet()</td>
</tr>
<tr>
<td>intArchLib</td>
<td>intLock(), intUnlock()</td>
</tr>
<tr>
<td>logLib</td>
<td>logMsg()</td>
</tr>
<tr>
<td>lstLib</td>
<td>All routines except lstFree()</td>
</tr>
<tr>
<td>mathALib</td>
<td>All routines, if fppSave()/fppRestore() are used</td>
</tr>
<tr>
<td>msgQLib</td>
<td>msgQSend()</td>
</tr>
<tr>
<td>rngLib</td>
<td>All routines except rngCreate() and rngDelete()</td>
</tr>
<tr>
<td>selectLib</td>
<td>selWakeup(), selWakeupAll()</td>
</tr>
<tr>
<td>semLib</td>
<td>semGive() except mutual-exclusion semaphores, semFlush()</td>
</tr>
<tr>
<td>sigLib</td>
<td>kill()</td>
</tr>
<tr>
<td>taskLib</td>
<td>taskSuspend(), taskResume(), taskPrioritySet(), taskPriorityGet(), taskIdVerify(), taskIdDefault(), taskIdReady(), taskIdSuspended(), taskTcb()</td>
</tr>
<tr>
<td>tickLib</td>
<td>tickAnnounce(), tickSet(), tickGet()</td>
</tr>
<tr>
<td>tyLib</td>
<td>tyIRd(), tyITx()</td>
</tr>
<tr>
<td>vxLib</td>
<td>vxTas(), vxMemProbe()</td>
</tr>
<tr>
<td>wdLib</td>
<td>wdStart(), wdCancel()</td>
</tr>
</tbody>
</table>
caller to the pended state. However, ISRs can give semaphores, releasing any tasks waiting on them.

Because the memory facilities `malloc()` and `free()` take a semaphore, they cannot be called by ISRs, and neither can routines that make calls to `malloc()` and `free()`. For example, ISRs cannot call any creation or deletion routines.

ISRs also must not perform I/O through VxWorks drivers. Although there are no inherent restrictions in the I/O system, most device drivers require a task context because they might block the caller to wait for the device.

VxWorks supplies a logging facility, in which a logging task prints text messages to the system console. This mechanism was specifically designed for ISR use, and is the most common way to print messages from ISRs. For more information, see the VxWorks API reference for `logLib`.

An ISR also must not call routines that use a floating-point coprocessor. In VxWorks, the interrupt driver code created by `intConnect()` does not save and restore floating-point registers; thus, ISRs must not include floating-point instructions. If an ISR requires floating-point instructions, it must explicitly save and restore the registers of the floating-point coprocessor using routines in `fppArchLib`.

All VxWorks utility libraries, such as the linked-list and ring-buffer libraries, can be used by ISRs. As discussed earlier (3.2.5 Task Error Status: `errno`, p.149), the global variable `errno` is saved and restored as a part of the interrupt enter and exit code generated by the `intConnect()` facility. Thus, `errno` can be referenced and modified by ISRs as in any other code. Table 3-22 lists routines that can be called from ISRs.

### 3.5.5 Exceptions at Interrupt Level

When a task causes a hardware exception such as an illegal instruction or bus error, the task is suspended and the rest of the system continues uninterrupted. However, when an ISR causes such an exception, there is no safe recourse for the system to handle the exception. The ISR has no context that can be suspended. Instead, VxWorks stores the description of the exception in a special location in low memory and executes a system restart.

The VxWorks boot loader tests for the presence of the exception description in low memory and if it is detected, display it on the system console. The boot loader’s `e` command re-displays the exception description; see Wind River Workbench User’s Guide: Setting up Your Hardware.
One example of such an exception is the following message:

    workQPanic: Kernel work queue overflow.

This exception usually occurs when kernel calls are made from interrupt level at a very high rate. It generally indicates a problem with clearing the interrupt signal or a similar driver problem. (See ISRs and the Kernel Work Queue, p. 211.)

3.5.6 Reserving High Interrupt Levels

The VxWorks interrupt support described earlier in this section is acceptable for most applications. However, on occasion, low-level control is required for events such as critical motion control or system failure response. In such cases it is desirable to reserve the highest interrupt levels to ensure zero-latency response to these events. To achieve zero-latency response, VxWorks provides the routine intLockLevelSet(), which sets the system-wide interrupt-lockout level to the specified level. If you do not specify a level, the default is the highest level supported by the processor architecture. For information about architecture-specific implementations of intLockLevelSet(), see the VxWorks Architecture Supplement.

⚠️ CAUTION: Some hardware prevents masking certain interrupt levels; check the hardware manufacturer’s documentation.

3.5.7 Additional Restrictions for ISRs at High Interrupt Levels

ISRs connected to interrupt levels that are not locked out (either an interrupt level higher than that set by intLockLevelSet(), or an interrupt level defined in hardware as non-maskable) have special restrictions:

- The ISR can be connected only with intVecSet().
- The ISR cannot use any VxWorks operating system facilities that depend on interrupt locks for correct operation. The effective result is that the ISR cannot safely make any call to any VxWorks function, except reboot.

For more information, see the VxWorks Architecture Supplement for the architecture in question.
3.5.8 Interrupt-to-Task Communication

While it is important that VxWorks support direct connection of ISRs that run at interrupt level, interrupt events usually propagate to task-level code. Many VxWorks facilities are not available to interrupt-level code. The following techniques can be used to communicate from ISRs to task-level code:

- **Shared Memory and Ring Buffers**
  
  ISRs can share variables, buffers, and ring buffers with task-level code.

- **Semaphores**
  
  ISRs can give semaphores (except for mutual-exclusion semaphores and VxMP shared semaphores) that tasks can take and wait for.

- **Message Queues**
  
  ISRs can send messages to message queues for tasks to receive (except for shared message queues using VxMP). If the queue is full, the message is discarded.

- **Signals**
  
  ISRs can signal tasks, causing asynchronous scheduling of their signal handlers.

- **VxWorks Events**
  
  ISRs can send VxWorks events to tasks.
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4.3 General POSIX Support 220
4.4 POSIX Header Files 221
4.5 POSIX Process Support 224
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4.14 POSIX Queued Signals 268
4.1 Introduction

VxWorks provides extensive POSIX support in many of its kernel libraries. To facilitate application portability, VxWorks provides additional POSIX interfaces as optional components. In the kernel VxWorks implements some of the interfaces described by the POSIX standard IEEE Std 1003.1 as well as interfaces designed specifically for VxWorks. In particular, it provides an implementation of the POSIX Realtime extensions and Threads extensions—which were previously known as the IEEE Std 1003.1b (Real-Time Extensions) and IEEE Std 1003.1c (Threads) standards.

For information about POSIX support in real-time processes (RTPs), see the VxWorks Application Programmer’s Guide: POSIX Standard Interfaces.

For information about VxWorks processes, see the VxWorks Application Programmer’s Guide: Applications and Processes.

VxWorks provides many POSIX compliant APIs. However, not all POSIX APIs are suitable for embedded and real-time systems, or are entirely compatible with the VxWorks operating system architecture. In a few cases, therefore, Wind River has imposed minor limitations on POSIX functionality to serve either real-time systems or VxWorks compatibility. For example:

- Swapping memory to disk is not appropriate in real-time systems, and VxWorks provides no facilities for doing so. It does, however, provide POSIX page-locking routines to facilitate porting code to VxWorks. The routines otherwise provide no useful function—pages are always locked in VxWorks systems (for more information see 4.8 POSIX Page-Locking Interface, p.227).

- VxWorks tasks (threads) are scheduled on a system-wide basis; processes themselves cannot be scheduled. As a consequence, while POSIX access routines allow two values for contention scope (PTHREAD_SCOPE_SYSTEM and PTHREAD_SCOPE_PROCESS), only system-wide scope is implemented in VxWorks for these routines (for more information, see 4.9 POSIX Threads, p.229).

Any such limitations on POSIX functionality are identified in this chapter, or in other chapters of this guide that provide more detailed information on specific POSIX APIs.

Note that this chapter uses the qualifier VxWorks to identify native non-POSIX APIs for purposes of comparison with POSIX APIs. For example, you can find a discussion of VxWorks semaphores contrasted to POSIX semaphores in 4.11.1 Comparison of POSIX and VxWorks Semaphores, p.246, although POSIX semaphores are also implemented in VxWorks.
4.2 Configuring VxWorks with POSIX Facilities

Although VxWorks provides extensive POSIX compliance in many of its libraries (see 4.3 General POSIX Support, p.220), the default configuration of VxWorks does not include many POSIX facilities. The optional VxWorks components that provide support for individual POSIX libraries are described in this section.

General POSIX support can be provided by configuring VxWorks with the BUNDLE_POSIX component bundle. If memory constraints require a finer-grained configuration, individual components can be used for selected features. See the configuration instructions for individual POSIX features.

Table 4-1 provides an overview of the individual VxWorks components that must be configured in the kernel to provide support for the specified POSIX facilities.

<table>
<thead>
<tr>
<th>POSIX Facility</th>
<th>Required VxWorks Component</th>
</tr>
</thead>
<tbody>
<tr>
<td>for Kernel</td>
<td>for Processes</td>
</tr>
<tr>
<td>Asynchronous I/O with system driver</td>
<td>INCLUDE_POSIX_AIO, INCLUDE_POSIX_AIO_SYSDRV and INCLUDE_PIPES</td>
</tr>
<tr>
<td>Clocks</td>
<td>INCLUDE_POSIX_CLOCKS, INCLUDE_POSIX_CLOCKS</td>
</tr>
<tr>
<td>dirLib directory utilities</td>
<td>INCLUDE_POSIX_DIRLIB, N/A</td>
</tr>
<tr>
<td>ftruncate</td>
<td>INCLUDE_POSIX_FTRUNCATE, N/A</td>
</tr>
<tr>
<td>Memory locking</td>
<td>INCLUDE_POSIX_MEM, N/A</td>
</tr>
<tr>
<td>Message queues</td>
<td>INCLUDE_POSIX_MQ, INCLUDE_POSIX_MQ</td>
</tr>
<tr>
<td>pthreads</td>
<td>INCLUDE_POSIX_THREADS, INCLUDE_POSIX_CLOCKS and INCLUDE_POSIX_PTHREAD_SCHEDULER</td>
</tr>
<tr>
<td>Scheduler</td>
<td>INCLUDE_POSIX_SCHED, INCLUDE_POSIX_SCHED</td>
</tr>
<tr>
<td>Semaphores</td>
<td>INCLUDE_POSIX_SEM, INCLUDE_POSIX_SEM</td>
</tr>
<tr>
<td>Signals</td>
<td>INCLUDE_POSIX_SIGNALS, N/A</td>
</tr>
<tr>
<td>Timers</td>
<td>INCLUDE_POSIX_TIMERS, INCLUDE_POSIX_TIMERS</td>
</tr>
</tbody>
</table>
4.3 General POSIX Support

Many POSIX-compliant libraries are provided for VxWorks. These libraries are listed in Table 4-2; see the API references for these libraries for detailed information.

Wind River advises that you do not combine use of the POSIX libraries with native VxWorks libraries that provide similar functionality. Doing so may result in undesirable interactions between the two, as some POSIX APIs manipulate resources that are also used by native VxWorks APIs. For example, do not use tickLib routines to manipulate the system’s tick counter if you are also using clockLib routines; do not use the taskLib API to change the priority of a POSIX thread instead of the pthread API, and so on.

The following sections of this chapter describe the optional POSIX API components are provided in addition to the native VxWorks APIs.

<table>
<thead>
<tr>
<th>Functionality</th>
<th>Library</th>
</tr>
</thead>
<tbody>
<tr>
<td>Asynchronous I/O</td>
<td>aioPxLib</td>
</tr>
<tr>
<td>Buffer manipulation</td>
<td>bLib</td>
</tr>
<tr>
<td>Clock facility</td>
<td>clockLib</td>
</tr>
<tr>
<td>Directory handling</td>
<td>dirLib</td>
</tr>
<tr>
<td>Environment handling</td>
<td>C Library</td>
</tr>
<tr>
<td>File duplication</td>
<td>ioLib for user mode, and iosLib for the kernel</td>
</tr>
<tr>
<td>File management</td>
<td>fsPxLib and ioLib</td>
</tr>
<tr>
<td>I/O functions</td>
<td>ioLib</td>
</tr>
<tr>
<td>Options handling</td>
<td>getopt</td>
</tr>
<tr>
<td>POSIX message queues</td>
<td>mqPxLib</td>
</tr>
<tr>
<td>POSIX semaphores</td>
<td>semPxLib</td>
</tr>
<tr>
<td>POSIX timers</td>
<td>timerLib</td>
</tr>
<tr>
<td>POSIX threads</td>
<td>pthreadLib</td>
</tr>
</tbody>
</table>
4.4 POSIX Header Files

The POSIX 1003.1 standard defines a set of header files as part of the environment of development of applications. VxWorks’ user-side development environment provides more POSIX header files than the kernel’s, and their content is also more in agreement with the standard than the kernel’s header files.

CAUTION: Currently the test macro _POSIX_C_SOURCE is not supported, so native symbols (types, macros, routine prototypes) from the VxWorks namespace cannot be hidden from the application and may conflict with its own symbols.

Some of the type definitions in user-side POSIX header files may conflict with the native VxWorks types that are made visible via the vxWorks.h header file. This is the case with stdint.h which should not be included if vxWorks.h is included. This situation will be resolved in future releases.

Table 4-3  POSIX Header Files

<table>
<thead>
<tr>
<th>Header File</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>aio.h</td>
<td>asynchronous input and output</td>
</tr>
<tr>
<td>assert.h</td>
<td>verify program assertion</td>
</tr>
<tr>
<td>Header File</td>
<td>Description</td>
</tr>
<tr>
<td>---------------</td>
<td>--------------------------------------------------</td>
</tr>
<tr>
<td>complex.h</td>
<td>complex arithmetic (user-side only)</td>
</tr>
<tr>
<td>ctype.h</td>
<td>character types</td>
</tr>
<tr>
<td>dirent.h</td>
<td>format of directory entries</td>
</tr>
<tr>
<td>dlfcn.h</td>
<td>dynamic linking (user-side only)</td>
</tr>
<tr>
<td>errno.h</td>
<td>system error numbers</td>
</tr>
<tr>
<td>fcntl.h</td>
<td>file control options</td>
</tr>
<tr>
<td>fenv.h</td>
<td>floating-point environment (user-side only)</td>
</tr>
<tr>
<td>float.h</td>
<td>floating types (user-side only)</td>
</tr>
<tr>
<td>inttypes.h</td>
<td>fixed size integer types (user-side only)</td>
</tr>
<tr>
<td>iso646.h</td>
<td>alternative spellings (user-side only)</td>
</tr>
<tr>
<td>limits.h</td>
<td>implementation-defined constants</td>
</tr>
<tr>
<td>locale.h</td>
<td>category macros</td>
</tr>
<tr>
<td>math.h</td>
<td>mathematical declarations</td>
</tr>
<tr>
<td>mqueue.h</td>
<td>message queues</td>
</tr>
<tr>
<td>pthread.h</td>
<td>threads</td>
</tr>
<tr>
<td>sched.h</td>
<td>execution scheduling</td>
</tr>
<tr>
<td>search.h</td>
<td>search tables (user-side only)</td>
</tr>
<tr>
<td>semaphore.h</td>
<td>semaphores</td>
</tr>
<tr>
<td>setjmp.h</td>
<td>stack environment declarations</td>
</tr>
<tr>
<td>signal.h</td>
<td>signals</td>
</tr>
<tr>
<td>stdbool.h</td>
<td>boolean type and values (user-side only)</td>
</tr>
<tr>
<td>stddef.h</td>
<td>standard type definitions (user-side only)</td>
</tr>
<tr>
<td>stdint.h</td>
<td>integer types (user-side only)</td>
</tr>
</tbody>
</table>
### POSIX Header Files

<table>
<thead>
<tr>
<th>Header File</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>stdio.h</td>
<td>standard buffered input/output</td>
</tr>
<tr>
<td>stdlib.h</td>
<td>standard library definitions</td>
</tr>
<tr>
<td>string.h</td>
<td>string operations</td>
</tr>
<tr>
<td>strings.h</td>
<td>string operations (user-side only)</td>
</tr>
<tr>
<td>sys/mman.h</td>
<td>memory management declarations</td>
</tr>
<tr>
<td>sys/resource.h</td>
<td>definitions for XSI resource operations</td>
</tr>
<tr>
<td>sys/select.h</td>
<td>select types (user-side only)</td>
</tr>
<tr>
<td>sys/stat.h</td>
<td>data returned by the stat() function</td>
</tr>
<tr>
<td>sys/types.h</td>
<td>data types</td>
</tr>
<tr>
<td>sys/un.h</td>
<td>definitions for UNIX domain sockets</td>
</tr>
<tr>
<td>sys/utsname.h</td>
<td>system name structure (user-side only)</td>
</tr>
<tr>
<td>sys/wait.h</td>
<td>declarations for waiting (user-side only)</td>
</tr>
<tr>
<td>tgmath.h</td>
<td>type-generic macros (user-side only)</td>
</tr>
<tr>
<td>time.h</td>
<td>time types</td>
</tr>
<tr>
<td>unistd.h</td>
<td>standard symbolic constants and types</td>
</tr>
<tr>
<td>utime.h</td>
<td>access and modification times structure</td>
</tr>
<tr>
<td>wchar.h</td>
<td>wide-character handling (user-side only)</td>
</tr>
<tr>
<td>wctype.h</td>
<td>wide-character classification and mapping utilities (user-side only)</td>
</tr>
</tbody>
</table>
4.5 **POSIX Process Support**

VxWorks provides support for a user-mode process model. The POSIX APIs described in Table 4-4 are present in user mode for manipulating processes, and take a `_pid_` argument (also known as an `RTP_ID` in VxWorks). Basic VxWorks process facilities are provided with the `INCLUDE_RTP` component.

<table>
<thead>
<tr>
<th>Routine</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>atexit()</code></td>
<td>Register a handler to be called at <code>exit()</code></td>
</tr>
<tr>
<td><code>_exit()</code></td>
<td>Terminate the calling process (system call).</td>
</tr>
<tr>
<td><code>exit()</code></td>
<td>Terminate a process, calling <code>atexit()</code> handlers.</td>
</tr>
<tr>
<td><code>getpid()</code></td>
<td>Get the process ID of the current process.</td>
</tr>
<tr>
<td><code>getppid()</code></td>
<td>Get the process ID of the parent’s process ID.</td>
</tr>
<tr>
<td><code>kill()</code></td>
<td>Send a signal to a process.</td>
</tr>
<tr>
<td><code>raise()</code></td>
<td>Send a signal to the caller’s process.</td>
</tr>
<tr>
<td><code>wait()</code></td>
<td>Wait for any child process to die.</td>
</tr>
<tr>
<td><code>waitpid()</code></td>
<td>Wait for a specific child process to die.</td>
</tr>
</tbody>
</table>
4.6 POSIX Clocks and Timers

A clock is a software construct that keeps time in seconds and nanoseconds. The software clock is updated by system-clock ticks. VxWorks provides a POSIX 1003.1b standard clock and timer interface.

See Table 4-5 for a list of the POSIX clock routines. The obsolete VxWorks-specific POSIX extension `clock_setres()` is available for backwards-compatibility purposes.

<table>
<thead>
<tr>
<th>Routine</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>clock_getres()</code></td>
<td>Get the clock resolution.</td>
</tr>
<tr>
<td><code>clock_setres()</code></td>
<td>Set the clock resolution. Obsolete VxWorks-specific POSIX extension.</td>
</tr>
<tr>
<td><code>clock_gettime()</code></td>
<td>Get the current clock time.</td>
</tr>
<tr>
<td><code>clock_settime()</code></td>
<td>Set the clock to a specified time.</td>
</tr>
</tbody>
</table>

The POSIX standard provides a means of identifying multiple virtual clocks, but only one clock is required: the system-wide real-time clock. Virtual clocks are not supported in VxWorks.

The system-wide real-time clock is identified in the clock and timer routines as `CLOCK_REALTIME`, and is defined in `time.h`. VxWorks provides routines to access the system-wide real-time clock. For more information, see the kernel and application API references for `clockLib`.

The POSIX timer facility provides routines for tasks to signal themselves at some time in the future. Routines are provided to create, set, and delete a timer. For more information, see the kernel and application API references for `timerLib`. When a timer goes off, the default signal, `SIGALRM`, is sent to the task. To install a signal handler that executes when the timer expires, use the `sigaction()` routine (see 3.3.10 Signals, p.202).

See Table 4-6 for a list of the POSIX timer routines. The VxWorks `timerLib` library includes a set of VxWorks-specific POSIX extensions: `timer_open()`, `timer_close()`, `timer_cancel()`, `timer_connect()`, and `timer_unlink()`. These routines allow for an easier and more powerful use of POSIX timers on VxWorks.
Table 4-6  POSIX Timer Routines

<table>
<thead>
<tr>
<th>Routine</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>timer_create()</td>
<td>Allocate a timer using the specified clock for a timing base.</td>
</tr>
<tr>
<td>timer_delete()</td>
<td>Remove a previously created timer.</td>
</tr>
<tr>
<td>timer_open()</td>
<td>Open a name timer. VxWorks-specific POSIX extension.</td>
</tr>
<tr>
<td>timer_close()</td>
<td>Close a name timer. VxWorks-specific POSIX extension.</td>
</tr>
<tr>
<td>timer_gettime()</td>
<td>Get the remaining time before expiration and the reload value.</td>
</tr>
<tr>
<td>timer_getoverrun()</td>
<td>Return the timer expiration overrun.</td>
</tr>
<tr>
<td>timer_settime()</td>
<td>Set the time until the next expiration and arm timer.</td>
</tr>
<tr>
<td>timer_cancel()</td>
<td>Cancel a timer. VxWorks-specific POSIX extension.</td>
</tr>
<tr>
<td>timer_connect()</td>
<td>Connect a user routine to the timer signal. VxWorks-specific POSIX extension.</td>
</tr>
<tr>
<td>timer_unlink()</td>
<td>Unlink a named timer. VxWorks-specific POSIX extension.</td>
</tr>
<tr>
<td>nanosleep()</td>
<td>Suspend the current task until the time interval elapses.</td>
</tr>
<tr>
<td>sleep()</td>
<td>Delay for a specified amount of time.</td>
</tr>
<tr>
<td>alarm()</td>
<td>Set an alarm clock for delivery of a signal.</td>
</tr>
</tbody>
</table>

Example 4-1  POSIX Timers

```c
/* This example creates a new timer and stores it in timerid. */

#include <vxWorks.h>
#include <time.h>

int createTimer (void)
{
    timer_t timerid;
```
The POSIX nanosleep() routine provides specification of sleep or delay time in units of seconds and nanoseconds, in contrast to the ticks used by the VxWorks taskDelay() function. Nevertheless, the precision of both is the same, and is determined by the system clock rate; only the units differ.

To include the timerLib library in the system, configure VxWorks with the INCLUDE_POSIX_TIMERS component. To include the clockLib library, configure VxWorks with the INCLUDE_POSIX_CLOCKS component.

Process-based applications are automatically linked with the timerLib and clockLib libraries when they are compiled. The libraries are automatically initialized when the process starts.

### 4.7 POSIX Asynchronous I/O

POSIX asynchronous I/O (AIO) routines are provided by the aioPxLib library, for both kernel and user mode. The VxWorks AIO implementation meets the specification of the POSIX 1003.1 standard. For more information, see 6.6 Asynchronous Input/Output, p.338.

### 4.8 POSIX Page-Locking Interface

Many operating systems perform memory paging and swapping, which copy blocks of memory to disk and back. These techniques allow you to use more virtual memory than there is physical memory on a system. Because they impose severe and unpredictable delays in execution time, paging and swapping are undesirable in real-time systems. Consequently, VxWorks does not support this functionality.
The real-time extensions of the POSIX 1003.1 standard are used with operating systems that do perform paging and swapping. On such systems, applications that attempt real-time performance can use the POSIX page-locking facilities to protect certain blocks of memory from paging and swapping.

To facilitate porting programs between other POSIX-conforming systems and VxWorks, VxWorks therefore includes the POSIX page-locking routines. The routines have no adverse effect in VxWorks systems, because all memory is essentially always locked.

The POSIX page-locking routines are part of the memory management library, mmanPxLib, and are listed in Table 4-7. When used in VxWorks, these routines do nothing except return a value of OK (0), since all pages are always kept in memory.

To include the mmanPxLib library in the system, configure VxWorks with the INCLUDE_POSIX_MEM component.

Process-based applications are automatically linked with the mmanPxLib library when they are compiled.

Table 4-7  POSIX Page-Locking Routines

<table>
<thead>
<tr>
<th>Routine</th>
<th>Purpose on Systems with Paging or Swapping</th>
</tr>
</thead>
<tbody>
<tr>
<td>mlockall()</td>
<td>Locks into memory all pages used by a task.</td>
</tr>
<tr>
<td>munlockall()</td>
<td>Unlocks all pages used by a task.</td>
</tr>
<tr>
<td>mlock()</td>
<td>Locks a specified page.</td>
</tr>
<tr>
<td>munlock()</td>
<td>Unlocks a specified page.</td>
</tr>
</tbody>
</table>
4.9 POSIX Threads

POSIX threads (pthreads) are similar to VxWorks tasks, but with additional characteristics. VxWorks implements POSIX threads on top of native tasks, and maintains thread IDs that differ from the ID of the underlying task. POSIX threads are provided primarily for code portability—to simplify using POSIX code with VxWorks.

A major difference between VxWorks tasks and POSIX threads is the way in which options and settings are specified. For VxWorks tasks these options are set with the task creation API, usually `taskSpawn()`. On the other hand, POSIX threads have characteristics that are called *attributes*. Each attribute contains a set of values, and a set of *access routines* to retrieve and set those values. You have to specify all thread attributes in an attributes object, `pthread_attr_t`, before thread creation. In a few cases, you can dynamically modify the attribute values of a running thread.

In the kernel, POSIX threads are scheduled by the VxWorks scheduler in the same way as VxWorks tasks. So the POSIX concept of concurrent scheduling policies does not apply to threads in this environment. This introduces limitations in the usage of the scheduling-inheritance and scheduling policy attributes. Although it is possible to set the scheduling-inheritance attribute to `PTHREAD_EXPLICIT_SCHED` and the scheduling policy to `SCHED_FIFO` or `SCHED_RR`, this will trigger a failure at pthread creation time if this does not match with the current native VxWorks scheduling policy. Wind River recommends that you always use `PTHREAD_INHERIT_SCHED` as a scheduling-inheritance attribute (this is the default). In this case the current native VxWorks scheduling policy applies, and the parent thread’s priority is used. Alternatively, if the thread must be started with a different priority than its parent, the scheduling-inheritance attribute can be set to `PTHREAD_EXPLICIT_SCHED` but the scheduling policy attribute must then be `SCHED_OTHER` (which applies the current native VxWorks scheduling policy whatever it may be). If this does not meet the needs of the application and the POSIX scheduling model is required, then Wind River recommends using user-mode POSIX threads; that is, pthreads running in a process (see *VxWorks Application Programmer’s Guide: POSIX Standard Interfaces*).

The POSIX attribute-access routines are described in Table 4-8. The VxWorks-specific POSIX extension routines are described in section 4.9.1 VxWorks-Specific Thread Attributes, p.232.

For more information, see 4.10.1 Comparison of POSIX and VxWorks Scheduling, p.236 and 4.10.4 Getting and Displaying the Current Scheduling Policy, p.244.
### POSIX Thread Attribute-Access Routines

<table>
<thead>
<tr>
<th>Routine</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>pthread_attr_getstacksize()</td>
<td>Get value of the stack size attribute.</td>
</tr>
<tr>
<td>pthread_attr_setstacksize()</td>
<td>Set the stack size attribute.</td>
</tr>
<tr>
<td>pthread_attr_getstackaddr()</td>
<td>Get value of stack address attribute.</td>
</tr>
<tr>
<td>pthread_attr_setstackaddr()</td>
<td>Set value of stack address attribute.</td>
</tr>
<tr>
<td>pthread_attr_getdetachstate()</td>
<td>Get value of detachstate attribute (joinable or detached).</td>
</tr>
<tr>
<td>pthread_attr_setdetachstate()</td>
<td>Set value of detachstate attribute (joinable or detached).</td>
</tr>
<tr>
<td>pthread_attr_getscope()</td>
<td>Get contention scope. (For VxWorks only PTHREAD_SCOPE_SYSTEM is supported.)</td>
</tr>
<tr>
<td>pthread_attr_setscope()</td>
<td>Set contention scope. (For VxWorks, only PTHREAD_SCOPE_SYSTEM is supported.)</td>
</tr>
<tr>
<td>pthread_attr_getinheritsched()</td>
<td>Get value of scheduling-inheritance attribute.</td>
</tr>
<tr>
<td>pthread_attr_setinheritsched()</td>
<td>Set value of scheduling-inheritance attribute.</td>
</tr>
<tr>
<td>pthread_attr_getschedpolicy()</td>
<td>Get value of the scheduling-policy attribute (which is not used by default).</td>
</tr>
<tr>
<td>pthread_attr_setschedpolicy()</td>
<td>Set scheduling-policy attribute (which is not used by default).</td>
</tr>
<tr>
<td>pthread_attr_getschedparam()</td>
<td>Get value of scheduling priority attribute.</td>
</tr>
<tr>
<td>pthread_attr_setschedparam()</td>
<td>Set scheduling priority attribute.</td>
</tr>
<tr>
<td>pthread_attr_getopt()</td>
<td>Get the task options applying to the thread. VxWorks-specific POSIX extension.</td>
</tr>
<tr>
<td>pthread_attr_setopt()</td>
<td>Set non-default task options for the thread. VxWorks-specific POSIX extension.</td>
</tr>
<tr>
<td>pthread_attr_getname()</td>
<td>Get the name of the thread. VxWorks-specific POSIX extension.</td>
</tr>
</tbody>
</table>
There are many routines provided with the POSIX thread functionality. Table 4-9 lists a few that are directly relevant to pthread creation or execution. See the API reference for information about the other routines, and more details about all of them.

<table>
<thead>
<tr>
<th>Routine</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>pthread_attr_setname()</td>
<td>Set a non-default name for the thread. VxWorks-specific POSIX extension.</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Routine</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>pthread_create( )</td>
<td>Create a POSIX thread.</td>
</tr>
<tr>
<td>pthread_cancel( )</td>
<td>Cancel the execution of a thread.</td>
</tr>
<tr>
<td>pthread_detach( )</td>
<td>Detach a running thread so that it cannot be joined by another thread.</td>
</tr>
<tr>
<td>pthread_join()</td>
<td>Wait for a thread to terminate.</td>
</tr>
<tr>
<td>pthread_getschedparam()</td>
<td>Dynamically set value of scheduling priority attribute.</td>
</tr>
<tr>
<td>pthread_setschedparam()</td>
<td>Dynamically set scheduling priority and policy parameter.</td>
</tr>
<tr>
<td>pthread_setschedprio()</td>
<td>Dynamically set scheduling priority parameter.</td>
</tr>
<tr>
<td>sched_get_priority_max()</td>
<td>Get the maximum priority that a thread can get.</td>
</tr>
<tr>
<td>sched_get_priority_min()</td>
<td>Get the minimum priority that a thread can get.</td>
</tr>
<tr>
<td>sched_rr_get_interval()</td>
<td>Get the time quantum of execution of the Round-Robin policy.</td>
</tr>
<tr>
<td>sched_yield()</td>
<td>Relinquishes the processor.</td>
</tr>
</tbody>
</table>
4.9.1 VxWorks-Specific Thread Attributes

The VxWorks implementation of POSIX threads provides two additional thread attributes (which are POSIX extensions)—thread name and thread options—and routines for accessing them.

Thread Name

Although POSIX threads are not named entities, the VxWorks tasks upon which they are constructed are (VxWorks tasks effectively impersonate POSIX threads). By default these tasks are named pthrNumber (for example, pthr3). The number part of the name is incremented each time a new thread is created (with a roll-over at 2^32 - 1). It is, however, possible to name these tasks using the thread name attribute.

- Attribute Name: threadname
- Possible Values: a null-terminated string of characters
- Default Value: none (the default naming policy is used)
- Access Functions (VxWorks-specific POSIX extensions): pthread_attr_setname() and pthread_attr_getname()

Thread Options

POSIX threads are agnostic with regard to target architecture. Some VxWorks tasks, on the other hand, may be created with specific options in order to benefit from certain features of the architecture. For example, for the Altivec-capable PowerPC architecture, tasks must be created with the VX_ALTIVEC_TASK in order to make use of the Altivec processor. The thread options attribute can be used to set such options for the VxWorks task that impersonates the POSIX thread.

- Attribute Name: threadoptions
- Possible Values: the same as the VxWorks task options. See taskLib.h
- Default Value: none (the default task options are used)
- Access Functions (VxWorks-specific POSIX extensions): pthread_attr_setopt() and pthread_attr_getopt()
Example 4-2  Creating a pthread Using Explicit Scheduling Attributes

```c
pthread_t tid;
pthread_attr_t attr;
int ret;

pthread_attr_init(&attr);
/* set the inheritsched attribute to explicit */
 pthread_attr_setinheritsched(&attr, PTHREAD_EXPLICIT_SCHED);
/* set the schedpolicy attribute to SCHED_FIFO */
 pthread_attr_setschedpolicy(&attr, SCHED_FIFO);
/* create the pthread */
ret = pthread_create(&tid, &attr, entryFunction, entryArg);
```

Example 4-3  Creating a pthread Using Default Attributes

```c
pthread_t tid;
int ret;
/* create the pthread with NULL attributes to designate default values */
ret = pthread_create(&tid, NULL, entryFunction, entryArg);
```

Example 4-4  Designating Your Own Stack for a pthread

```c
pthread_t threadId;
pthread_attr_t attr;
void * stackaddr = NULL;
int stacksize = 0;
/* initialize the thread's attributes */
 pthread_attr_init (&attr);
/* Allocate memory for a stack region for the thread. Malloc() is used * for simplification since a real-life case is likely to use memPartAlloc() * on the kernel side, or mmap() on the user side. */
stacksize = 2 * 4096 /* let's allocate two pages */ stackaddr = malloc (stacksize);
if (stackbase == NULL)
{
    printf ("FAILED: mystack: malloc failed\n");
    return (-1);
}
/* set the stackaddr attribute */
 pthread_attr_setstackaddr (&attr, stackaddr);
```
/* set the stacksize attribute */
pthread_attr_setstacksize (&attr, stacksize);
/* set the schedpolicy attribute to SCHED_FIFO */
pthread_attr_setschedpolicy (&attr, SCHED_FIFO);
/* create the pthread */
ret = pthread_create (&threadId, &attr, mystack_thread, 0);

4.9.3 Thread Private Data

POSIX threads can store and access private data; that is, thread-specific data. They use a key maintained for each pthread by the pthread library to access that data. A key corresponds to a location associated with the data. It is created by calling pthread_key_create() and released by calling pthread_key_delete(). The location is accessed by calling pthread_getspecific() and pthread_setspecific(). This location represents a pointer to the data, and not the data itself, so there is no limitation on the size and content of the data associated with a key.

The pthread library supports a maximum of 256 keys for all the threads in the kernel.

The pthread_key_create() routine has an option for a destructor function, which is called when the creating thread exits or is cancelled, if the value associated with the key is non-NULL.

This destructor function frees the storage associated with the data itself, and not with the key. It is important to set a destructor function for preventing memory leaks to occur when the thread that allocated memory for the data is cancelled. The key itself should be freed as well, by calling pthread_key_delete(), otherwise the key cannot be reused by the pthread library.

4.9.4 Thread Cancellation

POSIX provides a mechanism, called cancellation, to terminate a thread gracefully. There are two types of cancellation: deferred and asynchronous.

Deferred cancellation causes the thread to explicitly check to see if it was cancelled. This happens in one of the following ways:
The code of the thread executes calls to `pthread_testcancel()` at regular interval.

The thread calls a function that contains a cancellation point during which the thread may be automatically cancelled.

Asynchronous cancellation causes the execution of the thread to be forcefully interrupted and a handler to be called, much like a signal.\(^1\)

Automatic cancellation points are library routines that can block the execution of the thread for a lengthy period of time. Note that although the `msync()`, `fcntl()`, and `tcdrain()` routines are mandated POSIX 1003.1 cancellation points, they are not provided with VxWorks for this release.

The POSIX cancellation points provided in VxWorks libraries (kernel and application) are described in Table 4-10.

<table>
<thead>
<tr>
<th>Library</th>
<th>Routines</th>
</tr>
</thead>
<tbody>
<tr>
<td>aioPxLib</td>
<td><code>aio_suspend()</code></td>
</tr>
<tr>
<td>ioLib</td>
<td><code>creat()</code>, <code>open()</code>, <code>read()</code>, <code>write()</code>, <code>close()</code>, <code>fsync()</code>, <code>fdatasync()</code></td>
</tr>
<tr>
<td>mqPxLib</td>
<td><code>mq_receive()</code>, <code>mq_send()</code></td>
</tr>
<tr>
<td>pthreadLib</td>
<td><code>pthread_cond_timedwait()</code>, <code>pthread_cond_wait()</code>, <code>pthread_join()</code>, <code>pthread_testcancel()</code></td>
</tr>
<tr>
<td>semPxLib</td>
<td><code>sem_wait()</code></td>
</tr>
<tr>
<td>sigLib</td>
<td><code>pause()</code>, <code>sigsuspend()</code>, <code>sigtimedwait()</code>, <code>sigwait()</code>, <code>sigwaitinfo()</code>, <code>waitpid()</code></td>
</tr>
<tr>
<td>timerLib</td>
<td><code>sleep()</code>, <code>nanosleep()</code></td>
</tr>
</tbody>
</table>

Note: The `waitpid()` routine is available only in user-mode. It is not available in the kernel.

Table 4-11 lists routines that can be used with cancellation points of pthreads.

---

1. Asynchronous cancellation is actually implemented with a special signal, `SIGCNCL`, which users should be careful not to block or to ignore.
4.10 POSIX Scheduling

4.10.1 Comparison of POSIX and VxWorks Scheduling

VxWorks provides two different schedulers: the native VxWorks scheduler and the POSIX thread scheduler. Only one of them can be used at a time and their relationships and differences to POSIX are described below.

Also see 2.11 Kernel Schedulers, p.116.

Native VxWorks Scheduler

- The native VxWorks scheduler is the original VxWorks scheduler. POSIX and the native VxWorks scheduling differ in the following ways:
- POSIX supports a two-level scheduling model that supports the concept known as contention scope, by which the scheduling of threads (that is, how

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Table 4-11 Thread Cancellation Routines

<table>
<thead>
<tr>
<th>Routine</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>pthread_cancel()</td>
<td>Cancel execution of a thread.</td>
</tr>
<tr>
<td>pthread_testcancel()</td>
<td>Create a cancellation point in the calling thread.</td>
</tr>
<tr>
<td>pthread_setcancelstate()</td>
<td>Enables or disables cancellation.</td>
</tr>
<tr>
<td>pthread_setcanceltype()</td>
<td>Selects deferred or asynchronous cancellation.</td>
</tr>
<tr>
<td>pthread_cleanup_push()</td>
<td>Registers a function to be called when the thread is cancelled, exits, or calls pthread_cleanup_pop() with a non-null run parameter.</td>
</tr>
<tr>
<td>pthread_cleanup_pop()</td>
<td>Unregisters a function previously registered with pthread_cleanup_push(). This function is immediately executed if the run parameter is non-null.</td>
</tr>
</tbody>
</table>
POSIX Standard Interfaces

4.10 POSIX Scheduling

they compete for the CPU) can apply system wide or on a process basis. In contrast, VxWorks scheduling is based system wide on tasks and pthreads—in the kernel and in processes. VxWorks real-time processes cannot themselves be scheduled.

- POSIX applies scheduling algorithms on a process-by-process and thread-by-thread basis. VxWorks applies scheduling algorithms on a system-wide basis, for all tasks and pthreads, whether in the kernel or in processes. This means that all tasks and pthreads use either a preemptive priority scheme or a round-robin scheme.

- POSIX supports the concept of scheduling allocation domain; that is, the association between processes or threads and processors. VxWorks does not support multi-processor hardware then there is only one domain on VxWorks and all the tasks and pthreads are associated to it.

The VxWorks scheduling policies are very similar to the POSIX ones, so when the native scheduler is in place the POSIX, the scheduling policies are simply mapped on the VxWorks ones:

- SCHED_FIFO is mapped on VxWorks' preemptive priority scheduling.
- SCHED_RR is mapped on VxWorks' round-robin scheduling.
- SCHED_OTHER corresponds to the currently active VxWorks scheduling policy. This policy is the one used by default by all pthreads.

There is one minor difference between POSIX and VxWorks:

- The POSIX priority numbering scheme is the inverse of the VxWorks scheme. In POSIX, the higher the number, the higher the priority; in the VxWorks scheme, the lower the number, the higher the priority, where 0 is the highest priority. Accordingly, the priority numbers used with the POSIX scheduling library, schedPxLib, do not match those used and reported by all other components of VxWorks. You can override this default by setting the global variable posixPriorityNumbering to FALSE. If you do this, schedPxLib uses the VxWorks numbering scheme (a smaller number means a higher priority) and its priority numbers match those used by the other components of VxWorks.

POSIX Threads Scheduler

Although it is possible to use the native scheduler for VxWorks tasks in a process (RTP) as well as for POSIX threads in the kernel, the POSIX threads scheduler must be used if pthreads are used in processes. Failure to configure the operating system
with INCLUDE_POSIX_PTHREAD_SCHEDULER makes it impossible to create threads in a process.

The POSIX threads scheduler is conformant with POSIX 1003.1. This scheduler still applies to all tasks and threads in the system, but only the user-side POSIX threads (that is pthreads executing in processes) are scheduled accordingly to POSIX. VxWorks tasks in the kernel and in processes, and pthreads in the kernel, are scheduled accordingly to the VxWorks scheduling model.

When the POSIX threads scheduler is included in the system it is possible to assign a different scheduling policy to each pthread and change a pthread’s scheduling policy dynamically. See Configuring VxWorks for POSIX Thread Scheduling, p.238, for more information.

The POSIX scheduling policies are as follows:

- SCHED_FIFO is first in, first out, preemptive priority scheduling.
- SCHED_RR is round-robin, time-bound preemptive priority scheduling.
- SCHED_OTHER strictly corresponds to the current native VxWorks scheduling policy (that is, a thread using this policy is scheduled exactly like a VxWorks task. This can be useful for backward-compatibility reasons).
- SCHED_SPORADIC is preemptive scheduling with variable priority. It is not supported on VxWorks.

Configuring VxWorks for POSIX Thread Scheduling

To enable the POSIX thread scheduling support for threads in processes, the component INCLUDE_POSIX_PTHREAD_SCHEDULER must be included in VxWorks. This configuration applies strictly to threads in processes and does not apply to threads in the kernel. The INCLUDE_POSIX_PTHREAD_SCHEDULING component has a dependency on the INCLUDE_RTP component since this POSIX thread scheduling support only applies to threads in processes.

For the SCHED_RR policy threads, the configuration parameter POSIX_PTHREAD_RR_TIMESLICE may be used to configure the default time slicing interval. To modify the time slice at run time, the routine kernelTimeSlice() may be called with a different time slice value. The updated time slice value only affects new threads created after the kernelTimeSlice() call.

INCLUDE_POSIX_PTHREAD_SCHEDULER is a standalone component that is not depended on by other POSIX components. If POSIX threads are configured with INCLUDE_POSIX_PTHREADS, the POSIX scheduler is not automatically included. Explicit inclusion of INCLUDE_POSIX_PTHREAD_SCHEDULING must be done to get the POSIX thread scheduling behavior. This enables VxWorks to support
processes with (for POSIX conforming-applications) or without POSIX thread scheduling.

Once the POSIX thread scheduler is configured, all POSIX RTP applications using threads will have the expected POSIX-conforming thread scheduling behavior.

The inclusion of the INCLUDE_POSIX_PTHREAD_SCHEDULER component does not have an impact on VxWorks task scheduling since the VxWorks task scheduling decision has not been changed to support POSIX threads in user space.

Scheduling Behaviors

VxWorks tasks and POSIX threads, regardless of the policy, share a single priority range and the same priority based queuing scheme. Both VxWorks tasks and POSIX threads use the same queuing mechanism to schedule threads and tasks to run; and thus all tasks and threads share a global scheduling scheme.

The inclusion of the POSIX thread scheduling will have minimal impact on kernel tasks, since VxWorks task scheduling behavior is preserved. However, minor side effects may occur. When the POSIX scheduler is configured, the fairness expectation of VxWorks tasks in a system configured with round robin scheduling may not be achieved, because POSIX threads with the SCHED_FIFO policy, and at the same priority as the VxWorks tasks, may potentially usurp the CPU. Starvation of the VxWorks round robin tasks may occur. Care must be taken when mixing VxWorks round robin tasks and threads using SCHED_RR and SCHED_FIFO policies.

Threads with the SCHED_OTHER policy behave the same as the default VxWorks system-wide scheduling scheme. In other words, VxWorks configured with round robin scheduling means that threads created with the SCHED_OTHER policy will also execute in the round robin mode. VxWorks round robin will not affect POSIX threads created with the SCHED_RR and SCHED_FIFO policies but will affect POSIX threads created with the SCHED_OTHER policy.

One difference in the scheduling behavior when the POSIX scheduler is configured is that threads may be placed at the head of a priority list when the thread is lowered by a call to the POSIX pthread_setschedprio() routine. The lowering of the thread places the lowered thread at the head of its priority list. This is different from VxWorks task scheduling when tasks are lowered using the taskPrioritySet() routine, where the lowered task will be placed at the end of its priority list. The significance of this change is that threads that were of higher priority, when lowered, are considered to have more preference than tasks and threads in its lowered priority list.
The addition of the POSIX scheduler will change the behavior of existing POSIX applications. For existing applications that require backward-compatibility, the POSIX applications can change their scheduling policy to `SCHED_OTHER` for all POSIX threads since the `SCHED_OTHER` threads defaults to the VxWorks system-wide scheduling scheme as in previous versions of VxWorks.

Mixing POSIX thread APIs and VxWorks APIs in an application is not recommended, and may make a POSIX application non-POSIX conformant.

For information about VxWorks scheduling, see 3.2.2 Task Scheduling, p.134.

### 4.10.2 POSIX Scheduling Model

**CAUTION:** The API part of the `_POSIX_PRIORITY_SCHEDULING` option, and provided by `schedPxLib` on VxWorks, does not currently support processes (RTPs) and are simply meant to be used for VxWorks tasks or POSIX threads.

The POSIX 1003.1b scheduling routines, provided by `schedPxLib`, are shown in Table 4-12. These routines provide a portable interface for:

- Getting and setting task priority.
- Getting and setting scheduling policy.
- Getting the maximum and minimum priorities for tasks.
- If round-robin scheduling is in effect, getting the length of a time slice.

This section describes how to use these routines, beginning with a list of the minor differences between the POSIX and VxWorks methods of scheduling.

<table>
<thead>
<tr>
<th>Routine</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>sched_setparam()</code></td>
<td>Sets a task’s priority.</td>
</tr>
<tr>
<td><code>sched_getparam()</code></td>
<td>Gets the scheduling parameters for a specified task.</td>
</tr>
<tr>
<td><code>sched_setscheduler()</code></td>
<td>Sets the scheduling policy and parameters for a task (kernel-only routine).</td>
</tr>
<tr>
<td><code>sched_yield()</code></td>
<td>Relinquishes the CPU.</td>
</tr>
<tr>
<td><code>sched_getscheduler()</code></td>
<td>Gets the current scheduling policy.</td>
</tr>
</tbody>
</table>

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To include the `schedPxLib` library in the system, configure VxWorks with the `INCLUDE_POSIX_SCHED` component.

Process-based applications are automatically linked with the `schedPxLib` library when they are compiled.

4.10.3 Getting and Setting Task Priorities

⚠️ **CAUTION:** The `sched_setparam()` and `sched_getparam()` routines do not currently support the POSIX thread scheduler with this release, and are simply meant to be used with VxWorks tasks. POSIX threads have their own API which should be used instead: `pthread_setschedparam()` and `pthread_getschedparam()`.

The routines `sched_setparam()` and `sched_getparam()` set and get a task’s priority, respectively. Both routines take a task ID and a `sched_param` structure (defined in `installDir/vxworks-6.x/target/h/sched.h` for kernel code and `installDir/vxworks-6.x/target/usr/h/sched.h` for user-space application code). A task ID of 0 sets or gets the priority for the calling task.

The `sched_setparam()` routine writes the specified task’s current priority into the the `sched_priority` member of the `sched_param` structure that is passed in.
Example 4-5  Getting and Setting POSIX Task Priorities

/* This example sets the calling task's priority to 150, then verifies
 * that priority. To run from the shell, spawn as a task:
 * -> sp priorityTest
 */

/* includes */
#include <vxWorks.h>
#include <sched.h>

/* defines */
#define PX_NEW_PRIORITY 150

STATUS priorityTest (void)
{
     struct sched_param myParam;
     /* initialize param structure to desired priority */
     myParam.sched_priority = PX_NEW_PRIORITY;
     if (sched_setparam (0, &myParam) == ERROR)
     {
         printf ("error setting priority\n");
         return (ERROR);
     }

     /* demonstrate getting a task priority as a sanity check; ensure it
      * is the same value that we just set.
      */
     if (sched_getparam (0, &myParam) == ERROR)
     {
         printf ("error getting priority\n");
         return (ERROR);
     }
     if (myParam.sched_priority != PX_NEW_PRIORITY)
     {
         printf ("error - priorities do not match\n");
         return (ERROR);
     }
     else
     {
         printf ("task priority = %d\n", myParam.sched_priority);
     }
     return (OK);
}

The routine sched_setscheduler() is designed to set both scheduling policy and
priority for a single POSIX process. Its behavior is, however, necessarily different
for VxWorks because of differences in scheduling functionality.

All scheduling in VxWorks is done at the task level—processes themselves are not
scheduled—and all tasks have the same scheduling policy. Therefore, the
implementation of `sched_setscheduler()` for VxWorks only controls task priority, and only when the policy specification used in the call matches the system-wide policy. If it does not, the call fails. In other words:

- If the policy specification defined with a `sched_setscheduler()` call matches the current system-wide scheduling policy, the task priority is set to the new value (thereby acting like the `sched_setparam()` routine).

- If the policy specification defined with a `sched_setscheduler()` call does not match the current system-wide scheduling policy, it returns an error, and the priority of the task is not changed.

In VxWorks, the only way to change the scheduling policy is to change it for all tasks in the system. There is no POSIX routine for this purpose, and for security reasons, the scheduling policy cannot be changed from user mode. To set a system-wide scheduling policy, use the VxWorks kernel routine `kernelTimeSlice()`, which is described in *Round-Robin Scheduling*, p.136.
4.10.4 Getting and Displaying the Current Scheduling Policy

The POSIX routine `sched_getscheduler()` returns the current scheduling policy. There are the only two valid scheduling policies in VxWorks when the native scheduler is active: preemptive priority scheduling (in POSIX terms, `SCHED_FIFO`) and round-robin scheduling by priority (`SCHED_RR`).

Example 4-6 Getting POSIX Scheduling Policy

```c
/* This example gets the scheduling policy and displays it. */
/* includes */
#include <vxWorks.h>
#include <sched.h>

STATUS schedulerTest (void)
{
    int policy;
    if ((policy = sched_getscheduler (0)) == ERROR)
    {
        printf ("getting scheduler failed\n");
        return (ERROR);
    }
    /* sched_getscheduler returns either SCHED_FIFO or SCHED_RR */
    if (policy == SCHED_FIFO)
        printf ("current scheduling policy is FIFO\n");
    else
        printf ("current scheduling policy is round robin\n");
    return (OK);
}
```

4.10.5 Getting Scheduling Parameters: Priority Limits and Time Slice

The routines `sched_get_priority_max()` and `sched_get_priority_min()` return the maximum and minimum possible POSIX priority, respectively.

If round-robin scheduling is enabled, you can use `sched_rr_get_interval()` to determine the length of the current time-slice interval. This routine takes as an argument a pointer to a `timespec` structure (defined in `time.h`), and writes the number of seconds and nanoseconds per time slice to the appropriate elements of that structure.
Example 4-7  Getting the POSIX Round-Robin Time Slice

/* The following example checks that round-robin scheduling is enabled, */
/* gets the length of the time slice, and then displays the time slice. */

/* includes */
#include <vxWorks.h>
#include <sched.h>

STATUS rrgetintervalTest (void)
{
    struct timespec slice;
    /* turn on round robin */
    kernelTimeSlice (30);
    if (sched_rr_get_interval (0, &slice) == ERROR)
    {
        printf ("get-interval test failed\n");
        return (ERROR);
    }
    printf ("time slice is %l seconds and %l nanoseconds\n", slice.tv_sec, slice.tv_nsec);
    return (OK);
}

4.11 POSIX Semaphores

POSIX defines both named and unnamed semaphores, which have the same properties, but which use slightly different interfaces. The POSIX semaphore library provides routines for creating, opening, and destroying both named and unnamed semaphores.

When opening a named semaphore, you assign a symbolic name, which the other named-semaphore routines accept as an argument. The POSIX semaphore routines provided by semPxLib are shown in Table 4-13.
To include the POSIX semPxLib library semaphore routines in the system, configure VxWorks with the INCLUDE_POSIX_SEM component.

VxWorks also provides semPxLibInit(), a non-POSIX (kernel-only) routine that initializes the kernel’s POSIX semaphore library. It is called by default at boot time when POSIX semaphores have been included in the VxWorks configuration.

Process-based applications are automatically linked with the semPxLib library when they are compiled. The library is automatically initialized when the process starts.

### 4.11.1 Comparison of POSIX and VxWorks Semaphores

POSIX semaphores are counting semaphores; that is, they keep track of the number of times they are given. The VxWorks semaphore mechanism is similar to that

2. Some operating systems, such as UNIX, require symbolic names for objects that are to be shared among processes. This is because processes do not normally share memory in such operating systems. In VxWorks, named semaphores can be used to share semaphores between real-time processes. In the VxWorks kernel there is no need for named semaphores, because all kernel objects have unique identifiers. However, using named semaphores of the POSIX variety provides a convenient way of determining the object’s ID.
specified by POSIX, except that VxWorks semaphores offer these additional features:

- priority inheritance
- task-deletion safety
- the ability for a single task to take a semaphore multiple times
- ownership of mutual-exclusion semaphores
- semaphore timeouts
- queuing mechanism options

When these features are important, VxWorks semaphores are preferable to POSIX semaphores. (For information about these features, see 3. Multitasking.)

The POSIX terms `wait` (or `lock`) and `post` (or `unlock`) correspond to the VxWorks terms `take` and `give`, respectively. The POSIX routines for locking, unlocking, and getting the value of semaphores are used for both named and unnamed semaphores.

The routines `sem_init()` and `sem_destroy()` are used for initializing and destroying unnamed semaphores only. The `sem_destroy()` call terminates an unnamed semaphore and deallocates all associated memory.

The routines `sem_open()`, `sem_unlink()`, and `sem_close()` are for opening and closing (destroying) named semaphores only. The combination of `sem_close()` and `sem_unlink()` has the same effect for named semaphores as `sem_destroy()` does for unnamed semaphores. That is, it terminates the semaphore and deallocates the associated memory.

**WARNING:** When deleting semaphores, particularly mutual-exclusion semaphores, avoid deleting a semaphore still required by another task. Do not delete a semaphore unless the deleting task first succeeds in locking that semaphore. Similarly for named semaphores, close semaphores only from the same task that opens them.

### 4.11.2 Using Unnamed Semaphores

When using unnamed semaphores, typically one task allocates memory for the semaphore and initializes it. A semaphore is represented with the data structure `sem_t`, defined in `semaphore.h`. The semaphore initialization routine, `sem_init()`, lets you specify the initial value.
Once the semaphore is initialized, any task can use the semaphore by locking it with `sem_wait()` (blocking) or `sem_trywait()` (non-blocking), and unlocking it with `sem_post()`.

Semaphores can be used for both synchronization and exclusion. Thus, when a semaphore is used for synchronization, it is typically initialized to zero (locked). The task waiting to be synchronized blocks on a `sem_wait()`. The task doing the synchronizing unlocks the semaphore using `sem_post()`. If the task that is blocked on the semaphore is the only one waiting for that semaphore, the task unblocks and becomes ready to run. If other tasks are blocked on the semaphore, the task with the highest priority is unblocked.

When a semaphore is used for mutual exclusion, it is typically initialized to a value greater than zero, meaning that the resource is available. Therefore, the first task to lock the semaphore does so without blocking, setting the semaphore to 0 (locked). Subsequent tasks will block until the semaphore is released. As with the previous scenario, when the semaphore is released the task with the highest priority is unblocked.

When used in a user application, unnamed semaphores can be accessed only by the tasks belonging to the process executing the application. Only named semaphores can be shared between user applications (that is, different processes).

Example 4-8  POSIX Unnamed Semaphores

```c
/* This example uses unnamed semaphores to synchronize an action between the * calling task and a task that it spawns (tSyncTask). To run from the shell, * spawn as a task: * -> sp unnameSem */

/* includes */
#include <vxWorks.h>
#include <semaphore.h>

/* forward declarations */
void syncTask (sem_t * pSem);

void unnameSem (void)
{
    sem_t * pSem;

    /* reserve memory for semaphore */
    pSem = (sem_t *) malloc (sizeof (sem_t));
```
/* initialize semaphore to unavailable */
if (sem_init (pSem, 0, 0) == -1)
{
    printf ("unnameSem: sem_init failed\n");
    free ((char *) pSem);
    return;
}

/* create sync task */
printf ("unnameSem: spawning task...");
taskSpawn ("tSyncTask", 90, 0, 2000, syncTask, pSem);

/* do something useful to synchronize with syncTask */
/* unlock sem */
printf ("unnameSem: posting semaphore - synchronizing action\n");
if (sem_post (pSem) == -1)
{
    printf ("unnameSem: posting semaphore failed\n");
    sem_destroy (pSem);
    free ((char *) pSem);
    return;
}

/* all done - destroy semaphore */
if (sem_destroy (pSem) == -1)
{
    printf ("unnameSem: sem_destroy failed\n");
    return;
}
free ((char *) pSem);

void syncTask
(
    sem_t * pSem
)
{
    /* wait for synchronization from unnameSem */
    if (sem_wait (pSem) == -1)
    {
        printf ("syncTask: sem_wait failed \n");
        return;
    }
else
    printf ("syncTask: sem locked; doing sync'ed action\n");

    /* do something useful here */
}
4.11.3 Using Named Semaphores

The `sem_open()` routine either opens a named semaphore that already exists or, as an option, creates a new semaphore. You can specify which of these possibilities you want by combining the following flag values:

- **O_CREAT**
  Create the semaphore if it does not already exist. If it exists, either fail or open the semaphore, depending on whether **O_EXCL** is specified.

- **O_EXCL**
  Open the semaphore only if newly created; fail if the semaphore exists.

The results, based on the flags and whether the semaphore accessed already exists, are shown in Table 4-14. There is no entry for **O_EXCL** alone, because using that flag alone is not meaningful.

<table>
<thead>
<tr>
<th>Flag Settings</th>
<th>If Semaphore Exists</th>
<th>If Semaphore Does Not Exist</th>
</tr>
</thead>
<tbody>
<tr>
<td>None</td>
<td>Semaphore is opened.</td>
<td>Routine fails.</td>
</tr>
<tr>
<td><strong>O_CREAT</strong></td>
<td>Semaphore is opened.</td>
<td>Semaphore is created.</td>
</tr>
<tr>
<td><strong>O_CREAT</strong> and <strong>O_EXCL</strong></td>
<td>Routine fails.</td>
<td>Semaphore is created.</td>
</tr>
<tr>
<td><strong>O_EXCL</strong></td>
<td>Routine fails.</td>
<td>Routine fails.</td>
</tr>
</tbody>
</table>

Once initialized, a semaphore remains usable until explicitly destroyed. Tasks can explicitly mark a semaphore for destruction at any time, but the system only destroys the semaphore when no task has the semaphore open.

If VxWorks is configured with **INCLUDE_POSIX_SEM_SHOW**, you can use `show()` from the shell (with the C interpreter) to display information about a POSIX semaphore.3

This example shows information about the POSIX semaphore `mySem` with two tasks blocked and waiting for it:

```c
-> show semId
value = 0 = 0x0
```

3. The `show()` routine is not a POSIX routine, nor is it meant to be used programmatically. It is designed for interactive use with the shell (with the shell’s C interpreter).
Semaphore name : mySem | sem_open() count : 3 | Semaphore value : 0 | No. of blocked tasks : 2

For a group of collaborating tasks to use a named semaphore, one of the tasks first creates and initializes the semaphore, by calling `sem_open()` with the `O_CREAT` flag. Any task that needs to use the semaphore thereafter, opens it by calling `sem_open()` with the same name, but without setting `O_CREAT`. Any task that has opened the semaphore can use it by locking it with `sem_wait()` (blocking) or `sem_trywait()` (non-blocking), and then unlocking it with `sem_post()` when the task is finished with the semaphore.

To remove a semaphore, all tasks using it must first close it with `sem_close()`, and one of the tasks must also unlink it. Unlinking a semaphore with `sem_unlink()` removes the semaphore name from the name table. After the name is removed from the name table, tasks that currently have the semaphore open can still use it, but no new tasks can open this semaphore. If a task tries to open the semaphore without the `O_CREAT` flag, the operation fails. An unlinked semaphore is deleted by the system when the last task closes it.

POSIX named semaphores may be shared between processes only if their names start with a `/` (forward slash) character. They are otherwise private to the process in which they were created, and cannot be accessed from another process.

**Example 4-9  POSIX Named Semaphores**

```c
/*
* In this example, nameSem() creates a task for synchronization. The
* new task, tSyncSemTask, blocks on the semaphore created in nameSem().
* Once the synchronization takes place, both tasks close the semaphore,
* and nameSem() unlinks it. To run this task from the shell, spawn
* nameSem as a task:
* -> sp nameSem, "myTest"
*/

/* includes */
#include <vxWorks.h>
#include <semaphore.h>
#include <fcntl.h>

/* forward declaration */
int syncSemTask (char * name);

int nameSem
{
    char * name
}
{
    sem_t * semId;
```
/* create a named semaphore, initialize to 0*/
printf ("nameSem: creating semaphore\n");
if ((semId = sem_open (name, O_CREAT, 0, 0)) == (sem_t *) -1)
{
    printf ("nameSem: sem_open failed\n");
    return;
}

printf ("nameSem: spawning sync task\n");
taskSpawn ("tSyncSemTask", 90, 0, 2000, syncSemTask, name);
/* do something useful to synchronize with syncSemTask */
/* give semaphore */
printf ("nameSem: posting semaphore - synchronizing action\n");
if (sem_post (semId) == -1)
{
    printf ("nameSem: sem_post failed\n");
    return;
}

/* all done */
if (sem_close (semId) == -1)
{
    printf ("nameSem: sem_close failed\n");
    return;
}
if (sem_unlink (name) == -1)
{
    printf ("nameSem: sem_unlink failed\n");
    return;
}
printf ("nameSem: closed and unlinked semaphore\n");
}

int syncSemTask
{
    char * name
}
{
    sem_t * semId;
    /* open semaphore */
printf ("syncSemTask: opening semaphore\n");
if ((semId = sem_open (name, 0)) == (sem_t *) -1)
{
    printf ("syncSemTask: sem_open failed\n");
    return;
}
/* block waiting for synchronization from nameSem */
printf("syncSemTask: attempting to take semaphore...\n");
if (sem_wait (semId) == -1)
{
    printf("syncSemTask: taking sem failed\n");
    return;
}

printf("syncSemTask: has semaphore, doing sync’ed action ...\n");
/* do something useful here */
if (sem_close (semId) == -1)
{
    printf("syncSemTask: sem_close failed\n");
    return;
}
}
4.12 POSIX Mutexes and Condition Variables

Thread mutexes (mutual exclusion variables) and condition variables provide compatibility with the POSIX 1003.1c standard. They perform essentially the same role as VxWorks mutual exclusion and binary semaphores (and are in fact implemented using them). They are available with pthreadLib. Like POSIX threads, mutexes and condition variables have attributes associated with them. Mutex attributes are held in a data type called pthread_mutexattr_t, which contains two attributes, protocol and prioceiling.

For information about VxWorks mutual exclusion and binary semaphores, see 3.3.4 Semaphores, p.162, as well as the API references for semBLib and semMLib.

Protocol

The protocol mutex attribute describes how the mutex variable deals with the priority inversion problem described in the section for VxWorks mutual-exclusion semaphores (Mutual-Exclusion Semaphores, p.167).

- Attribute Name: protocol
- Possible Values: PTHREAD_PRIO_INHERIT (default) and PTHREAD_PRIO_PROTECT
- Access Routines: pthread_mutexattr_getprotocol() and pthread_mutexattr_setprotocol()

To create a mutual-exclusion variable with priority inheritance, use the PTHREAD_PRIO_INHERIT value (which is equivalent to the association of SEM_Q_PRIORITY and INHERITSEM_INVERSION_SAFE options with semMCreate()). A thread owning a mutex variable created with the PTHREAD_PRIO_INHERIT value inherits the priority of any higher-priority thread waiting for this mutex and executes at this elevated priority until it releases the mutex, at which points it returns to its original priority. The PTHREAD_PRIO_INHERIT option is the default value for the protocol attribute.

Because it might not be desirable to elevate a lower-priority thread to a too-high priority, POSIX defines the notion of priority ceiling, described below. Mutual-exclusion variables created with priority protection use the PTHREAD_PRIO_PROTECT value.

Priority Ceiling

The prioceiling attribute is the POSIX priority ceiling for mutex variables created with the protocol attribute set to PTHREAD_PRIO_PROTECT.
Attribute Name: `prioceiling`

Possible Values: any valid (POSIX) priority value (0-255, with zero being the lowest).

Access Routines: `pthread_mutexattr_getprioceiling()` and `pthread_mutexattr_setprioceiling()`

Dynamic Access Routines: `pthread_mutex_getprioceiling()` and `pthread_mutex_setprioceiling()`

Note that the POSIX priority numbering scheme is the inverse of the VxWorks scheme. See 4.10.1 Comparison of POSIX and VxWorks Scheduling, p.236.

A priority ceiling is defined by the following conditions:

- Any thread attempting to acquire a mutex, whose priority is higher than the ceiling, cannot acquire the mutex.
- Any thread whose priority is lower than the ceiling value has its priority elevated to the ceiling value for the duration that the mutex is held.
- The thread’s priority is restored to its previous value when the mutex is released.
4.13 POSIX Message Queues

The POSIX message queue routines, provided by \texttt{mqPxLib}, are shown in Table 4-15.

<table>
<thead>
<tr>
<th>Routine</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>\texttt{mq_open()}</td>
<td>Opens a message queue.</td>
</tr>
<tr>
<td>\texttt{mq_close()}</td>
<td>Closes a message queue.</td>
</tr>
<tr>
<td>\texttt{mq_unlink()}</td>
<td>Removes a message queue.</td>
</tr>
<tr>
<td>\texttt{mq_send()}</td>
<td>Sends a message to a queue.</td>
</tr>
<tr>
<td>\texttt{mq_receive()}</td>
<td>Gets a message from a queue.</td>
</tr>
<tr>
<td>\texttt{mq_notify()}</td>
<td>Signals a task that a message is waiting on a queue.</td>
</tr>
<tr>
<td>\texttt{mq_setattr()}</td>
<td>Sets a queue attribute.</td>
</tr>
<tr>
<td>\texttt{mq_getattr()}</td>
<td>Gets a queue attribute.</td>
</tr>
</tbody>
</table>

The VxWorks initialization routine \texttt{mqPxLibInit()} initializes the kernel’s POSIX message queue library (this is a kernel-only routine). It is called automatically at boot time when the \texttt{INCLUDE\_POSIX\_MQ} component is part of the system.

For information about the VxWorks message queue library, see the \texttt{msgQLib} API reference.

4.13.1 Comparison of POSIX and VxWorks Message Queues

The POSIX message queues are similar to VxWorks message queues, except that POSIX message queues provide messages with a range of priorities. The differences between the POSIX and VxWorks message queues are summarized in Table 4-16.
4.13 POSIX Message Queues

4.13.2 POSIX Message Queue Attributes

A POSIX message queue has the following attributes:

- an optional O_NONBLOCK flag, which prevents a `mq_receive()` call from being a blocking call if the message queue is empty
- the maximum number of messages in the message queue
- the maximum message size
- the number of messages currently on the queue

Tasks can set or clear the O_NONBLOCK flag using `mq_setattr()`, and get the values of all the attributes using `mq_getattr()`. (As allowed by POSIX, this implementation of message queues makes use of a number of internal flags that are not public.)

<table>
<thead>
<tr>
<th>Feature</th>
<th>VxWorks Message Queues</th>
<th>POSIX Message Queues</th>
</tr>
</thead>
<tbody>
<tr>
<td>Message Priority Levels</td>
<td>1</td>
<td>32</td>
</tr>
<tr>
<td>Blocked Task Queues</td>
<td>FIFO or priority-based</td>
<td>Priority-based</td>
</tr>
<tr>
<td>Receive with Timeout</td>
<td>Optional</td>
<td>Not available in VxWorks</td>
</tr>
<tr>
<td>Task Notification</td>
<td>Not available</td>
<td>Optional (one task)</td>
</tr>
<tr>
<td>Close/Unlink Semantics</td>
<td>No</td>
<td>Yes</td>
</tr>
</tbody>
</table>

Table 4-16 Message Queue Feature Comparison

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Example 4-10  Setting and Getting Message Queue Attributes

/*
 * This example sets the O_NONBLOCK flag and examines message queue
 * attributes.
 */

/* includes */
#include <vxWorks.h>
#include <mqueue.h>
#include <fcntl.h>
#include <errno.h>

/* defines */
#define MSG_SIZE    16

int attrEx
    (char * name)
{
    mqd_t mqPXId; /* mq descriptor */
    struct mq_attr attr; /* queue attribute structure */
    struct mq_attr oldAttr; /* old queue attributes */
    char buffer[MSG_SIZE];
    int prio;

    /* create read write queue that is blocking */
    attr.mq_flags = 0;
    attr.mq_maxmsg = 1;
    attr.mq_msgsize = 16;
    if ((mqPXId = mq_open (name, O_CREAT | O_RDWR , 0, &attr))
        == (mqd_t) -1)
        return (ERROR);
    else
        printf ("mq_open with non-block succeeded\n");

    /* change attributes on queue - turn on non-blocking */
    attr.mq_flags = O_NONBLOCK;
    if (mq_setattr (mqPXId, &attr, &oldAttr) == -1)
        return (ERROR);
    else
    {
        /* paranoia check - oldAttr should not include non-blocking. */
        if (oldAttr.mq_flags & O_NONBLOCK)
            return (ERROR);
        else
            printf ("mq_setattr turning on non-blocking succeeded\n");
    }
/* try receiving - there are no messages but this shouldn’t block */
if (mq_receive (mqPXId, buffer, MSG_SIZE, &prio) == -1)
    {
        if (errno != EAGAIN)
            return (ERROR);
        else
            printf ("mq_receive with non-blocking didn’t block on empty queue\n");
    }
else
    return (ERROR);

/* use mq_getattr to verify success */
if (mq_getattr (mqPXId, &oldAttr) == -1)
    return (ERROR);
else
    {
        /* test that we got the values we think we should */
        if (!(oldAttr.mq_flags & O_NONBLOCK) || (oldAttr.mq_curmsgs != 0))
            return (ERROR);
        else
            printf ("queue attributes are:\n\tblocking is %s\n\tmessage size is: %d\n\tmax messages in queue: %d\n\tno. of current msgs in queue: %d",
                oldAttr.mq_flags & O_NONBLOCK ? "on" : "off",
                oldAttr.mq_msgsize, oldAttr.mq_maxmsg,
                oldAttr.mq_curmsgs);
    }

/* clean up - close and unlink mq */
if (mq_unlink (name) == -1)
    return (ERROR);
if (mq_close (mqPXId) == -1)
    return (ERROR);
return (OK);

4.13.3 Displaying Message Queue Attributes

The VxWorks shell command show() produces a display of the key message queue attributes, for either POSIX or VxWorks message queues. VxWorks must be configured with include the INCLUDE_POSIX_MQ_SHOW component to provide this functionality.4

4. The show() routine is not a POSIX routine, nor is it meant to be used programmatically. It is designed for interactive use with the shell (with the shell’ C interpreter).
For example, if \texttt{mqPXId} is a POSIX message queue, the \texttt{show()} command can be used from the shell (with the C interpreter) as follows:


dir
  -> show mqPXId
  value = 0 = 0x0
  Message queue name : MyQueue
  No. of messages in queue : 1
  Maximum no. of messages : 16
  Maximum message size : 16

Compare this to the output for \texttt{myMsgQId}, a VxWorks message queue:


dir
  -> show myMsgQId
  Message Queue Id : 0x3adaf0
  Task Queuing : FIFO
  Message Byte Len : 4
  Messages Max : 30
  Messages Queued : 14
  Receivers Blocked : 0
  Send timeouts : 0
  Receive timeouts : 0


text

4.13.4 Communicating Through a Message Queue

Before a set of tasks can communicate through a POSIX message queue, one of the tasks must create the message queue by calling \texttt{mq_open()} with the \texttt{O_CREAT} flag set. Once a message queue is created, other tasks can open that queue by name to send and receive messages on it. Only the first task opens the queue with the \texttt{O_CREAT} flag; subsequent tasks can open the queue for receiving only (\texttt{O_RDONLY}), sending only (\texttt{O_WRONLY}), or both sending and receiving (\texttt{O_RDWR}).

To put messages on a queue, use \texttt{mq_send()}. If a task attempts to put a message on the queue when the queue is full, the task blocks until some other task reads a message from the queue, making space available. To avoid blocking on \texttt{mq_send()}, set \texttt{O_NONBLOCK} when you open the message queue. In that case, when the queue is full, \texttt{mq_send()} returns -1 and sets \texttt{errno} to \texttt{EAGAIN} instead of pending, allowing you to try again or take other action as appropriate.

One of the arguments to \texttt{mq_send()} specifies a message priority. Priorities range from 0 (lowest priority) to 31 (highest priority); see \texttt{4.10.1 Comparison of POSIX and VxWorks Scheduling}, p.236.

When a task receives a message using \texttt{mq_receive()}, the task receives the highest-priority message currently on the queue. Among multiple messages with the same priority, the first message placed on the queue is the first received (FIFO
order). If the queue is empty, the task blocks until a message is placed on the queue.

To avoid pending (blocking) on `mq_receive()`, open the message queue with `O_NONBLOCK`; in that case, when a task attempts to read from an empty queue, `mq_receive()` returns -1 and sets `errno` to `EAGAIN`.

To close a message queue, call `mq_close()`. Closing the queue does not destroy it, but only asserts that your task is no longer using the queue. To request that the queue be destroyed, call `mq_unlink()`. Unlinking a message queue does not destroy the queue immediately, but it does prevent any further tasks from opening that queue, by removing the queue name from the name table. Tasks that currently have the queue open can continue to use it. When the last task closes an unlinked queue, the queue is destroyed.

In VxWorks, POSIX message queues can be shared between processes only if their names start with a `/` (forward slash) character. POSIX message queues are otherwise private to the process in which they were created, and they cannot be accessed from another process. See 3.3.1 Public and Private Objects, p.157.

Example 4-11  POSIX Message Queues

/* In this example, the mqExInit() routine spawns two tasks that communicate using the message queue. */

/* mqEx.h - message example header */

/* defines */
#define MQ_NAME "exampleMessageQueue"

/* forward declarations */
void receiveTask (void);
void sendTask (void);

/* testMQ.c - example using POSIX message queues */

/* includes */
#include <vxWorks.h>
#include <mqueue.h>
#include <fcntl.h>
#include <errno.h>
#include <mqEx.h>

/* defines */
#define HI_PRIO 31
#define MSG_SIZE 16
int mqExInit (void)
{
/* create two tasks */
if (taskSpawn ("tRcvTask", 95, 0, 4000, receiveTask, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0) == ERROR)
{
    printf ("taskSpawn of tRcvTask failed\n");
    return (ERROR);
}

if (taskSpawn ("tSendTask", 100, 0, 4000, sendTask, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0) == ERROR)
{
    printf ("taskSpawn of tSendTask failed\n");
    return (ERROR);
}
}

void receiveTask (void)
{
    mqd_t mqPXId; /* msg queue descriptor */
    char msg[MSG_SIZE]; /* msg buffer */
    int prio; /* priority of message */

    /* open message queue using default attributes */
    if ((mqPXId = mq_open (MQ_NAME, O_RDWR | O_CREAT, 0, NULL)) == (mqd_t) -1)
    {
        printf ("receiveTask: mq_open failed\n");
        return;
    }

    /* try reading from queue */
    if (mq_receive (mqPXId, msg, MSG_SIZE, &prio) == -1)
    {
        printf ("receiveTask: mq_receive failed\n");
        return;
    }
    else
    {
        printf ("receiveTask: Msg of priority %d received:\n		%s"
            ,
            prio, msg);
    }
}

/* sendTask.c - mq sending example */

/* includes */
#include <vxWorks.h>
#include <mqueue.h>
#include <fcntl.h>
#include <mqEx.h>
/* defines */
#define MSG "greetings"
#define HI_PRIO 30

void sendTask (void)
{
    mqd_t mqPIXid; /* msg queue descriptor */
    /* open msg queue; should already exist with default attributes */
    if ((mqPIXid = mq_open (MQ_NAME, O_RDWR, 0, NULL)) == (mqd_t) -1)
    {
        printf ("sendTask: mq_open failed\n");
        return;
    }
    /* try writing to queue */
    if (mq_send (mqPIXid, MSG, sizeof (MSG), HI_PRIO) == -1)
    {
        printf ("sendTask: mq_send failed\n");
        return;
    }
    else
    {
        printf ("sendTask: mq_send succeeded\n");
    }
}

4.13.5 Notifying a Task that a Message is Waiting

A task can use the mq_notify() routine to request notification when a message it arrives for it at an empty queue. The advantage of this is that a task can avoid blocking or polling to wait for a message.

The mq_notify() routine specifies a signal to be sent to the task when a message is placed on an empty queue. This mechanism uses the POSIX data-carrying extension to signaling, which allows you, for example, to carry a queue identifier with the signal (see 4.14 POSIX Queued Signals, p.268).

The mq_notify() mechanism is designed to alert the task only for new messages that are actually available. If the message queue already contains messages, no notification is sent when more messages arrive. If there is another task that is blocked on the queue with mq_receive(), that other task unblocks, and no notification is sent to the task registered with mq_notify().

Notification is exclusive to a single task: each queue can register only one task for notification at a time. Once a queue has a task to notify, no further attempts to register with mq_notify() can succeed until the notification request is satisfied or cancelled.
Once a queue sends notification to a task, the notification request is satisfied, and the queue has no further special relationship with that particular task; that is, the queue sends a notification signal only once for each `mq_notify()` request. To arrange for one particular task to continue receiving notification signals, the best approach is to call `mq_notify()` from the same signal handler that receives the notification signals.

To cancel a notification request, specify `NULL` instead of a notification signal. Only the currently registered task can cancel its notification request.

**Example 4-12 Notifying a Task that a Message Queue is Waiting**

```c
/*
 * In this example, a task uses mq_notify() to discover when a message
 * is waiting for it on a previously empty queue.
 */

/* includes */
#include <vxWorks.h>
#include <signal.h>
#include <mqueue.h>
#include <fcntl.h>
#include <errno.h>

/* defines */
#define QNAM "PxQ1"
#define MSG_SIZE 64 /* limit on message sizes */

/* forward declarations */
static void exNotificationHandle (int, siginfo_t *, void *);
static void exMqRead (mqd_t);

/* exMqNotify - example of how to use mq_notify() */

/* This routine illustrates the use of mq_notify() to request notification
 * via signal of new messages in a queue. To simplify the example, a
 * single task both sends and receives a message.
 */

int exMqNotify
(char * pMess /* text for message to self */) {

    struct mq_attr attr; /* queue attribute structure */
    struct sigevent sigNotify; /* to attach notification */
    struct sigaction mySigAction; /* to attach signal handler */
    mqd_t exMqId /* id of message queue */
```
/* Minor sanity check; avoid exceeding msg buffer */
if (MSG_SIZE <= strlen (pMess))
{
    printf ("exMqNotify: message too long\n");
    return (-1);
}

/* Install signal handler for the notify signal and fill in
   a sigaction structure and pass it to sigaction(). Because the handler
   needs the siginfo structure as an argument, the SA_SIGINFO flag is
   set in sa_flags. */
mySigAction.sa_sigaction = exNotificationHandle;
mySigAction.sa_flags = SA_SIGINFO;
sigemptyset (&mySigAction.sa_mask);

if (sigaction (SIGUSR1, &mySigAction, NULL) == -1)
{
    printf ("sigaction failed\n");
    return (-1);
}

/* Create a message queue - fill in a mq_attr structure with the
   size and no. of messages required, and pass it to mq_open(). */
attr.mq_flags  = O_NONBLOCK; /* make nonblocking */
attr.mq_maxmsg = 2;
attr.mq_msgsize = MSG_SIZE;
if ( (exMqId = mq_open (QNAM, O_CREAT | O_RDWR, 0, &attr)) ==
    (mqd_t) - 1 )
{
    printf ("mq_open failed\n");
    return (-1);
}

/* Set up notification: fill in a sigevent structure and pass it
   to mq_notify(). The queue ID is passed as an argument to the
   signal handler. */
sigNotify.sigev_signo = SIGUSR1;
sigNotify.sigev_notify = SIGEV_SIGNAL;
sigNotify.sigev_value.sival_int = (int) exMqId;

if (mq_notify (exMqId, &sigNotify) == -1)
{
    printf ("mq_notify failed\n");
    return (-1);
}
/* We just created the message queue, but it may not be empty; 
* a higher-priority task may have placed a message there while 
* we were requesting notification. mq_notify() does nothing if 
* messages are already in the queue; therefore we try to 
* retrieve any messages already in the queue. */
exMqRead (exMqId);

/*
* Now we know the queue is empty, so we will receive a signal 
* the next time a message arrives.
* We send a message, which causes the notify handler to be invoked. 
* It is a little silly to have the task that gets the notification 
* be the one that puts the messages on the queue, but we do it here 
* to simplify the example. A real application would do other work 
* instead at this point.
*/
if (mq_send (exMqId, pMess, 1 + strlen (pMess), 0) == -1)
{
    printf ("mq_send failed\n");
    return (-1);
}
/* Cleanup */
if (mq_close (exMqId) == -1)
{
    printf ("mq_close failed\n");
    return (-1);
}
/* More cleanup */
if (mq_unlink (QNAM) == -1)
{
    printf ("mq_unlink failed\n");
    return (-1);
}
return (0);

/*
exNotificationHandle - handler to read in messages
* This routine is a signal handler; it reads in messages from a
* message queue.
*/
static void exNotificationHandle
(
    int sig,            /* signal number */
    siginfo_t * pInfo,  /* signal information */
    void * pSigContext /* unused (required by posix) */
)
{
    struct sigevent sigNotify;
    mqd_t exMqId;

    /* Get the ID of the message queue out of the siginfo structure. */
    exMqId = (mqd_t) pInfo->si_value.sival_int;

    /* Request notification again; it resets each time
     * a notification signal goes out. */
    sigNotify.sigev_signo = pInfo->si_signo;
    sigNotify.sigev_value = pInfo->si_value;
    sigNotify.sigev_notify = SIGEV_SIGNAL;

    if (mq_notify (exMqId, &sigNotify) == -1)
    {
        printf ("mq_notify failed\n");
        return;
    }

    /* Read in the messages */
    exMqRead (exMqId);
}

static void exMqRead
(
    mqd_t exMqId
)
{
    char msg[MSG_SIZE];
    int prio;

    /* Read in the messages - uses a loop to read in the messages
     * because a notification is sent ONLY when a message is sent on
     * an EMPTY message queue. There could be multiple msgs if, for
     * example, a higher-priority task was sending them. Because the
     * message queue was opened with the O_NONBLOCK flag, eventually
     * this loop exits with errno set to EAGAIN (meaning we did an
     * mq_receive() on an empty message queue).
     */
while (mq_receive (exMqId, msg, MSG_SIZE, &prio) != -1) {
    printf ("exMqRead: received message: %s\n", msg);
}

if (errno != EAGAIN)
{
    printf ("mq_receive: errno = %d\n", errno);
}
}

4.14  POSIX Queued Signals

Signals are handled differently in the kernel and in real-time processes. In the kernel the target of a signal is always a task; but in user space, the target of a signal may be either a specific task or an entire process.

For user-mode applications (processes), all POSIX API that take a process identifier as one of their parameters use a process ID (\(\text{pid}_t\) mapping on a \(\text{RTP}_{\text{ID}}\) in the VxWorks implementation of the signal support for processes.

However, for the VxWorks kernel—for backward compatibility with prior versions of VxWorks—these API continue to use a task identifier for the kernel APIs. Also, in order to maintain functionality equivalent to that provided by previous releases of VxWorks and to support signals between kernel and user applications, additional non-POSIX APIs have been added: \text{taskSigqueue()}\), \text{rtpSigqueue()}\), \text{rtpTaskSigqueue()}\), \text{taskKill()}\), \text{rtpKill()}\), and \text{rtpTaskKill()}\).

In accordance with the POSIX standard, a signal sent to a process is handled by the first available task in the process.

The \text{sigqueue()} family of routines provides an alternative to the \text{kill()} family of routines for sending signals. The important differences between the two are:

- \text{sigqueue()} includes an application-specified value that is sent as part of the signal. You can use this value to supply whatever context your signal handler finds useful. This value is of type \text{signal} (defined in signal.h); the signal handler finds it in the \text{si_value} field of one of its arguments, a structure \text{siginfo_t}. An extension to the POSIX \text{sigaction()} routine allows you to register signal handlers that accept this additional argument.
**sigqueue()** enables the queueing of multiple signals for any task. The **kill()** routine, by contrast, delivers only a single signal, even if multiple signals arrive before the handler runs.

Currently, VxWorks includes signals reserved for application use, numbered consecutively from **SIGRTMIN**. The presence of a minimum of eight (**_POSIX_RTSIG_MAX**) of these reserved signals is required by POSIX 1003.1, but VxWorks supports only seven (**RTSIG_MAX**). The specific signal values are not specified by POSIX; for portability, specify these signals as offsets from **SIGRTMIN** (for example, write **SIGRTMIN+2** to refer to the third reserved signal number). All signals delivered with **sigqueue()** are queued by numeric order, with lower-numbered signals queuing ahead of higher-numbered signals.

POSIX 1003.1 also introduced an alternative means of receiving signals. The routine **sigwaitinfo()** differs from **sigsuspend()** or **pause()** in that it allows your application to respond to a signal without going through the mechanism of a registered signal handler: when a signal is available, **sigwaitinfo()** returns the value of that signal as a result, and does not invoke a signal handler even if one is registered. The routine **sigtimedwait()** is similar, except that it can time out.

Basic queued signal routines are described in Table 4-17. For detailed information on signals, see the kernel and application (process) API references for **sigLib**.

<table>
<thead>
<tr>
<th>Routine</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>sigqueue()</strong></td>
<td>Sends a queued signal to a task (kernel API) or to a process (application API).</td>
</tr>
<tr>
<td><strong>sigwaitinfo()</strong></td>
<td>Waits for a signal.</td>
</tr>
<tr>
<td><strong>sigtimedwait()</strong></td>
<td>Waits for a signal with a timeout.</td>
</tr>
</tbody>
</table>

Additional non-POSIX VxWorks queued signal routines are described in Table 4-18. These routines are provided for assisting in porting VxWorks 5.x kernel applications to processes. The POSIX routines described in Table 4-17 should be used for developing new applications that execute as real-time processes.

Note that a parallel set of non-POSIX APIs are provided for the **kill()** family of POSIX routines.
To include POSIX queued signals in the system, configure VxWorks with the INCLUDE_POSIX_SIGNALS component. This component automatically initializes POSIX queued signals with sigqueueInit(). The sigqueueInit() routine allocates buffers for use by sigqueue(), which requires a buffer for each currently queued signal. A call to sigqueue() fails if no buffer is available.

The maximum number of queued signals in the kernel is set with the configuration parameter NUM_SIGNAL_QUEUES. The default value is 16.

Process-based applications are automatically linked with the application API mqPxLib when they are compiled. Initialization of the library is automatic when the process starts.

### Table 4-18 Non-POSIX Queued Signal Routines

<table>
<thead>
<tr>
<th>Routine</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>taskSigqueue()</td>
<td>Sends a queued signal from a task in a process to another task in the same process (user-space only).</td>
</tr>
<tr>
<td>rtpSigqueue()</td>
<td>Sends a queued signal from a kernel task to a process (kernel-space only).</td>
</tr>
<tr>
<td>rtpTaskSigqueue()</td>
<td>Sends a queued signal from a kernel task to a specified task in a process (kernel-space only).</td>
</tr>
</tbody>
</table>
Example 4-13  Queued Signals

/* queSig.c - signal demo */
/*
DESCRIPTION
This demo program exhibits the following functionalities in queued signals.
1) Sending a queued signal to a kernel task
2) Sending a queued signal to a RTP task
3) Sending a queued signal to a RTP

For simplicity the sender is assumed to be a kernel task.

Do the following in order to see the demo.

Sending a queued signal to a kernel task
------------------------------------------
1) Build this file (queSig.c) alongwith the VxWorks image.
2) Spawn a task with main() as the entry address. For e.g. from the kernel shell
do "sp main".
3) sig (int id, int value) provided in this file is a helper function to send
a queued signal. Where <id> is the kernel task Id and <value> is the signal
value to be sent.
4) Send a queued signal to the spawned kernel task. From kernel shell do
   sig <kernelTaskId> , <value>

Sending a queued signal to a RTP task
--------------------------------------
1) Build this file (queSig.c) as an RTP executable.
2) Spawn the queSig RTP.
3) From a kernel task, use the sig (int id, int value); helper routine to send
   a queued signal to the RTP task. The <id> being the RTP task Id.

Sending a queued signal to a RTP
---------------------------------
1) Build this file (queSig.c) as an RTP executable.
2) Spawn the queSig RTP.
3) From a kernel task, use the sig (int id, int value); helper routine to send
   a queued signal to the RTP. The <id> being the RTP Id.
*/

#include <stdio.h>
#include <signal.h>
#include <taskLib.h>
#include "rtpLib.h"
#ifdef _WRS_KERNEL
#include "private/rtpLibP.h"
#include "private/taskLibP.h"
#endif
typedef void (*FPTR) (int);

void sigMasterHandler
{
    int     sig,                /* caught signal */
    #ifdef _WRS_KERNEL
    int      code,
    #else
    siginfo_t * pInfo ,         /* signal info */
    #endif
    struct sigcontext *pContext /* unused */
};

>Status main ()
{
    sigset_t sig = sigmask (SIGUSR1);
    union sigval sval;
    struct sigaction in;
    sigprocmask (SIG_UNBLOCK, &sig, NULL);

    in.sa_handler = (FPTR) sigMasterHandler;
    in.sa_flags = 0;
    (void) sigemptyset (&in.sa_mask);
    sigaction (SIGUSR1, &in, NULL);

    printf ("Task 0x%x installed signal handler for signal # %d.\n    Ready for signal.\n", taskIdCurrent, SIGUSR1);
    for (;;);
}

>Status sigMasterHandler
{
    int     sig,                /* caught signal */
    #ifdef _WRS_KERNEL

```c
int     code, 
#else
    siginfo_t * pInfo , /* signal info */ 
#endif
    struct sigcontext *pContext /* unused */
{
    printf ("Task 0x%x got signal # %d signal value %d \n", taskIdCurrent, sig,
#if defined _WRS_KERNEL
    code
#else
    pInfo->si_value.sival_int
#endif
    );
}

/****************************************************************************
* sig - helper routine to send a queued signal
* This routine can send a queued signal to a kernel task or RTP task or RTP.
* <id> is the ID of the receiver entity. <value> is the value to be sent
* along with the signal. The signal number being sent is SIGUSR1.
*/
#endif

#if defined _WRS_KERNEL
STATUS sig (int id, int val)
{
    union sigval valueCode;
    valueCode.sival_int = val;
    if (TASK_ID_VERIFY (id) == OK)
    {
        if (IS_KERNEL_TASK (id))
        {
            sigqueue (id, SIGUSR1, valueCode);
        }
        else
        {
            rtpTaskSigqueue ((WIND_TCB *)id, SIGUSR1, valueCode);
        }
    }
    else if (OBJ_VERIFY ((RTP_ID)id, rtpClassId) != ERROR)
    {
        rtpSigqueue ((RTP_ID)id, SIGUSR1, valueCode);
    }
    else
    {
        return (ERROR);
    }
    return (OK);
}
#endif
```
5 Memory Management

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5.2 Configuring VxWorks With Memory Management Facilities 277
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5.4 Shell Commands 285
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5.1 Introduction

VxWorks provides memory management facilities for all code that executes in the kernel, as well as memory management facilities for applications that execute as real-time processes. This chapter deals primarily with kernel-space memory management, although it also provides information about what memory maps look like for systems that include support for processes (and related facilities).

This chapter discusses the following topics:

- The VxWorks components required for different types of memory management support.
- The layout of memory for different configurations of VxWorks.
- Excluding memory from VxWorks use.
- Using run-time memory autosizing.
- The kernel heap and memory partition management facilities that are available in the kernel.
- Memory error detection facilities, including instrumentation provided by VxWorks components and the Wind River compiler.
- Virtual memory management, both automated and programmatic.
- Using the real-time process environment without an MMU.

For information about the memory management facilities available to process-based applications, see *VxWorks Application Programmer’s Guide: Memory Management*.

For information about additional error detection facilities useful for debugging software faults, see 9. Error Detection and Reporting.

**NOTE:** This chapter provides information about facilities available in the VxWorks kernel. For information about facilities available to real-time processes, see the corresponding chapter in the *VxWorks Application Programmer’s Guide.*
5 Memory Management

5.2 Configuring VxWorks With Memory Management Facilities

Information about configuring VxWorks with various memory management facilities is provided in the context of the discussions of those facilities. See:

- 5.4 Shell Commands, p.285
- 5.5 System RAM Autosizing, p.285
- 5.6 Reserved Memory, p.286
- 5.7 Kernel Heap and Memory Partition Management, p.287
- 5.8 Memory Error Detection, p.289
- 5.9 Virtual Memory Management, p.301
- 5.11 Processes Without MMU Support, p.314

5.3 System Memory Maps

This section describes the VxWorks memory map as it appears with different configurations and runtime activity:

- A system without process support.
- A system with process support, but without processes running.
- A system with process support and two processes running, as well as a shared library and a shared data region.

In addition, it describes various memory views within a single system.
5.3.1 System Memory Map Without Process Support

In a VxWorks system RAM is delimited by:

- The `LOCAL_MEM_LOCAL_ADRS` BSP configuration parameter, which defines the start of the system RAM.

- The address returned by the routine `sysPhysMemTop()`, which is at the top of system RAM. This address is either determined at runtime if RAM autosizing is enabled (see 5.5 System RAM Autosizing, p.285). If autosizing is disabled, then `sysPhysMemTop()` is calculated using the BSP configuration parameter `LOCAL_MEM_SIZE`; that is `sysPhysMemTop()` returns `LOCAL_MEM_LOCAL_ADRS + LOCAL_MEM_SIZE`.

`LOCAL_MEM_LOCAL_ADRS` and `LOCAL_MEM_SIZE` are configuration parameters of the INCLUDE_MEMORY_CONFIG component.

System RAM must be contiguous. For systems without an MMU or with the MMU disabled, this means that the system RAM must be in contiguous physical memory. For systems with MMU enabled, the system RAM must be mapped contiguously in virtual memory. In the latter case, the physical space may be non-contiguous for some architectures that do not require an identity mapped kernel. For the architecture specific requirements, see the VxWorks Architecture Supplement.

Within system RAM, the elements of a VxWorks system are arranged as follows:

- Below `RAM_LOW_ADRS` there is an architecture specific layout of memory blocks used for saving boot parameters, the system exception message area, the exception or interrupt vector table, and so on. For specific details, see the VxWorks Architecture Supplement.

- The kernel code (text, data, and bss) starting at address `RAM_LOW_ADRS`. ROM-resident images are an exception, for which the text segment is located outside of the system RAM (see 2.5.1 VxWorks Image Types, p.19).

- The WDB target agent memory pool is located immediately above the kernel code, if WDB is configured into the system (see 10.6 WDB Target Agent, p.540).

- The kernel heap follows the WDB memory pool.

- An optional area of persistent memory.

- An optional area of user-reserved memory may be located above the kernel heap.

Figure 5-1 illustrates a typical memory map for a system without process support. For a comparable illustration of a system with process support—which means it
has unmapped memory available for processes that have not yet been created—see Figure 5-2.

Note that the memory pool for the WDB target agent is present only if WDB is configured into the kernel. Without WDB, the kernel heap starts right above the end of the kernel BSS ELF segment.

The routine `sysMemTop()` returns the end of the kernel heap area. If both the user-reserved memory size (`USER_RESERVED_MEM`) and the persistent memory size (`PM_RESERVED_MEM`) are zero, then `sysMemTop()` returns the same value than `sysPhysMemTop()`, and the kernel heap extends to the end of the system RAM area. For more information about configuring user-reserved memory and persistent memory. See 5.6 Reserved Memory, p.286 for more information. Also see 5.3.2 System Memory Map with Process Support, p.280.

Figure 5-1 Memory Map of a System Without Process Support
5.3.2 System Memory Map with Process Support

Kernel applications have access to the same memory management facilities as described in 5.3 System Memory Maps, p.277, whether process support is included or not.

The only difference between the two configurations relates to the size of the kernel heap. Without process support, the kernel heap extends up to `sysMemTop()`.

With process support the kernel heap does not extend up to `sysMemTop()`, but instead uses the `KERNEL_HEAP_SIZE` parameter (set in the INCLUDE_RTP component) as its size. This parameter is disregarded if process support is not included in VxWorks.

By default, `KERNEL_HEAP_SIZE` is set to two-thirds of the RAM located between `sysMemTop()` and the end of the kernel code, or the end of the WDB memory pool when the WDB component is included into the system configuration.

Figure 5-2 illustrates this configuration. The RAM located between `sysMemTop()` and the end of the kernel heap is left unmapped. RAM pages are allocated from that unmapped RAM area when process, shared library, or shared data region space must be mapped. For a comparable image of a system without process support, see Figure 5-1.
The default setting of `KERNEL_HEAP_SIZE` should be adjusted to meet the requirements of the system.

### 5.3.3 System Memory Map with Processes Running

A VxWorks system configured for real-time processes may have one or more applications executing as processes at runtime. It may also have shared libraries and shared data regions instantiated. The kernel, each of the processes, shared libraries, and shared data regions occupy a discrete space in virtual memory.

Each VxWorks process has its own region of virtual memory; processes do not overlap in virtual memory. This flat virtual-memory map provides advantages in
speed, in a programming model that accommodates systems with and without an MMU, and in debugging applications (see 5.11 Processes Without MMU Support, p.314).

The virtual space assigned to a process is not necessarily composed of one large contiguous block of virtual memory. In some cases it will be composed of several smaller blocks of virtual space which are discontinuous from each other.

Figure 5-3 illustrates the memory map of a system with the kernel areas (RAM and I/O), two different processes (RTP A and RTP B), as well as one shared library, and one shared data region.

Each process has its own virtual memory context, defined by its MMU translation table used to map virtual and physical memory, and other information about each page of memory. This memory context describes the virtual space that all of the
tasks in a the process can access. In other words, it defines the memory view of a process.

The kernel space is mapped with supervisor access privilege in the memory context of each process (but not with user mode privilege). Therefore tasks executing in a process can access kernel memory space only in system calls, during which the execution is switched to supervisor mode. (For information about system calls, see VxWorks Application Programmer’s Guide: Applications and Processes.)

A shared library or shared data region is mapped into the virtual context of a process only when the process’ application code opens or creates it, and it effectively disappears from the process’ memory view when the application closes or deletes the shared library or shared data region.

Figure 5-4 illustrates the different memory views of a system with two processes (RTP A and RTP B), a shared library that both RTP A and RTP B opened, as well as a shared data region that both a kernel application and RTP B opened.

The first memory view corresponds to the memory space accessible by kernel tasks. The second and third memory views correspond to the memory space accessible by tasks executing in process A, respectively process B. Note that the grayed areas are only accessible during system calls.
Note that on system without an MMU, or with the MMU disabled, there is only one memory view shared by the kernel and all process tasks. This memory view corresponds to Figure 5-3. Any task in the system, whether it is a kernel or a task executing in a process, has access to all the memory: kernel space, I/O regions, any processes memory, shared libraries, and shared data regions. In other words, such configurations do not provide any memory protection. For more information, see 5.11 Processes Without MMU Support, p.314.
5.4 Shell Commands

The shell’s `adrSpaceShow`() show routine (for the C interpreter) or the `adrs p info` command (for the command interpreter) can be used to display an overview of the address space usage at time of the call. These are included in the kernel with the `INCLUDE_ADR_SPACE_SHOW` and `INCLUDE_ADR_SPACE_SHELL_CMD` components, respectively.

The `rtpMemShow`() show routine or the `rtp meminfo` command can be used to display the private mappings of a process. These are included with the `INCLUDE_RTP_SHOW` and `INCLUDE_RTP_SHOW_SHELL_CMD` components, respectively.

The kernel mappings can be displayed with the `vmContextShow`() show routine or the `vm context` command. These are included with the `INCLUDE_VM_SHOW` and `INCLUDE_VM_SHOW_SHELL_CMD` components, respectively.

5.5 System RAM Autosizing

When RAM autosizing is supported by the BSP, defining the configuration parameter `LOCAL_MEM_AUTOSIZE` will enable run time memory sizing. The default definition state for this parameter and the implementation itself is BSP-dependent. Check the BSP reference to see if this feature is supported or not.

When autosizing is supported by the BSP and `LOCAL_MEM_AUTOSIZE` is defined, the top of system RAM as reported by `sysPhysMemTop`() is the value determined at runtime.

If the `LOCAL_MEM_AUTOSIZE` is not defined, the top of the system RAM as reported by `sysPhysMemTop`() is the address calculated as:

\[
\text{LOCAL_MEM_LOCAL_ADRS + LOCAL_MEM_SIZE}
\]

If the BSP is unable to perform run time memory sizing then a compile time error should be generated, informing the user of the limitation.

`LOCAL_MEM_AUTOSIZE`, `LOCAL_MEM_LOCAL_ADRS` and `LOCAL_MEM_SIZE` are parameters of the `INCLUDE_MEM_CONFIG` component.
5.6 Reserved Memory

Two types of reserved memory can be configured in VxWorks system RAM: user-reserved memory and persistent memory. Reserved memory is not cleared by VxWorks at startup or during system operation. Boot loaders may or may not clear the area; see Boot Loaders and Reserved Memory, p.286.

User-reserved memory, configured with the BSP parameter USER_RESERVED_MEM, is part of the system RAM that can managed by kernel applications independently of the kernel heap.

Persistent memory, configured with the parameter PM_RESERVED_MEMORY, is the part of system RAM that is used by the error detection and reporting facilities (see 9. Error Detection and Reporting).

For the layout of the user-reserved memory and the persistent memory, see figures Figure 5-1Memory Map of a System Without Process Support, p.279 and Figure 5-2Memory Map of a System with Process Support, p.281.

Boot Loaders and Reserved Memory

Boot loaders may or may not clear reserved memory, depending on the configuration that was used to create them. If the boot loader is built with both USER_RESERVED_MEM and PM_RESERVED_MEMORY set to zero, the system RAM is cleared through the address calculated as:

\[(\text{LOCAL_MEM_LOCAL_ADRS} + \text{LOCAL_MEM_SIZE})\]

To ensure that reserved memory is not cleared, the boot loader should be created with the USER_RESERVED_MEM and the PM_RESERVED_MEMORY parameter set to the desired sizes; that is, the same values that are used to build the downloaded VxWorks image.

For information about VxWorks boot loaders, see 2.4 Boot Loader, p.12.

NOTE: If autosizing of system RAM is enabled, the top of the system RAM detected at runtime may be different from the address calculated as \(\text{LOCAL_MEM_LOCAL_ADRS} + \text{LOCAL_MEM_SIZE}\), resulting in non-identical location of the memory range not being cleared by the boot loader. For more information about autosizing, see 5.5 System RAM Autosizing, p.285.
5.7 Kernel Heap and Memory Partition Management

VxWorks provides facilities for heap access and memory partition management. The memLib and memPartLib libraries provide routines to access the kernel heap, including standard ANSI-compatible routines as well as routines to manipulate kernel memory partitions. The kernel heap is used by all code running in the kernel, including kernel libraries and components, kernel applications, and by processes when executing system calls.

Memory partitions consist of areas of memory that are used for dynamic memory allocations by applications and kernel components. Memory partitions may be used to reserve portions of memory for specific applications, or to isolate dynamic memory usage on an application basis.

The kernel heap is a specific memory partition, which is also referred to as the system memory partition.

5.7.1 Configuring the Kernel Heap and the Memory Partition Manager

There are two kernel components for configuring the kernel heap and the memory partition manager. The core functionality for both the kernel heap and memory partition is provided by the INCLUDE_MEM_MGR_BASIC component (see the VxWorks API reference for memPartLib). The INCLUDE_MEM_MGR_FULL component extends the functionality required for a full-featured heap and memory partition manager (see the VxWorks API reference for memLib).

The kernel heap is automatically created by the system when either one of these components are included in the VxWorks configuration. The size of the kernel heap is set as described in 5.3 System Memory Maps, p. 277; see Figure 5-1 and Figure 5-2.

Information about allocation statistics in the kernel heap and in kernel memory partitions can be obtained with the show routines provided with the INCLUDE_MEM_SHOW component. For more information, see the VxWorks API reference for memShow.

5.7.2 Basic Heap and Memory Partition Manager

The memPartLib library (INCLUDE_MEM_MGR_BASIC) provides the core facilities for memory partition support, including some of the standard
ANSI-compatible routines such as `malloc()` and `free()`. The core functionality of `memPartLib` provides for the following API:

- Creation and deletion of memory partitions with `memPartCreate()` and `memPartDelete()`.
- Addition of memory to a specific memory partition with `memPartAddToPool()`, or to the heap with `memAddToPool()`.
- Allocation and freeing of memory blocks from a specific memory partitions with `memPartAlloc()`, `memPartAlignedAlloc()`, and `memPartFree()`; and from the heap with `malloc()` and `free()`.

### 5.7.3 Full Heap and Memory Partition Manager

The `memLib` library (provided by the `INCLUDE_MEM_MGR_FULL` component) adds a few more routines to provide a full-featured memory partition and heap allocator. The features provided in this library are:

- Allocation of memory aligned to a specific boundary with `memalign()`, and alignment to a page with `valloc()`.
- Reallocation of blocks of memory in a specific partition with `memPartRealloc()`, or in the heap with `realloc()`.
- The ANSI-compatible routines `calloc()`, and `cfree()`.
- Obtaining memory partition statistics with routines `memPartInfoGet()` and `memPartFindMax()`, or in the heap with `memFindMax()` and `memInfoGet()`.
- Built-in error checking. This feature is controlled with the heap and partition options. Two types of errors can be enabled. The first type, block error, is detected during block validation in `free()`, `realloc()`, `memPartFree()` and `memPartRealloc()`. The second type, allocation error, is detected by any of the allocation and re-allocation routines. There are options to enable logging an error message and/or to suspend the task hitting the error. Setting and getting error handling options of a specific memory partition can be done with `memPartOptionsSet()` and `memPartOptionsGet()`. The debug options for the heap are controlled via `memOptionsSet()` and `memOptionGet()`. Additional heap and memory partition error detection is provided with heap and partition memory instrumentation (see 5.8.1 Heap and Partition Memory Instrumentation, p.289).
5 Memory Management

5.8 Memory Error Detection

For more information, refer to the VxWorks API references for `memPartLib` and `memLib`.

5.8 Memory Error Detection

Support for memory error detection is provided by two optional instrumentation libraries. The `memEdrlib` library performs error checks of operations in the kernel heap and memory partitions in the kernel. The Run-Time Error Checking (RTEC) feature of the Wind River Compiler can be used to check for additional errors, such as buffer overruns and underruns, static and automatic variable reference checks.

Errors detected by these facilities are reported by the error detection and reporting facility, which must, therefore be included in the VxWorks kernel configuration. See 9. Error Detection and Reporting.

5.8.1 Heap and Partition Memory Instrumentation

To supplement the error detection features built into `memLib` and `memPartLib` (such as valid block checking), components can be added to VxWorks to perform automatic, programmatic, and interactive error checks on `memLib` and `memPartLib` operations.

The component helps detect common programming errors such as double-freeing an allocated block, freeing or reallocating an invalid pointer, memory leaks. In addition, with compiler-assisted code instrumentation, it helps detect bounds-check violations, buffer over-runs and under-runs, pointer references to free memory blocks, pointer references to automatic variables outside the scope of the variable, etc.

Errors detected by the automatic checks are logged by the error detection and reporting facility.
Configuring VxWorks with Memory Partition and Heap Instrumentation

To enable the basic level of memory partition and heap instrumentation, the following components must be included into the kernel configuration:

- **INCLUDE_MEM_EDR**, includes the basic memory partition debug functionality and instrumentation code.

- **INCLUDE_EDR_ERRLOG, INCLUDE_EDR_POLICIES** and **INCLUDE_EDR_SHOW** for error detection, reporting, and persistent memory. For more information see 9. Error Detection and Reporting.

The following component may also be included:

- **INCLUDE_MEM_EDR_SHOW**, for enabling the show routines.

In addition, the following parameters of the **INCLUDE_MEM_EDR** component can be modified:

**MEDR_EXTENDED_ENABLE**
Set to **TRUE** to enable logging trace information for each allocated block, but at the cost of increased memory used to store entries in the allocation database.
The default setting is **FALSE**.

**MEDR_FILL_FREE_ENABLE**
Set to **TRUE** to enable pattern-filling queued free blocks. This aids detecting writes into freed buffers. The default setting is **FALSE**.

**MEDR_FREE_QUEUE_LEN**
Maximum length of the free queue. When a memory block is freed, instead of immediately returning it to the partition’s memory pool, it is kept in a queue. This is useful for detecting references to a memory block after it has been freed. When the queue reaches the maximum length allowed, the blocks are returned to the respective memory pool in a FIFO order. Queuing is disabled when this parameter is 0. Default setting for this parameter is 64.

**MEDR_BLOCK_GUARD_ENABLE**
Enable guard signatures in the front and the end of each allocated block. Enabling this feature aids in detecting buffer overruns, underruns, and some heap memory corruption. The default setting is **FALSE**.

**MEDR_POOL_SIZE**
Set the size of the memory pool used to maintain the memory block database. Default setting in the kernel is 1MB. The database uses 32 bytes per memory block without extended information enabled, and 64 bytes per block with extended information enabled (call stack trace). This pool is allocated from the kernel heap.
Error Types

During execution, errors are automatically logged when the allocation, free, and re-allocation functions are called. The following error types are automatically identified and logged:

- Allocation returns block address within an already allocated block from the same partition. This would indicate corruption in the partition data structures.
- Allocation returns block address that is in the task’s stack space. This would indicate corruption in the partition data structures.
- Allocation returns block address that is in the kernel’s static data section. This would indicate corruption in the partition data structures.
- Freeing a pointer that is in the task’s stack space.
- Freeing memory that was already freed and is still in the free queue.
- Freeing memory that is in the kernel’s static data section.
- Freeing memory in a different partition than the one in which it was allocated.
- Freeing a partial memory block.
- Freeing a memory block with the guard zone corrupted, when the MEDR_BLOCK_GUARD_ENABLE environment variable is TRUE.
- Pattern in a memory block which is in the free queue has been corrupted, when the MEDR_FILL_FREE_ENABLE environment variable is TRUE.
Shell Commands

The show routines and commands described in Table 5-1 are available for use with the shell’s C and command interpreters to display information.

<table>
<thead>
<tr>
<th>C Interpreter</th>
<th>Command Interpreter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>edrShow()</td>
<td>edr show</td>
<td>Displays error records.</td>
</tr>
<tr>
<td>memEdrPartShow()</td>
<td>mem part list</td>
<td>Displays a summary of the instrumentation information for memory partitions in the kernel.</td>
</tr>
<tr>
<td>memEdrBlockShow()</td>
<td>mem block list</td>
<td>Displays information about allocated blocks. Blocks can be selected using a combination of various querying criteria: partition ID, block address, allocating task ID, block type.</td>
</tr>
<tr>
<td>memEdrFreeQueueFlush()</td>
<td>mem queue flush</td>
<td>Flushes the free queue. When this routine is called, freeing of all blocks in the free queue is finalized so that all corresponding memory blocks are returned the free pool of the respective partition.</td>
</tr>
</tbody>
</table>
5 Memory Management

5.8 Memory Error Detection

Table 5-1 Shell Commands (cont’d)

<table>
<thead>
<tr>
<th>C Interpreter</th>
<th>Command Interpreter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>memEdrBlockMark( )</td>
<td>mem block mark and</td>
<td>Marks or unmarks selected blocks allocated at the time of the call.</td>
</tr>
<tr>
<td></td>
<td>mem block unmark</td>
<td>The selection criteria may include partition ID and/or allocating task ID.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>This routine can be used to monitor memory leaks by displaying information</td>
</tr>
<tr>
<td></td>
<td></td>
<td>of unmarked blocks with memBlockShow( ) or mem block list.</td>
</tr>
</tbody>
</table>

Code Example

The following kernel application code is used to demonstrate various errors detected with the heap and partition memory instrumentation (line numbers are included for reference purposes). Its use is illustrated in Shell Session Example, p.293.

```c
#include <vxWorks.h>
#include <stdlib.h>

void heapErrors (void)
{
    char * pChar;
    pChar = malloc (24);
    free (pChar + 2); /* free partial block */
    free (pChar);
    free (pChar);    /* double-free block */
    pChar = malloc (32); /* leaked memory */
}
```

Shell Session Example

The following shell session is executed with the C interpreter. The sample code listed above is compiled and linked in the VxWorks kernel (see 2.7.7 Linking Kernel-Based Application Object Modules with VxWorks, p.65). The kernel must include the INCLUDE_MEM_EDR and INCLUDE_MEM_EDR_SHOW components. In order to enable saving call stack information, the parameter MEDR_EXTENDED_ENABLE is set TRUE. Also, the kernel should be configured
with the error detection and reporting facility, including the show routines, as described in 9.2 Configuring Error Detection and Reporting Facilities, p.481.

First mark all allocated blocks:

```c
-> memEdrBlockMark
   value = 6390 = 0x18f6
```

Next, clear the error log. This step is optional, and is done only to start with a clean log:

```c
-> edrClear
   value = 0 = 0x0
```

Start the kernel application in a new task spawned with the `sp()` utility:

```c
-> taskId = sp(heapErrors)
   New symbol "taskId" added to kernel symbol table.
   Task spawned: id = 0x246d010, name = t1
   taskId = 0x2469ed0: value = 38195216 = 0x246d010
```

At this point the application finished execution. The following command lists the memory blocks allocated, but not freed by the application task. Note that the listing shows the call stack at the time of the allocation:

```c
-> memEdrBlockShow 0, 0, taskId, 5, 1
```

<table>
<thead>
<tr>
<th>Addr</th>
<th>Type</th>
<th>Size</th>
<th>Part ID</th>
<th>Task ID</th>
<th>Task Name</th>
<th>Trace</th>
</tr>
</thead>
<tbody>
<tr>
<td>246d7a0</td>
<td>alloc</td>
<td>32</td>
<td>269888</td>
<td>246d010</td>
<td>-t1 heapErrors()</td>
<td>memPartAlloc() 0x01bdc88()</td>
</tr>
</tbody>
</table>

Errors detected while executing the application are logged in persistent memory region.
Display the log using `edrShow()`. The first error corresponds to line 9 in the test code; the second error corresponds to line 12.

```c
-> edrShow
    ERROR LOG
    =========
    Log Size: 524288 bytes (128 pages)
    Record Size: 4096 bytes
    Max Records: 123
    CPU Type: 0x5a
    Errors Missed: 0 (old) + 0 (recent)
    Error count: 2
    Boot count: 20
    Generation count: 94

==[1/2]==============================================================================
Severity/Facility: NON-FATAL/KERNEL
Boot Cycle: 20
OS Version: 6.0.0
Time: THU JAN 01 00:00:31 1970 (ticks = 1880)
Task: "t1" (0x0246d010)

freeing part of allocated memory block
PARTITION: 0x269888
    PTR=0x246bea2
    BLOCK: allocated at 0x0246bea0, 24 bytes

<<<<<Traceback>>>>>
0x0011d240 vxTaskEntry +0xS4 : heapErrors ()
0x00111364 heapErrors +0x24 : free ()
0x001c26f8 memPartFree +0xa4 : 0x001bdbb4 ()
0x001bdc6c memEdrItemGet+0x588: 0x001bd71c ()

==[2/2]==============================================================================
Severity/Facility: NON-FATAL/KERNEL
Boot Cycle: 20
OS Version: 6.0.0
Time: THU JAN 01 00:00:31 1970 (ticks = 1880)
Task: "t1" (0x0246d010)

freeing memory in free list
PARTITION: 0x269888
    PTR=0x246bea0
    BLOCK: free block at 0x0246bea0, 24 bytes

<<<<<Traceback>>>>>
0x0011d240 vxTaskEntry +0xS4 : heapErrors ()
0x00111374 heapErrors +0x34 : free ()
0x001c26f8 memPartFree +0xa4 : 0x001dbb4 ()
0x001bdc6c memEdrItemGet+0x588: 0x001bd71c ()
value = 0 = 0x0
5.8.2 Compiler Instrumentation

Additional errors are detected if the application is compiled using the Run-Time Error Checking (RTEC) feature of the Wind River Compiler (Diab). The following flag should be used:

\[-Xrtc=option\]

**NOTE:** This feature is not available with the GNU compiler.

Code compiled with the `-Xrtc` flag is instrumented for runtime checks such as pointer reference check and pointer arithmetic validation, standard library parameter validation, and so on. These instrumentations are supported through the memory partition run-time error detection library. Table 5-2 lists the `-Xrtc` options that are supported.

<table>
<thead>
<tr>
<th>Option</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>0x01</td>
<td>register and check static (global) variables</td>
</tr>
<tr>
<td>0x02</td>
<td>register and check automatic variables</td>
</tr>
<tr>
<td>0x08</td>
<td>pointer reference checks</td>
</tr>
<tr>
<td>0x10</td>
<td>pointer arithmetic checks</td>
</tr>
<tr>
<td>0x20</td>
<td>pointer increment/decrement checks</td>
</tr>
<tr>
<td>0x40</td>
<td>standard function checks; for example <code>memset()</code> and <code>bcopy()</code></td>
</tr>
<tr>
<td>0x80</td>
<td>report source code filename and line number in error logs</td>
</tr>
</tbody>
</table>

The errors and warnings detected by the RTEC compile-in instrumentation are logged by the error detection and reporting facility (see 9. Error Detection and Reporting). The following error types are identified:

- Bounds-check violation for allocated memory blocks.
- Bounds-check violation of static (global) variables.
- Bounds-check violation of automatic variables.
- Reference to a block in the free queue.
- Reference to the free part of the task’s stack.
- De-referencing a NULL pointer.
Configuring VxWorks for RTEC Support

Support for this feature in the kernel is enabled by adding the `INCLUDE_MEM_EDR_RTC` component, as well as the components described in section *Configuring VxWorks with Memory Partition and Heap Instrumentation*, p. 290.

Shell Commands

The compiler provided instrumentation automatically logs errors detected in applications using the error detection and reporting facility. For a list of the shell commands available for error logs see *9.4 Displaying and Clearing Error Records*, p. 484.

Code Example

Application code built with the RTEC instrumentation has compiler-generated constructors. To ensure that the constructors are called when a module is dynamically downloaded, the module must be processed similarly to a C++ application. For example, the following make rule can be used:

```
TGT_DIR=$(WIND_BASE)/target

%.out : %.c
@ $(RM) $@
$(CC) $(CFLAGS) -Xrtc=0xfb $(OPTION_OBJECT_ONLY) memEdrDemo.c
@ $(RM) ctdt_$(BUILD_EXT).c
$(NM) $(basename $@).o | $(MUNCH) > ctdt_$(BUILD_EXT).c
$(MAKE) CC_COMPILER=$(OPTION_DOLLAR_SYMBOLS) ctdt_$(BUILD_EXT).o
$(LD_PARTIAL) $(LD_PARTIAL_LAST_FLAGS) $(OPTION_OBJECT_NAME)$@ $(basename $@).o ctdt_$(BUILD_EXT).o
include $(TGT_DIR)/h/make/rules.library
```
The following application code generates various errors that can be recorded and displayed (line numbers are included for reference purposes). Its use is illustrated in Shell Session Example, p.298

```c
#include <vxWorks.h>
#include <stdlib.h>
#include <string.h>

void refErrors ()
{
    char name[] = "very_long_name";
    char * pChar;
    int state[] = { 0, 1, 2, 3 };
    int ix = 0;

    pChar = malloc (13);
    memcpy (pChar, name, strlen (name)); /* bounds check violation */

    for (ix = 0; ix < 4; ix++)
        state[ix] = state [ix + 1]; /* bounds check violation */

    free (pChar);

    memcpy (pChar, "another_name", 12); /* reference a free block */
}
```

**Shell Session Example**

The following shell session log is executed with the C interpreter. The sample code listed above is compiled and linked in the VxWorks kernel (see 2.7.7 Linking Kernel-Based Application Object Modules with VxWorks, p.65). The kernel must include the INCLUDE_MEM_EDR and INCLUDE_MEM_EDR_RTC components. Also, the kernel should be configured with the error detection and reporting facility, including the show routines, as described in 9.2 Configuring Error Detection and Reporting Facilities, p.481.

First, clear the error log to start with a clean log:

```
-> edrClear
value = 0 = 0x0
```

Start the kernel application in a new task spawned with the `sp()` utility:

```
-> sp refErrors
   Task spawned: id = 0x246d7d0, name = t2
   value = 38197200 = 0x246d7d0
```

At this point the application finished execution. Errors detected while executing the application are logged in the persistent memory region. Display the log using
edrShow(). In the example below, the log display is interspersed with description of the errors.

-> edrShow
    ERROR LOG
    ========
    Log Size:         524288 bytes (128 pages)
    Record Size:      4096 bytes
    Max Records:      123
    CPU Type:         0x5a
    Errors Missed:    0 (old) + 0 (recent)
    Error count:      3
    Boot count:       21
    Generation count: 97

The first error corresponds to line 13 in the test code. A string of length 14 is copied into a allocated buffer of size 13:

==[1/3]===[==============================================================
Severity/Facility:   NON-FATAL KERNEL
Boot Cycle:          21
OS Version:          6.0.0
Time:                THU JAN 01 00:14:22 1970 (ticks = 51738)
Task:                "t2" (0x0246d7d0)
Injection Point:     refErr.c:13
memory block bounds-check violation
    PTR=0x246be60 OFFSET=0 SIZE=14
    BLOCK: allocated at 0x0246be60, 13 bytes
<<<<<Traceback>>>>>
0x0011d240 vxTaskEntry +0x54 : 0x00111390 ()
0x00111470 refErrors +0xe4 : __rtc_chk_at ()
0x001bd02c memEdrErrorLog+0x13c: _sigCtxSave ()
The second error refers to line 18: the local **state** array is referenced with index 4. Since the array has only four elements, the range of valid indexes is 0 to 3:

```
===[2/3]===============================================
Severity/Facility: NON-FATAL/KERNEL
Boot Cycle: 21
OS Version: 6.0.0
Time: THU JAN 01 00:14:22 1970 (ticks = 51738)
Task: "t2" (0x0246d7d0)
Injection Point: refErr.c:18

defined block bounds-check violation
PTR=0x278ba94 OFFSET=16 SIZE=4
BLOCK: automatic at 0x0278ba94, 16 bytes

<<<Traceback>>>>>
0x0011d240 vxTaskEntry +0x54 : 0x00111390 ()
0x001114a0 refErrors +0x114: __rtc_chk_at ()
0x001bd02c memEdrErrorLog+0x13c: _sigCtxSave ()
```

The last error is caused by the code on line 22. A memory block that has been freed is being modified:

```
===[3/3]===============================================
Severity/Facility: NON-FATAL/KERNEL
Boot Cycle: 21
OS Version: 6.0.0
Time: THU JAN 01 00:14:22 1970 (ticks = 51739)
Task: "t2" (0x0246d7d0)
Injection Point: refErr.c:22

defined block bounds-check violation
PTR=0x246be60 OFFSET=0 SIZE=12
BLOCK: free block at 0x0246be60, 13 bytes

<<<Traceback>>>>>
0x0011d240 vxTaskEntry +0x54 : 0x00111390 ()
0x00111518 refErrors +0x18c: __rtc_chk_at ()
0x001bd02c memEdrErrorLog+0x13c: _sigCtxSave ()
```
5.9 Virtual Memory Management

VxWorks can be configured with an architecture-independent interface to the
CPU’s memory management unit (MMU) to provide virtual memory support.
This support includes the following features:

- Setting up the kernel memory context at boot time.
- Mapping pages in virtual space to physical memory.
- Setting caching attributes on a per-page basis.
- Setting protection attributes on a per-page basis.
- Setting a page mapping as valid or invalid.
- Locking and unlocking TLB entries for pages of memory.
- Enabling page optimization.

The programmable elements of virtual memory (VM) support are provided by the
vmBaseLib library.

When process (RTP) support is included in VxWorks with the INCLUDE_RTP
component, the virtual memory facilities also provide system support for
managing multiple virtual memory contexts, such as creation and deletion of
process memory context.

For information about additional MMU-based memory protection features
beyond basic virtual memory support, see 5.10 Additional Memory Protection
Features, p.310.

Also note that errors (exceptions) generated with the use of virtual memory
features can be detected and managed with additional VxWorks facilities. See

5.9.1 Configuring Virtual Memory Management

The components listed in Table 5-3 provide basic virtual memory management, as
well as show routines for use from the shell.
Configuring the Kernel Virtual Memory Context

The kernel virtual memory context is created automatically at boot time based on configuration data provided by the BSP. The primary data is in the `sysPhysMemDesc[]` table, which is usually defined in the BSP’s `sysLib.c` file. The table defines the initial kernel mappings and initial attributes. The entries in this table are of `PHYS_MEM_DESC` structure type, which is defined in `vmLib.h`.

There is usually no need to change the default `sysPhysMemDesc[]` configuration. However, modification may be required or advisable, for example, when:

- New driver support or new devices (for example, flash memory) are added to the system.
- The protection or cache attributes of certain entries need to be changed. For example, entries for flash memory can be read-only if the content of the flash

---

**Table 5-3 MMU Components**

<table>
<thead>
<tr>
<th>Constant</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>INCLUDE_MMU_GLOBAL_MAP</td>
<td>Initialize the kernel's global MMU mappings according to the BSP’s <code>sysPhysMemDesc[]</code> table. See Configuring the Kernel Virtual Memory Context, p.302.</td>
</tr>
<tr>
<td>INCLUDE_MMU_BASIC</td>
<td>Include the <code>vmBaseLib</code> API, which is used for programmatic management of virtual memory (see 5.9.2 Managing Virtual Memory Programmatically, p.304).</td>
</tr>
<tr>
<td>INCLUDE_LOCK_TEXT_SECTION</td>
<td>Kernel text TLB locking optimization.</td>
</tr>
<tr>
<td>INCLUDE_PAGE_SIZE_OPTIMIZATION</td>
<td>Page size optimization for the kernel.</td>
</tr>
<tr>
<td>INCLUDE_VM_SHOW</td>
<td>Virtual memory show routines for the shell C interpreter.</td>
</tr>
<tr>
<td>INCLUDE_VM_SHOW_SHELL_CMD</td>
<td>Virtual memory show commands for the shell command interpreter.</td>
</tr>
</tbody>
</table>

For information about related components see 5.10 Additional Memory Protection Features, p.310.
is never written from VxWorks. However, if a flash driver such as TrueFFS is used, the protection attribute has to be set to writable.

- There are unused entries in the table. In general, it is best to keep only those entries that actually describe the system, as each entry may require additional system RAM for page tables (depending on size of the entry, its location relative to other entries, and architecture-specific MMU parameters). The larger the memory blocks mapped, the more memory is used for page tables.

The `sysPhysMemDesc[]` table can be modified at runtime. This is useful, for example, with PCI drivers that can be auto-configured, which means that memory requirements are detected at runtime. In this case the size and address fields can be updated programatically for the corresponding `sysPhysMemDesc[]` entries. It is important to make such updates before the VM subsystem is initialized by `usrMmuInit()`, for example during execution of `sysHwInit()`.

⚠️ **CAUTION:** The regions of memory defined in `sysPhysMemDesc[]` must be page-aligned, and must span complete pages. In other words, the first three fields (virtual address, physical address, and length) of a `PHYS_MEM_DESC` structure must all be even multiples of the MMU page size. Specifying elements of `sysPhysMemDesc[]` that are not page-aligned causes the target to reboot during initialization. See the *VxWorks Architecture Supplement* to determine what page size is supported for the architecture in question.

### Configuration Example

This example is based on multiple CPUs using the shared-memory network. A separate memory board is used for the shared-memory pool. Because this memory is not mapped by default, it must be added to `sysPhysMemDesc[]` for all the boards on the network. The memory starts at 0x4000000 and must be made non-cacheable, as shown in the following code fragment:

```c
/* shared memory */
{
  (VIRT_ADDR) 0x4000000, /* virtual address */
  (PHYS_ADDR) 0x4000000, /* physical address */
  0x20000, /* length */
  /* initial state mask */
  MMU_ATTR_VALID_MSK | MMU_ATTR_PROT_MSK | MMU_ATTR_CACHE_MSK,
  /* initial state */
  MMU_ATTR_VALID | MMU_ATTR_PROT_SUP_READ | MMU_ATTR_PROT_SUP_WRITE | MMU_ATTR_CACHE_OFF
}
```

For some architectures, the system RAM (the memory used for the VxWorks kernel image, kernel heap, and so on) must be identity mapped. This means that
for the corresponding entry in the `sysPhysMemDesc[]` table, the virtual address must be the same as the physical address. For more information see 5.3 System Memory Maps, p.277 and the VxWorks Architecture Supplement.

5.9.2 Managing Virtual Memory Programmatically

This section describes the facilities provided for manipulating the MMU programmatically using low-level routines in `vmBaseLib`. You can make portions of memory non-cacheable, write-protect portions of memory, invalidate pages, lock TLB entries, or optimize the size of memory pages.

For more information about the virtual memory routines, see the VxWorks API reference for `vmBaseLib`.

Modifying Page States

Each virtual memory page (typically 4 KB) has a state associated with it. A page can be valid/invalid, readable, writable, executable, or cacheable/non-cacheable.

The state of a page can be changed with the `vmStateSet()` routine. See Table 5-4 and Table 5-5 for lists of the page state constants and page state masks that can be used with `vmStateSet()`. A page state mask must be used to describe which flags are being changed. A logical OR operator can be used with states and masks to define both mapping protection and cache attributes.

<table>
<thead>
<tr>
<th>Constant</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>MMU_ATTR_VALID</td>
<td>Valid translation</td>
</tr>
<tr>
<td>MMU_ATTR_VALID_NOT</td>
<td>Invalid translation</td>
</tr>
<tr>
<td>MMU_ATTR_PRO_SUP_READ</td>
<td>Readable memory in kernel mode</td>
</tr>
<tr>
<td>MMU_ATTR_PRO_SUP_WRITE</td>
<td>Writable memory in kernel mode</td>
</tr>
<tr>
<td>MMU_ATTR_PRO_SUP_EXE</td>
<td>Executable memory in kernel mode</td>
</tr>
<tr>
<td>MMU_ATTR_PRO_USR_READ</td>
<td>Readable memory in user mode</td>
</tr>
<tr>
<td>MMU_ATTR_PRO_USR_WRITE</td>
<td>Writable memory in user mode</td>
</tr>
</tbody>
</table>
### Table 5-4  Page State Constants (cont'd)

<table>
<thead>
<tr>
<th>Constant</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>MMU_ATTR_PRO_USR_EXE</td>
<td>Executable memory in user mode</td>
</tr>
<tr>
<td>MMU_ATTR_CACHE_OFF</td>
<td>Non-cacheable memory</td>
</tr>
<tr>
<td>MMU_ATTR_CACHE_COPYBACK</td>
<td>Cacheable memory, copyback mode</td>
</tr>
<tr>
<td>MMU_ATTR_CACHE_WRITETHRU</td>
<td>Cacheable memory, writethrough mode</td>
</tr>
<tr>
<td>MMU_ATTR_CACHE_DEFAULT</td>
<td>Default cache mode (equal to either COPYBACK, WRITETHRU, or CACHE_OFF, depending on the setting of USER_D_CACHE_MODE)</td>
</tr>
<tr>
<td>MMU_ATTR_CACHE_COHERENCY</td>
<td>Memory coherency is enforced (not supported on all architectures; for more information, see the VxWorks Architecture Supplement)</td>
</tr>
<tr>
<td>MMU_ATTR_CACHE_GUARDED</td>
<td>Prevent out-of-order load operations, and pre-fetches (not supported on all architectures; for more information, see the VxWorks Architecture Supplement)</td>
</tr>
<tr>
<td>MMU_ATTR_NO_BLOCK</td>
<td>Page attributes can be changed from ISR.</td>
</tr>
<tr>
<td>MMU_ATTR_SPL_0</td>
<td>Optional Architecture Specific States (only used by some architectures; for more information, see the VxWorks Architecture Supplement)</td>
</tr>
<tr>
<td>...</td>
<td></td>
</tr>
<tr>
<td>MMU_ATTR_SPL_7</td>
<td></td>
</tr>
</tbody>
</table>

### Table 5-5  Page State Masks

<table>
<thead>
<tr>
<th>Constant</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>MMU_ATTR_VALID_MSK</td>
<td>Modify valid flag</td>
</tr>
<tr>
<td>MMU_ATTR_PROT_MSK</td>
<td>Modify protection flags</td>
</tr>
<tr>
<td>MMU_ATTR_CACHE_MSK</td>
<td>Modify cache flags</td>
</tr>
<tr>
<td>MMU_ATTR_SPL_MSK</td>
<td>Modify architecture specific flags</td>
</tr>
</tbody>
</table>
Not all combinations of protection settings are supported by all CPUs. For example, many processor types do not provide setting for execute or non-execute settings. On such processors, readable also means executable.

For information about architecture-specific page states and their combination, see the *VxWorks Architecture Supplement*.

**Making Memory Non-Writable**

Sections of memory can be write-protected using `vmStateSet()` to prevent inadvertent access. This can be used, for example, to restrict modification of a data object to a particular routine. If a data object is global but read-only, tasks can read the object but not modify it. Any task that must modify this object must call the associated routine. Inside the routine, the data is made writable for the duration of the routine, and on exit, the memory is set to `MMU_ATTR_PROT_SUP_READ`.

**Nonwritable Memory Example**

In this code example, a task calls `dataModify()` to modify the data structure pointed to by `pData`. This routine makes the memory writable, modifies the data, and sets the memory back to nonwritable. If a task subsequently tries to modify the data without using `dataModify()`, a data access exception occurs.

```c
/* privateCode.h - header file to make data writable from routine only */
#define MAX 1024
typedef struct myData
{
    char stuff[MAX];
    int moreStuff;
} MY_DATA;

/* privateCode.c - uses VM contexts to make data private to a code segment */
#include <vxWorks.h>
#include <string.h>
#include <vmLib.h>
#include <semLib.h>
#include "privateCode.h"
MY_DATA * pData;
SEM_ID dataSemId;
int pageSize;

/** initData - allocate memory and make it nonwritable
 * This routine initializes data and should be called only once.
 */
STATUS initData (void)
{
```
pageSize = vmPageSizeGet();
/* create semaphore to protect data */
dataSemId = semBCreate (SEM_Q_PRIORITY, SEM_EMPTY);
/* allocate memory = to a page */
pData = (MY_DATA *) valloc (pageSize);
/* initialize data and make it read-only */
bzero ((char *) pData, pageSize);
if (vmStateSet (NULL, (VIRT_ADDR) pData, pageSize, MMU_ATTR_PROT_MSK,
               MMU_ATTR_PROT_SUP_READ) == ERROR)
  {
    semGive (dataSemId);
    return (ERROR);
  }
/* release semaphore */
semGive (dataSemId);
return (OK);
}

/*******************************************************************************
* dataModify - modify data
* To modify data, tasks must call this routine, passing a pointer to
* the new data.
* To test from the shell use:
* - -> initData
* - -> sp dataModify
* - -> d pData
* - -> bfill (pData, 1024, 'X')
*/
STATUS dataModify
(
  MY_DATA * pNewData
)
{
  /* take semaphore for exclusive access to data */
  semTake (dataSemId, WAIT_FOREVER);
  /* make memory writable */
  if (vmStateSet (NULL, (VIRT_ADDR) pData, pageSize, MMU_ATTR_PROT_MSK,
                  MMU_ATTR_PROT_SUP_READ | MMU_ATTR_PROT_SUP_WRITE) == ERROR)
    {
      semGive (dataSemId);
      return (ERROR);
    }
  /* update data*/
bcopy ((char *) pNewData, (char *) pData, sizeof(MY_DATA));
  /* make memory not writable */
  if (vmStateSet (NULL, (VIRT_ADDR) pData, pageSize, MMU_ATTR_PROT_MSK,
                  MMU_ATTR_PROT_SUP_READ) == ERROR)
    {
      semGive (dataSemId);
      return (ERROR);
    }
  semGive (dataSemId);
  return (OK);
}
Invalidating Memory Pages

To invalidate memory on a page basis, use `vmStateSet()` as follows:

```
vmStateSet (NULL, address, len, MMU_ATTR_VALID_MSK, MMU_ATTR_VALID_NOT);
```

Any access to a mapping made invalid generates an exception whether it is a read or a write access.

To re-validate the page, use `vmStateSet()` as follows:

```
vmStateSet (NULL, address, len, MMU_ATTR_VALID_MSK, MMU_ATTR_VALID);
```

Locking TLB Entries

For some processors it is possible to force individual entries in the Translation Look-aside Buffer (TLB) to remain permanently in the TLB. When the architecture-specific MMU library supports this feature, the `vmPageLock()` routine can be used to lock page entries, and `vmPageUnlock()` to unlock page entries.

The `INCLUDE_LOCK_TEXT_SECTION` component provides facilities for TLB locking. When this component is included in VxWorks, the kernel image text section is automatically locked at system startup.

This feature can be used for performance optimizations in a manner similar to cache locking. When often-used page entries are locked in the TLB, the number of TLB misses can be reduced. Note that the number of TLB entries are generally limited on all processors types, so locking too many entries can result in contention for the remaining entries that are used dynamically.

For more information, see the *VxWorks Architecture Supplement*.

Page Size Optimization

For some processors it is possible to enable larger page sizes than the default (defined by `VM_PAGE_SIZE`) for large, contiguous memory blocks that have homogeneous memory attributes. There are several advantages to using such optimization, including:

- Reducing the number of page table entries (PTE) needed to map memory, resulting in less memory used.
- More efficient TLB entry usage, resulting in fewer TLB misses, therefore potentially better performance.
Optimization of the entire kernel memory space (including I/O blocks) at startup can be accomplished by configuring VxWorks with the
\texttt{INCLUDE\_PAGE\_SIZE\_OPTIMIZATION} component.

Page size optimization for specific blocks of memory can be accomplished at runtime with the \texttt{vmPageOptimize()} routine.

De-optimization is performed automatically when necessary. For example, if part of a memory block that has been optimized is set with different attributes, the large page is automatically broken up into multiple smaller pages and the new attribute is set to the requested pages only.

\textbf{Setting Page States in ISRs}

For many types of processors, \texttt{vmStateSet()} is a non-blocking routine, and can therefore be called safely from ISRs. However, it may block in some cases, such as on processors that support page size optimization (see \textit{Page Size Optimization}, p.308).

To make sure that \texttt{vmStateSet()} can be called safely from an ISR for specific pages, the page must first have the \texttt{MMU\_ATTR\_NO\_BLOCK} attribute set. The following code example shows how this can be done:

\begin{verbatim}
#include <vxWorks.h>
#include <vmLib.h>

#define DATA_SIZE   0x10000
char * pData;

void someInitFunction ()
{
    /* allocate buffer */
    pData = (char *) valloc (DATA_SIZE);
    /* set no-block attribute for the buffer */
    vmStateSet (NULL, (VIRT_ADDR) pData, DATA_SIZE, MMU_ATTR_SPL_MSK, MMU_ATTR_NO_BLOCK);
}

void someISR ()
{
    ...
    /* now it's safe to set any attribute for the buffer in an ISR */
\end{verbatim}
vmStateSet(NULL, (VIRT_ADDR) pData, DATA_SIZE,
            MMU_ATTR_PROT_MSK, MMU_ATTR_SUP_RWX);

...
Memory Management

5.10 Additional Memory Protection Features

- interrupt stack overrun and underrun detection
- non-executable task stacks
- text segment write-protection
- exception vector table write-protection

For information about basic virtual memory support, see 5.9 Virtual Memory Management, p.301.

Errors generated with the use of these features can be detected and managed with additional VxWorks facilities. See 9. Error Detection and Reporting for more information.

5.10.1 Configuring VxWorks for Additional Memory Protection

The components listed in Table 5-7 provide additional memory-protection features. They can be added to VxWorks as a unit with the INCLUDE_KERNEL_HARDENING component. The individual and composite components all include the basic virtual memory component INCLUDE_MMU_BASIC by default.

Table 5-7 Additional Memory Protection Components

<table>
<thead>
<tr>
<th>Component</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>INCLUDE_PROTECT_TASK_STACK</td>
<td>Task stack overrun and underrun protection.</td>
</tr>
<tr>
<td>INCLUDE_TASK_STACK_NO_EXEC</td>
<td>Non-executable task stacks.</td>
</tr>
<tr>
<td>INCLUDE_PROTECT_TEXT</td>
<td>Text segment write-protection.</td>
</tr>
<tr>
<td>INCLUDE_PROTECT_VEC_TABLE</td>
<td>Exception vector table write-protection and NULL pointer reference detection.</td>
</tr>
<tr>
<td>INCLUDE_PROTECT_INTERRUPT_STACK</td>
<td>Interrupt stack overrun and underrun protection.</td>
</tr>
</tbody>
</table>

Note that protection of the kernel text segment—and the text segments of kernel modules dynamically loaded into the kernel space—is not provided by default. On the other hand, the text segment of processes and shared libraries is always write-protected, whether or not VxWorks is configured with the INCLUDE_PROTECT_TEXT component. Similarly, the execution stack of a process
task is not affected by the INCLUDE_PROTECT_TASK_STACK or INCLUDE_TASK_STACK_NO_EXEC components—it is always protected unless the task is spawned with the taskSpawn() option VX_NO_STACK_PROTECT.

5.10.2 Stack Overrun and Underrun Detection

VxWorks can be configured so that guard zones are inserted at the beginning and end of task execution stacks.

The operating system can also be configured to insert guard zones at both ends of the interrupt stack.

An overrun guard zone prevents a task from going beyond the end of its predefined stack size and ensures that the integrity of the system is not compromised. An underrun guard zone typically prevents buffer overflows from corrupting memory above the stack. The CPU generates an exception when a task attempts to access any of the guard zones. The size of a stack is always rounded up to a MMU page size when either a guard zone is inserted or when the stack is made non-executable.

Task Stack Overrun and Underrun Detection

Guard zones are inserted at the end and start of kernel and process (RTP) stacks when VxWorks is configured with the INCLUDE_PROTECT_TASK_STACK component. The size of the guard zones can be configured uniquely for kernel and process tasks. The size of the guard zones are defined by the following configuration parameters:

- TASK_USER_EXEC_STACK_OVERFLOW_SIZE for user task execution stack overflow size.
- TASK_USER_EXEC_STACK_UNDERFLOW_SIZE for user task execution stack underflow size.
- TASK_USER_EXC_STACK_OVERFLOW_SIZE for user task exception stack overflow size.
- TASK_KERNEL_EXEC_STACK_OVERFLOW_SIZE for kernel task execution stack overflow size.
- TASK_KERNEL_EXEC_STACK_UNDERFLOW_SIZE for kernel task execution stack underflow size.
5 Memory Management

5.10 Additional Memory Protection Features

The value of these parameters can be modified to increase the size of the guard zones on a system-wide basis. The size of a guard zone is also rounded up to the CPU MMU page size. The insertion of a guard zone can be prevented by setting the parameter to zero.

Guard zones can be disabled for a specific task with the VX_NO_STACK_PROTECT taskSpawn() option.

Note that for stacks allocated in the kernel, stack guard zones consume RAM, as guard zones correspond to mapped memory for which accesses are made invalid.

For general information about task stacks, see Task Stack, p.140 for kernel information, and VxWorks Application Programmer’s Guide: Multitasking for process information.

Interrupt Stack Overrun and Underrun Detection

Guard zones are inserted at the end and start of the interrupt stack when VxWorks is configured with the INCLUDE_PROTECT_INTERRUPT_STACK component. The sizes of the guard zones are defined by the following configuration parameters:

- INTERRUPT_STACK_OVERFLOW_SIZE for interrupt stack overflow size.
- INTERRUPT_STACK_UNDERFLOW_SIZE for interrupt stack underflow size.

The value of these parameters can be modified to increase the size of the guard zones on a system-wide basis. The size of a guard zone is also rounded up to the CPU MMU page size. The insertion of a guard zone can be prevented by setting the parameter to zero.

For general information about the interrupt stack, see 3.5.2 Interrupt Stack, p.210.

5.10.3 Non-Executable Task Stack

VxWorks creates kernel task stacks with a non-executable attribute only if it is configured with the INCLUDE_TASK_STACK_NO_EXEC component, and if the CPU supports making memory non-executable on a page basis. The size of a stack is always rounded up to a MMU page size when the stack is made non-executable (as is also the case when guard zones are inserted).
5.10.4 **Text Segment Write Protection**

All text segments are write-protected when VxWorks is configured with the **INCLUDE_PROTECT_TEXT** component. When VxWorks is loaded, all text segments are write-protected. The text segments of any additional object modules loaded in the kernel space using `ld()` are automatically marked as read-only. When object modules are loaded, memory that is to be write-protected is allocated in page-size increments. No additional steps are required to write-protect kernel application code.

5.10.5 **Exception Vector Table Write Protection**

When VxWorks is configured with the **INCLUDE_PROTECT_VEC_TABLE** component, the exception vector table is write-protected during system initialization.

The architecture-specific API provided to modify the vector table automatically write-enables the exception vector table for the duration of the call. For more information about these APIs, see the *VxWorks Architecture Supplement* for the architecture in question.

5.11 **Processes Without MMU Support**

VxWorks can be configured to provide support for real-time processes on a system based on a processor without an MMU, or based on a processor with MMU but with the MMU disabled.

With this configuration, a software simulation-based memory page management library keeps track of identity mappings only. This means that there is no address translation, and memory page attributes (protection attributes and cache attributes) are not supported.

The advantages of such configuration are that it:

- Enables the process environment on systems without an MMU. It provides private namespace for applications, for building applications independently from the kernel, and for simple migration from systems without an MMU to those with one.
5 Memory Management
5.11 Processes Without MMU Support

- Allows application code be run in non-privileged (user) mode.
- Under certain conditions, it may provide increased performance by eliminating overhead of the TLB miss and reload. This assumes, however, that there is no negative impact due to the changed cache conditions.

The limitations of this configuration are:
- Depending on the processor type, BSP configuration, drivers and OS facilities used, disabling the MMU may require disabling the data cache as well. Disabling the data cache results in significant performance penalty that is much greater than the benefit derived from avoiding TLB misses.
- There is no memory protection. That is, memory cannot be write-protected, and neither the kernel or any process are protected from other processes.
- The address space is limited to the available system RAM, which is typically smaller than it would be available on systems with MMU-based address translation enabled. Because of the smaller address space, a system is more likely to run out of large contiguous blocks of memory due to fragmentation.
- Not all processors and target boards can be used with the MMU disabled. For the requirements of your system see the hardware manual of the board and processor used.

For information about architecture and processor-specific limitations, see the VxWorks Architecture Supplement.

Configuring VxWorks With Process Support for Systems Without an MMU

There are no special components needed for the process environment with software-simulated paging. As with any configurations that provide process support, the INCLUDE_RTP component must be added to the kernel.

The steps required to enable software-simulated paging are:

1. Add the INCLUDE_RTP component to include process support. This automatically includes all dependent subsystems, among them INCLUDE_MMU_BASIC.

2. Change the SW_MMU_ENABLE parameter of the INCLUDE_MMU_BASIC component to TRUE (the default value is FALSE).

In addition, the following optional configuration steps can reduce the footprint of the system:
3. Change the `VM_PAGE_SIZE` parameter of the `INCLUDE_MMU_BASIC` component. The default is architecture-dependent; usually 4K or 8K. Allowed values are 1K, 2K, 4K, 8K, 16K, 32K, 64K. Typically, a smaller page size results in finer granularity and therefore more efficient use of the memory space. However, smaller page size requires more memory needed for keeping track the mapping information.

4. Disable stack guard page protection by changing the `TASK_STACK_OVERFLOW_SIZE` and `TASK_STACK_UNDERFLOW_SIZE` configuration parameters to zero. Without protection provided by an MMU, stack overflow and underflow cannot be detected, so the guard pages serve no purpose.

5. Remove the following components from the VxWorks configuration: `INCLUDE_KERNEL_HARDENING`, `INCLUDE_PROTECT_TEXT`, `INCLUDE_PROTECT_VEC_TABLE`, `INCLUDE_PROTECT_TASK_STACK`, `INCLUDE_TASK_STACK_NO_EXEC`, and `INCLUDE_PROTECT_INTERRUPT_STACK`. Without an MMU, these features do not work. Including them only results in unnecessary consumption of resources.
6

I/O System

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6.9 Internal I/O System Structure 374
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6.11 Peripheral Component Interconnect: PCI 400
6.1 Introduction

The VxWorks I/O system is designed to present a simple, uniform, device-independent interface to any kind of device, including:

- character-oriented devices such as terminals or communications lines
- random-access block devices such as disks
- virtual devices such as intertask pipes and sockets
- monitor and control devices such as digital and analog I/O devices
- network devices that give access to remote devices

The VxWorks I/O system provides standard C libraries for both basic and buffered I/O. The basic I/O libraries are UNIX-compatible; the buffered I/O libraries are ANSI C-compatible.

Internally, the VxWorks I/O system has a unique design that makes it faster and more flexible than most other I/O systems. These are important attributes in a real-time system.

This chapter first describes the nature of files and devices, and the user view of basic and buffered I/O. The next section discusses the details of some specific devices. The third section is a detailed discussion of the internal structure of the VxWorks I/O system. The final sections describe PCMCIA and PCI support.

The diagram in Figure 6-1 illustrates the relationships between the different elements of the VxWorks I/O system. All of these elements are discussed in this chapter, except for file system routines (which are dealt with in 7. Local File Systems), and the network elements (which are covered in the Wind River Network Stack for VxWorks 6 Programmer’s Guide).
6 I/O System
6.1 Introduction

Figure 6-1   Overview of the VxWorks I/O System

Application

Basic I/O Routines (device independent)
read()
write()

Driver Routines
xxRead()
xxWrite()

Hardware Devices
Network
Disk Drive
Serial Device

Buffered I/O: stdio
fread()
fwrite()

fioLib
fioRead()
printf()
sprintf()

Network Sockets Interface
send()
recv()

File System Routines
xxRead()
xxWrite()

Network Stack Routines
bsdSend()
bsdReceive()

Library Routines
tyLib

XBD Routines
xyzStrategy()

NOTE: This chapter provides information about facilities available in the VxWorks kernel. For information about facilities available to real-time processes, see the corresponding chapter in the VxWorks Application Programmer's Guide.
6.2 Files, Devices, and Drivers

In VxWorks, applications access I/O devices by opening named files. A file can refer to one of two things:

- An unstructured raw device such as a serial communications channel or an intertask pipe.
- A logical file on a structured, random-access device containing a file system.

Consider the following named files:

- `/usr/myfile`
- `/pipe/mypipe`
- `/tyCo/0`

The first refers to a file called `myfile`, on a disk device called `/usr`. The second is a named pipe (by convention, pipe names begin with `/pipe`). The third refers to a physical serial channel. However, I/O can be done to or from any of these in the same way. Within VxWorks, they are all called files, even though they refer to very different physical objects.

Devices are handled by program modules called drivers. In general, using the I/O system does not require any further understanding of the implementation of devices and drivers. Note, however, that the VxWorks I/O system gives drivers considerable flexibility in the way they handle each specific device. Drivers conform to the conventional user view presented here, but can differ in the specifics. See 6.7 Devices in VxWorks, p.346.

Although all I/O is directed at named files, it can be done at two different levels: basic and buffered. The two differ in the way data is buffered and in the types of calls that can be made. These two levels are discussed in later sections.

6.2.1 Filenames and the Default Device

A filename is specified as a character string. An unstructured device is specified with the device name. In the case of file system devices, the device name is followed by a filename. Thus, the name `/tyCo/0` might name a particular serial I/O channel, and the name `DEV1:/file1` indicates the file `file1` on the `DEV1:` device.

When a filename is specified in an I/O call, the I/O system searches for a device with a name that matches at least an initial substring of the filename. The I/O function is then directed at this device.
If a matching device name cannot be found, then the I/O function is directed at a default device. You can set this default device to be any device in the system, including no device at all, in which case failure to match a device name returns an error. You can obtain the current default path by using `ioDefPathGet()` and set the default path by using `ioDefPathSet()`.

Non-block devices are named when they are added to the I/O system, usually at system initialization time. Block devices are named when they are initialized for use with a specific file system. The VxWorks I/O system imposes no restrictions on the names given to devices. The I/O system does not interpret device or filenames in any way, other than during the search for matching device and filenames.

It is useful to adopt some naming conventions for device and file names: most device names begin with a slash (`/`), except non-NFS network devices, and VxWorks HRFS and dosFs file system devices.

By convention, NFS-based network devices are mounted with names that begin with a slash. For example:

```
/usr
```

Non-NFS network devices are named with the remote machine name followed by a colon. For example:

```
host:
```

The remainder of the name is the filename in the remote directory on the remote system.

File system devices using dosFs are often named with uppercase letters and digits followed by a colon. For example:

```
DEV1:
```

**NOTE:** Filenames and directory names on dosFs devices are often separated by backslashes (`\`). These can be used interchangeably with forward slashes (`/`).

**CAUTION:** Because device names are recognized by the I/O system using simple substring matching, a slash (`/` or `\`) should not be used alone as a device name, nor should a slash be used as any part of a device name itself.
6.3 Basic I/O

Basic I/O is the lowest level of I/O in VxWorks. The basic I/O interface is source-compatible with the I/O primitives in the standard C library. There are seven basic I/O calls, shown in Table 6-1.

<table>
<thead>
<tr>
<th>Routine</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>creat( )</td>
<td>Creates a file.</td>
</tr>
<tr>
<td>remove( )</td>
<td>Deletes a file.</td>
</tr>
<tr>
<td>open( )</td>
<td>Opens a file (optionally, creates a file if it does not already exist.)</td>
</tr>
<tr>
<td>close( )</td>
<td>Closes a file.</td>
</tr>
<tr>
<td>read( )</td>
<td>Reads a previously created or opened file.</td>
</tr>
<tr>
<td>write( )</td>
<td>Writes to a previously created or opened file.</td>
</tr>
<tr>
<td>ioctl( )</td>
<td>Performs special control functions on files.</td>
</tr>
</tbody>
</table>

6.3.1 File Descriptors

At the basic I/O level, files are referred to by a file descriptor. A file descriptor is a small integer returned by a call to open() or creat(). The other basic I/O calls take a file descriptor as a parameter to specify a file.

File descriptors are not global. The kernel has its own set of file descriptors, and each process (RTP) has its own set. Tasks within the kernel, or within a specific process share file descriptors. The only instance in which file descriptors may be shared across these boundaries, is when one process is a child of another process or of the kernel (processes created by kernel tasks share only the spawning kernel task’s standard I/O file descriptors 0, 1 and 2), and it does not explicitly close a file using the descriptors it inherits from its parent. For example:

- If task A and task B are running in process foo, and they each perform a write() on file descriptor 7, they will write to the same file (and device).
- If process bar is started independently of process foo (it is not foo’s child) and its tasks X and Y each perform a write() on file descriptor 7, they will be writing to a different file than tasks A and B in process foo.
If process `foobar` is started by process `foo` (it is `foo`'s child) and its tasks `M` and `N` each perform a `write()` on file descriptor 7, they will be writing to the same file as tasks `A` and `B` in process `foo`. However, this is only true as long as the tasks do not close the file. If they close it, and subsequently open file descriptor 7 they will operate on a different file.

When a file is opened, a file descriptor is allocated and returned. When the file is closed, the file descriptor is deallocated.

The number of file descriptors available in the kernel is defined with the `NUM_FILES` configuration macro. This specifies the size of the file descriptor table, which controls how many file descriptors can be simultaneously in use. The default number is 50, but it can be changed to suit the needs of the system.

To avoid running out of file descriptors, and encountering errors on file creation, applications should close any descriptors that are no longer in use.

The size of the file descriptor table for the kernel can also be changed at programmatically. The `rtpIoTableSizeGet()` routine reads the size of the file descriptor table and the `rtpIoTableSizeSet()` routine changes it. Note that these routines can be used with both the kernel and processes (the I/O system treats the kernel as a special kind of process).

The calling entity (kernel or process) can be specified with an `rtpIoTableSizeSet()` call by setting the first parameter to zero. The new size of the file descriptor table is set with the second parameter. Note that you can only increase the size.

### 6.3.2 Standard Input, Standard Output, and Standard Error

Three file descriptors have special meanings:

- 0 is used for standard input (`stdin`).
- 1 is used for standard output (`stdout`).
- 2 is used for standard error output (`stderr`).

All tasks read their standard input—like `getchar()`—from file descriptor 0. Similarly file descriptor 1 is used for standard output—like `printf()` . And file descriptor 2 is used for outputting error messages. Using these descriptors, you can manipulate the input and output for many tasks at once by redirecting the files associated with the descriptors.

These standard file descriptors are used to make tasks and modules independent of their actual I/O assignments. If a module sends its output to standard output (file descriptor 1), its output can then be redirected to any file or device, without altering the module.
VxWorks allows two levels of redirection. First, there is a global assignment of the three standard file descriptors. Second, individual tasks can override the global assignment of these file descriptors with assignments that apply only to that task.

### 6.3.3 Standard I/O Redirection

When VxWorks is initialized, the global standard I/O file descriptors, `stdin` (0), `stdout` (1) and `stderr` (2), are set to the system console device file descriptor by default, which is usually the serial tty device.

Each kernel task uses these global standard I/O file descriptors by default. Thus, any standard I/O operations like calls to `printf()` and `getchar()` use the global standard I/O file descriptors.

Standard I/O can be redirected, however, either at the individual task level, or globally for the kernel.

The standard I/O of a specific task can be changed with the `ioTaskStdSet()` routine. The parameters of this routine are the task ID of the task for which the change is to be made (0 is used for the calling task itself), the standard file descriptor to be redirected, and the file descriptor to direct it to. For example, a task can make the following call to write standard output to the `fileFd` file descriptor:

```
ioTaskStdSet(0, 1, fileFd);
```

The third argument (`fileFd` in this case) can be any valid open file descriptor. If it is a file system file, all the task’s subsequent standard output, such as that from `printf()`, is written to it.

To reset the task standard I/O back to global standard I/O, the third argument can be 0, 1, or 2.

The global standard I/O file descriptors can also be changed from the default setting, which affects all kernel tasks except that have had their task-specific standard I/O file descriptors changed from the global ones.

Global standard I/O file descriptors are changed by calling `ioGlobalStdSet()`. The parameters to this routine are the standard I/O file descriptor to be redirected, and the file descriptor to direct it to. For example:

```
ioGlobalStdSet(1, newFd);
```

This call sets the global standard output to `newFd`, which can be any valid open file descriptor. All tasks that do not have their individual task standard output redirected are affected by this redirection, and all their subsequent standard I/O output goes to `newFd`. 
The current settings of the global and any task's task standard I/O can be determined by calling `ioGlobalStdGet()` and `ioTaskStdGet()`. For more information, see the VxWorks API references for these routines.

**Issues with Standard I/O Redirection**

Be careful with file descriptors used for task standard I/O redirection to ensure that data corruption does not occur. Before any task’s standard I/O file descriptors are closed, they should be replaced with new file descriptors with a call to `ioTaskStdSet()`.

If a task’s standard I/O is set with `ioTaskStdSet()`, the file descriptor number is stored in that task’s memory. In some cases, this file descriptor may be closed, released by some other task or the one that opened it. Once it is released, it may be reused and opened to track a different file. Should the task holding it as a task standard I/O descriptor continue to use it for I/O, data corruption is unavoidable.

As an example, consider a task spawned from a telnet or rlogin session. The task inherits the network session task’s standard I/O file descriptors. If the session exits, the standard I/O file descriptors of the network session task are closed. However, the spawned task still holds those file descriptors as its task standard I/O continued with input and output to them. If the closed file descriptors are recycled and re-used by other `open()` call, however, data corruption results, perhaps with serious consequences for the system. To prevent this from happening, all spawned tasks need to have their standard I/O file descriptors redirected before the network session is terminated.

The following example illustrates this scenario, with redirection of a spawned task’s standard I/O to the global standard I/O from the shell before logout. The `taskspawn()` call is abbreviated to simplify presentation.

```plaintext
-> taskSpawn "someTask";
Task spawned: id = 0x52a010, name = t4
value = 5414928 = 0x52a010
-> ioTaskStdSet 0x52a010,0,0
value = 0 = 0x0
-> ioTaskStdSet 0x52a010,1,1
value = 0 = 0x0
-> ioTaskStdSet 0x52a010,2,2
value = 0 = 0x0
-> logout
```
The next example illustrates task standard I/O redirection to other file descriptors.

```
-> taskSpawn "someTask",......
Task spawned: id = 0x52a010, name = t4
value = 5414928 = 0x52a010
-> ioTaskStdSet 0x52a010,0,someOtherFx
value = 0 = 0x0
-> ioTaskStdSet 0x52a010,1,someOtherFdy
value = 0 = 0x0
-> ioTaskStdSet 0x52a010,2,someOtherFdz
value = 0 = 0x0
-> logout
```

### 6.3.4 Open and Close

Before I/O can be performed on a device, a file descriptor must be opened to the device by invoking the `open()` routine—or `creat()`, as discussed in the next section. The arguments to `open()` are the filename, the type of access, and the mode (file permissions):

```
fd = open("name", flags, mode);
```

For `open()` calls made in the kernel, the mode parameter can be set to zero if file permissions do not need to be specified.

The file-access options that can be used with the `flags` parameter to `open()` are listed in Table 6-2.

<table>
<thead>
<tr>
<th>Flag</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>O_RDONLY</td>
<td>Open for reading only.</td>
</tr>
<tr>
<td>O_WRONLY</td>
<td>Open for writing only.</td>
</tr>
<tr>
<td>O_RDWR</td>
<td>Open for reading and writing.</td>
</tr>
<tr>
<td>O_CREAT</td>
<td>Create a file if it does not already exist.</td>
</tr>
<tr>
<td>O_EXCL</td>
<td>Error on open if the file exists and O_CREAT is also set.</td>
</tr>
<tr>
<td>O_SYNC</td>
<td>Write on the file descriptor complete as defined by synchronized I/O file integrity completion.</td>
</tr>
</tbody>
</table>
Note the following special cases with regard to use of the file access and mode (file permissions) parameters to `open()`:

- In general, you can open only preexisting devices and files with `open()`. However, with NFS network, dosFs, and HRFS devices, you can also create files with `open()` by OR'ing `O_CREAT` with one of the other access flags.
- HRFS directories can be opened with the `open()` routine, but only using the `O_RDONLY` flag.
- With both dosFs and NFS devices, you can use the `O_CREAT` flag to create a subdirectory by setting `mode` to `FSTAT_DIR`. Other uses of the mode parameter with dosFs devices are ignored.
With an HRFS device you cannot use the `O_CREAT` flag and the `FSTAT_DIR` mode option to create a subdirectory. HRFS will ignore the mode option and simply create a regular file.

- The netDrv default file system does not support the `F_STAT_DIR` mode option or the `O_CREAT` flag.
- For NFS devices, the third parameter to `open()` is normally used to specify the mode of the file. For example:
  ```c
  myFd = open("fooFile", O_CREAT | O_RDWR, 0644);
  ```
- While HRFS supports setting the permission mode for a file, it is not used by the VxWorks operating system.
- Files can be opened with the `O_SYNC` flag, indicating that each write should be immediately written to the backing media. This flag is currently supported by the dosFs file system, and includes synchronizing the FAT and the directory entries.
- The `O_SYNC` flag has no effect with HRFS because file system is always synchronous. HRFS updates files as though the `O_SYNC` flag were set.

**NOTE:** Drivers or file systems may or may not honor the flag values or the mode values. A file opened with `O_RDONLY` mode may in fact be writable if the driver allows it. Consult the driver or file system information for specifics.

Refer to the VxWorks file system API references for more information about the features that each file system supports.

The `open()` routine, if successful, returns a file descriptor. This file descriptor is then used in subsequent I/O calls to specify that file. The file descriptor is an identifier that is not task specific; that is, it is shared by all tasks within the memory space. Within a given process or the kernel, therefore, one task can open a file and any other task can then use the file descriptor. The file descriptor remains valid until `close()` is invoked with that file descriptor, as follows:

```c
  close (fd);
```

At that point, I/O to the file is flushed (completely written out) and the file descriptor can no longer be used by any task within the process (or kernel). However, the same file descriptor number can again be assigned by the I/O system in any subsequent `open()`.

Since the kernel only terminates when the system shuts down, there is no situation analogous to file descriptors being closed automatically when a process terminates. File descriptors in the kernel can only be closed by direct command.
6.3.5 Create and Remove

File-oriented devices must be able to create and remove files as well as open existing files.

The `creat()` routine directs a file-oriented device to make a new file on the device and return a file descriptor for it. The arguments to `creat()` are similar to those of `open()` except that the filename specifies the name of the new file rather than an existing one; the `creat()` routine returns a file descriptor identifying the new file.

```c
fd = creat("name", flag);
```

Note that with the HRFS file system the `creat()` routine is POSIX compliant, and the second parameter is used to specify file permissions; the file is opened in `O_RDWR` mode.

With dosFs, however, the `creat()` routine is not POSIX compliant and the second parameter is used for open mode flags.

The `remove()` routine deletes a named file on a file-system device:

```c
remove("name");
```

Files should be closed before they are removed.

With non-file-system devices, the `creat()` routine performs the same function as `open()`. The `remove()` routine, however, has no effect.

6.3.6 Read and Write

After a file descriptor is obtained by invoking `open()` or `creat()`, tasks can read bytes from a file with `read()` and write bytes to a file with `write()`. The arguments to `read()` are the file descriptor, the address of the buffer to receive input, and the maximum number of bytes to read:

```c
nBytes = read(fd, &buffer, maxBytes);
```

The `read()` routine waits for input to be available from the specified file, and returns the number of bytes actually read. For file-system devices, if the number of bytes read is less than the number requested, a subsequent `read()` returns 0 (zero), indicating end-of-file. For non-file-system devices, the number of bytes read can be less than the number requested even if more bytes are available; a subsequent `read()` may or may not return 0. In the case of serial devices and TCP sockets, repeated calls to `read()` are sometimes necessary to read a specific number of bytes. (See the reference entry for `fioRead()` in `fioLib`). A return value of `ERROR` (-1) indicates an unsuccessful read.
The arguments to \texttt{write()} are the file descriptor, the address of the buffer that contains the data to be output, and the number of bytes to be written:

\[
\text{actualBytes} = \text{write}(\text{fd}, \&\text{buffer}, \text{nBytes});
\]

The \texttt{write()} routine ensures that all specified data is at least queued for output before returning to the caller, though the data may not yet have been written to the device (this is driver dependent). The \texttt{write()} routine returns the number of bytes written; if the number returned is not equal to the number requested, an error has occurred.

### 6.3.7 File Truncation

It is sometimes convenient to discard part of the data in a file. After a file is open for writing, you can use the \texttt{ftruncate()} routine to truncate a file to a specified size. Its arguments are a file descriptor and the desired length of the file in bytes:

\[
\text{status} = \text{ftruncate}(\text{fd}, \text{length});
\]

If it succeeds in truncating the file, \texttt{ftruncate()} returns \texttt{OK}.

If the file descriptor refers to a device that cannot be truncated, \texttt{ftruncate()} returns \texttt{ERROR}, and sets \texttt{errno} to \texttt{EINVAL}.

If the size specified is larger than the actual size of the file, the result depends on the file system. For both dosFs and HRFS, the size of the file is extended to the specified size; however, for other file systems, \texttt{ftruncate()} returns \texttt{ERROR}, and sets \texttt{errno} to \texttt{EINVAL} (just as if the file descriptor referred to a device that cannot be truncated).

The \texttt{ftruncate()} routine is part of the POSIX 1003.1b standard. It is fully supported as such by the HRFS. The dosFs implementation is, however, only partially compliant: creation and modification times are not changed.

Also note that with HRFS the \texttt{seek} position is not modified by truncation, but with dosFs the \texttt{seek} position is set to the end of the file.

### 6.3.8 I/O Control

The \texttt{ioctl()} routine provides an open-ended mechanism for performing I/O functions that are not performed by the other basic I/O calls. Examples include determining how many bytes are currently available for input, setting device-specific options, obtaining information about a file system, and positioning random-access files to specific byte positions. The arguments to the \texttt{ioctl()} routine
are the file descriptor, a code that identifies the control function requested, and an optional function-dependent argument:

\[ \text{result} = \text{ioctl}(\text{fd, function, arg}); \]

For example, the following call uses the FIOBAUDRATE function to set the baud rate of a tty device to 9600:

\[ \text{status} = \text{ioctl(}\text{fd, FIOBAUDRATE, 9600}); \]

The discussion of specific devices in 6.7 Devices in VxWorks, p.346 summarizes the ioctl() functions available for each device. The ioctl() control codes are defined in ioLib.h. For more information, see the reference entries for specific device drivers or file systems.

### 6.3.9 Pending on Multiple File Descriptors: The Select Facility

The VxWorks select facility provides a UNIX- and Windows-compatible method for pending on multiple file descriptors. The library selectLib provides both task-level support, allowing tasks to wait for multiple devices to become active, and device driver support, giving drivers the ability to detect tasks that are pended while waiting for I/O on the device. To use this facility, the header file selectLib.h must be included in your application code.

Task-level support not only gives tasks the ability to simultaneously wait for I/O on multiple devices, but it also allows tasks to specify the maximum time to wait for I/O to become available. An example of using the select facility to pend on multiple file descriptors is a client-server model, in which the server is servicing both local and remote clients. The server task uses a pipe to communicate with local clients and a socket to communicate with remote clients. The server task must respond to clients as quickly as possible. If the server blocks waiting for a request on only one of the communication streams, it cannot service requests that come in on the other stream until it gets a request on the first stream. For example, if the server blocks waiting for a request to arrive in the socket, it cannot service requests that arrive in the pipe until a request arrives in the socket to unblock it. This can delay local tasks waiting to get their requests serviced. The select facility solves this problem by giving the server task the ability to monitor both the socket and the pipe and service requests as they come in, regardless of the communication stream used.

Tasks can block until data becomes available or the device is ready for writing. The select() routine returns when one or more file descriptors are ready or a timeout has occurred. Using the select() routine, a task specifies the file descriptors on which to wait for activity. Bit fields are used in the select() call to specify the read
and write file descriptors of interest. When \texttt{select()} returns, the bit fields are modified to reflect the file descriptors that have become available. The macros for building and manipulating these bit fields are listed in Table 6-3.

Table 6-3 \textbf{Select Macros}

<table>
<thead>
<tr>
<th>Macro</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>FD_ZERO</td>
<td>Zeroes all bits.</td>
</tr>
<tr>
<td>FD_SET</td>
<td>Sets the bit corresponding to a specified file descriptor.</td>
</tr>
<tr>
<td>FD_CLR</td>
<td>Clears a specified bit.</td>
</tr>
<tr>
<td>FD_ISSET</td>
<td>Returns non-zero if the specified bit is set; otherwise returns 0.</td>
</tr>
</tbody>
</table>

Applications can use \texttt{select()} with any character I/O devices that provide support for this facility (for example, pipes, serial devices, and sockets).

For information on writing a device driver that supports \texttt{select()}, see \textit{Implementing \texttt{select}()}, p.392.

Example 6-1 \textbf{The Select Facility}

```c
/* selServer.c - select example
   * In this example, a server task uses two pipes: one for normal-priority requests, the other for high-priority requests. The server opens both pipes and blocks while waiting for data to be available in at least one of the pipes.
   */

#include <vxWorks.h>
#include <selectLib.h>
#include <fcntl.h>

#define MAX_FDS 2
#define MAX_DATA 1024
#define PIPEHI "/pipe/highPriority"
#define PIPENORM "/pipe/normalPriority"

/***************************************************************************/
** selServer - reads data as it becomes available from two different pipes *
** * Opens two pipe fds, reading from whichever becomes available. The server code assumes the pipes have been created from either another task or the shell. To test this code from the shell do the following: *
**   * -> ld < selServer.o *
**   * -> pipeDevCreate ("/pipe/highPriority", 5, 1024) *
**   * -> pipeDevCreate ("/pipe/normalPriority", 5, 1024) */

/***************************************************************************/
At this point you should see selServer’s state as pended. You can now write to either pipe to make the selServer display your message.

```c
* -> write fdNorm, "Howdy", 6
* -> write fdHi, "Urgent", 7
*/
```

```
STATUS selServer (void)
{
    struct fd_set readFds; /* bit mask of fds to read from */
    int     fds[MAX_FDS]; /* array of fds on which to pend */
    int     width; /* number of fds on which to pend */
    int     i; /* index for fd array */
    char    buffer[MAX_DATA]; /* buffer for data that is read */

    /* open file descriptors */
    if ((fds[0] = open (PIPEHI, O_RDONLY, 0)) == ERROR)
    {
        close (fds[0]);
        return (ERROR);
    }
    if ((fds[1] = open (PIPENORM, O_RDONLY, 0)) == ERROR)
    {
        close (fds[0]);
        close (fds[1]);
        return (ERROR);
    }

    /* loop forever reading data and servicing clients */
    FOREVER
    {
        /* clear bits in read bit mask */
        FD_ZERO (&readFds);

        /* initialize bit mask */
        FD_SET (fds[0], &readFds);
        FD_SET (fds[1], &readFds);
        width = (fds[0] > fds[1]) ? fds[0] : fds[1];
        width++;
    }
}
/* pend, waiting for one or more fds to become ready */

if (select (width, &readFds, NULL, NULL, NULL) == ERROR)
{
    close (fds[0]);
    close (fds[1]);
    return (ERROR);
}

/* step through array and read from fds that are ready */

for (i=0; i< MAX_FDS; i++)
{
    /* check if this fd has data to read */
    if (FD_ISSET (fds[i], &readFds))
    {
        /* typically read from fd now that it is ready */
        read (fds[i], buffer, MAX_DATA);
        /* normally service request, for this example print it */
        printf ("SELSERVER Reading from %s: %s\n",
            (i == 0) ? PIPEHI : PIPENORM, buffer);
    }
}

6.3.10 POSIX File System Routines

The POSIX fsPxLib library provides I/O and file system routines for various file manipulations. These routines are described in Table 6-4.

<table>
<thead>
<tr>
<th>Routine</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>unlink()</td>
<td>Unlink a file.</td>
</tr>
<tr>
<td>link()</td>
<td>Link a file.</td>
</tr>
<tr>
<td>fsync()</td>
<td>Synchronize a file.</td>
</tr>
<tr>
<td>fdatasync()</td>
<td>Synchronize a file data.</td>
</tr>
<tr>
<td>rename()</td>
<td>Change the name of a file.</td>
</tr>
<tr>
<td>fpathconf()</td>
<td>Determine the current value of a configurable limit.</td>
</tr>
<tr>
<td>pathconf()</td>
<td>Determine the current value of a configurable limit.</td>
</tr>
<tr>
<td>access()</td>
<td>Determine accessibility of a file.</td>
</tr>
</tbody>
</table>
6.4 Buffered I/O: stdio

The VxWorks I/O library provides a buffered I/O package that is compatible with the UNIX and Windows stdio package, and provides full ANSI C support. Configure VxWorks with the ANSI Standard component bundle to provide buffered I/O support.

**NOTE:** The implementation of printf(), sprintf(), and sscanf(), traditionally considered part of the stdio package, is part of a different package in VxWorks. These routines are discussed in 6.5 Other Formatted I/O, p.337.

6.4.1 Using stdio

Although the VxWorks I/O system is efficient, some overhead is associated with each low-level call. First, the I/O system must dispatch from the device-independent user call (read(), write(), and so on) to the driver-specific routine for that function. Second, most drivers invoke a mutual exclusion or queuing mechanism to prevent simultaneous requests by multiple users from interfering with each other.

This overhead is quite small because the VxWorks primitives are fast. However, an application processing a single character at a time from a file incurs that overhead for each character if it reads each character with a separate read() call:

\[ n = \text{read}(fd, \text{uchar}, 1); \]

To make this type of I/O more efficient and flexible, the stdio package implements a buffering scheme in which data is read and written in large chunks and buffered privately. This buffering is transparent to the application; it is handled.

---

Table 6-4  File System Routines

<table>
<thead>
<tr>
<th>Routine</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>chmod()</td>
<td>Change the permission mode of a file.</td>
</tr>
</tbody>
</table>

For more information, see the API reference for fsPxLib.
automatically by the stdio routines and macros. To access a file with stdio, a file is opened with fopen() instead of open() (many stdio calls begin with the letter f):

\[ fp = \text{fopen} \left( \text{"/usr/foo"}, \text{"r"} \right); \]

The returned value, a file pointer is a handle for the opened file and its associated buffers and pointers. A file pointer is actually a pointer to the associated data structure of type FILE (that is, it is declared as FILE *). By contrast, the low-level I/O routines identify a file with a file descriptor, which is a small integer. In fact, the FILE structure pointed to by the file pointer contains the underlying file descriptor of the open file.

A file descriptor that is already open can be associated belatedly with a FILE buffer by calling fdopen():

\[ fp = \text{fdopen} \left( \text{fd}, \text{"r"} \right); \]

After a file is opened with fopen(), data can be read with fread(), or a character at a time with getc(), and data can be written with fwrite(), or a character at a time with putc().

The routines and macros to get data into or out of a file are extremely efficient. They access the buffer with direct pointers that are incremented as data is read or written by the user. They pause to call the low-level read or write routines only when a read buffer is empty or a write buffer is full.

**WARNING:** The stdio buffers and pointers are private to a particular task. They are not interlocked with semaphores or any other mutual exclusion mechanism, because this defeats the point of an efficient private buffering scheme. Therefore, multiple tasks must not perform I/O to the same stdio FILE pointer at the same time.

The FILE buffer is deallocated when fclose() is called.

### 6.4.2 Standard Input, Standard Output, and Standard Error

As discussed in 6.3 Basic I/O, p.322, there are three special file descriptors (0, 1, and 2) reserved for standard input, standard output, and standard error. Three corresponding stdio FILE buffers are automatically created when a task uses the standard file descriptors, stdin, stdout, and stderr, to do buffered I/O to the standard file descriptors. Each task using the standard I/O file descriptors has its own stdio FILE buffers. The FILE buffers are deallocated when the task exits.
6.5 Other Formatted I/O

This section describes additional formatting routines and facilities.

6.5.1 Special Cases: printf(), sprintf(), and sscanf()

The routines printf(), sprintf(), and sscanf() are generally considered to be part of the standard stdio package. However, the VxWorks implementation of these routines, while functionally the same, does not use the stdio package. Instead, it uses a self-contained, formatted, non-buffered interface to the I/O system in the library fioLib.

Note that these routines provide the functionality specified by ANSI; however, printf() is not buffered.

Because these routines are implemented in this way, the full stdio package, which is optional, can be omitted from a VxWorks configuration without sacrificing their availability. Applications requiring printf-style output that is buffered can still accomplish this by calling fprintf() explicitly to stdout.

While sscanf() is implemented in fioLib and can be used even if stdio is omitted, the same is not true of scanf(), which is implemented in the usual way in stdio.

6.5.2 Additional Routines: printErr() and fdprintf()

Additional routines in fioLib provide formatted but unbuffered output. The routine printErr() is analogous to printf() but outputs formatted strings to the standard error file descriptor (2). The routine fdprintf() outputs formatted strings to a specified file descriptor.

6.5.3 Message Logging

Another higher-level I/O facility is provided by the library logLib, which allows formatted messages to be logged without having to do I/O in the current task’s context, or when there is no task context. The message format and parameters are sent on a message queue to a logging task, which then formats and outputs the message. This is useful when messages must be logged from interrupt level, or when it is desirable not to delay the current task for I/O or use the current task’s stack for message formatting (which can take up significant stack space). The
message is displayed on the console unless otherwise redirected at system startup using \texttt{logInit()} or dynamically using \texttt{logFdSet()}.

6.6 Asynchronous Input/Output

Asynchronous Input/Output (AIO) is the ability to perform input and output operations concurrently with ordinary internal processing. AIO enables you to de-couple I/O operations from the activities of a particular task when these are logically independent.

The benefit of AIO is greater processing efficiency: it permits I/O operations to take place whenever resources are available, rather than making them await arbitrary events such as the completion of independent operations. AIO eliminates some of the unnecessary blocking of tasks that is caused by ordinary synchronous I/O; this decreases contention for resources between input/output and internal processing, and expedites throughput.

The VxWorks AIO implementation meets the specification in the POSIX 1003.1b standard. Include AIO in your VxWorks configuration with the \texttt{INCLUDE_POSIX_AIO} and \texttt{INCLUDE_POSIX_AIO_SYSDRV} components. The second configuration constant enables the auxiliary AIO system driver, required for asynchronous I/O on all current VxWorks devices.

6.6.1 The POSIX AIO Routines

The VxWorks library \texttt{aioPxLib} provides POSIX AIO routines. To access a file asynchronously, open it with the \texttt{open()} routine, like any other file. Thereafter, use the file descriptor returned by \texttt{open()} in calls to the AIO routines. The POSIX AIO routines (and two associated non-POSIX routines) are listed in Table 6-5.

The default VxWorks initialization code calls \texttt{aioPxLibInit()} automatically when the POSIX AIO component is included in VxWorks with \texttt{INCLUDE_POSIX_AIO}.

The \texttt{aioPxLibInit()} routine takes one parameter, the maximum number of \texttt{llo_listio()} calls that can be outstanding at one time. By default this parameter is \texttt{MAX_LIO_CALLS}. When the parameter is 0 (the default), the value is taken from \texttt{AIO_CLUST_MAX} (defined in \texttt{installDir/vxworks-6.x/target/h/private/aioPxLibP.h}).
The AIO system driver, `aioSysDrv`, is initialized by default with the routine `aioSysInit()` when both `INCLUDE_POSIX_AIO` and `INCLUDE_POSIX_AIO_SYSDRV` are included in VxWorks. The purpose of `aioSysDrv` is to provide request queues independent of any particular device driver, so that you can use any VxWorks device driver with AIO.

Table 6-5  Asynchronous Input/Output Routines

<table>
<thead>
<tr>
<th>Function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>aioPxLibInit()</td>
<td>Initializes the AIO library (non-POSIX).</td>
</tr>
<tr>
<td>aioShow()</td>
<td>Displays the outstanding AIO requests (non-POSIX).</td>
</tr>
<tr>
<td>aio_read()</td>
<td>Initiates an asynchronous read operation.</td>
</tr>
<tr>
<td>aio_write()</td>
<td>Initiates an asynchronous write operation.</td>
</tr>
<tr>
<td>aio_listio()</td>
<td>Initiates a list of up to LIO_MAX asynchronous I/O requests.</td>
</tr>
<tr>
<td>aio_error()</td>
<td>Retrieves the error status of an AIO operation.</td>
</tr>
<tr>
<td>aio_return()</td>
<td>Retrieves the return status of a completed AIO operation.</td>
</tr>
<tr>
<td>aio_cancel()</td>
<td>Cancels a previously submitted AIO operation.</td>
</tr>
<tr>
<td>aio_suspend()</td>
<td>Waits until an AIO operation is done, interrupted, or timed out.</td>
</tr>
<tr>
<td>aio_fsync()</td>
<td>Asynchronously forces file synchronization.</td>
</tr>
</tbody>
</table>

a. This function is not built into the host shell. To use it from the host shell, VxWorks must be configured with the `INCLUDE_POSIX_AIO_SHOW` component. When you invoke the function, its output is sent to the standard output device.

The routine `aioSysInit()` takes three parameters: the number of AIO system tasks to spawn, and the priority and stack size for these system tasks. The number of AIO system tasks spawned equals the number of AIO requests that can be handled in parallel. The default initialization call uses three constants: `MAX_AIO_SYS_TASKS`, `AIO_TASK_PRIORITY`, and `AIO_TASK_STACK_SIZE`.

When any of the parameters passed to `aioSysInit()` is 0, the corresponding value is taken from `AIO_IO_TASKS_DFLT`, `AIO_IO_PRIO_DFLT`, and `AIO_IO_STACK_DFLT` (all defined in `installDir/vxworks-6.x/target/h/aioSysDrv.h`).
Table 6-6 lists the names of the constants, and shows the constants used within initialization routines when the parameters are left at their default values of 0, and where these constants are defined.

<table>
<thead>
<tr>
<th>Init Routine</th>
<th>Configuration Parameter</th>
<th>Def. Value</th>
<th>Header File Constant</th>
<th>Def. Value</th>
<th>Header File</th>
</tr>
</thead>
<tbody>
<tr>
<td>aioPxLibInit()</td>
<td>MAX_LIO_CALLS</td>
<td>0</td>
<td>AIO_CLUST_MAX</td>
<td>100</td>
<td>private/aioPxLibP.h</td>
</tr>
<tr>
<td>aioSysInit()</td>
<td>MAX_AIO_SYS_TASKS</td>
<td>0</td>
<td>AIO_IO_TASKS_DFLT</td>
<td>2</td>
<td>aioSysDrv.h</td>
</tr>
<tr>
<td></td>
<td>AIO_TASK_PRIORITY</td>
<td>0</td>
<td>AIO_IO_PRIO_DFLT</td>
<td>50</td>
<td>aioSysDrv.h</td>
</tr>
<tr>
<td></td>
<td>AIO_TASK_STACK_SIZE</td>
<td>0</td>
<td>AIO_IO_STACK_DFLT</td>
<td>0x7000</td>
<td>aioSysDrv.h</td>
</tr>
</tbody>
</table>

6.6.2 AIO Control Block

Each of the AIO calls takes an AIO control block (aiocb) as an argument to describe the AIO operation. The calling routine must allocate space for the control block, which is associated with a single AIO operation. No two concurrent AIO operations can use the same control block; an attempt to do so yields undefined results.

The aiocb and the data buffers it references are used by the system while performing the associated request. Therefore, after you request an AIO operation, you must not modify the corresponding aiocb before calling aio_return(); this function frees the aiocb for modification or reuse.

The aiocb structure is defined in aio.h. It contains the following fields:

- **aio_fildes**: The file descriptor for I/O.
- **aio_offset**: The offset from the beginning of the file.
- **aio_buf**: The address of the buffer from/to which AIO is requested.
- **aio_nbytes**: The number of bytes to read or write.
- **aio_reqprio**: The priority reduction for this AIO request.
6.6 Asynchronous Input/Output

**aioSigevent**

The signal to return on completion of an operation (optional).

**aioLioOpcode**

An operation to be performed by a liolistio() call.

**aioSysP**

The address of VxWorks-specific data (non-POSIX).

For full definitions and important additional information, see the reference entry for **aioPxLib**.

⚠️ **CAUTION:** If a routine allocates stack space for the **aiocb**, that routine must call **aio_return()** to free the **aiocb** before returning.

### 6.6.3 Using AIO

The routines **aio_read()**, **aio_write()**, or **liolistio()** initiate AIO operations. The last of these, **liolistio()**, allows you to submit a number of asynchronous requests (read and/or write) at one time. In general, the actual I/O (reads and writes) initiated by these routines does not happen immediately after the AIO request. For this reason, their return values do not reflect the outcome of the actual I/O operation, but only whether a request is successful—that is, whether the AIO routine is able to put the operation on a queue for eventual execution.

After the I/O operations themselves execute, they also generate return values that reflect the success or failure of the I/O. There are two routines that you can use to get information about the success or failure of the I/O operation: **aio_error()** and **aio_return()**. You can use **aio_error()** to get the status of an AIO operation (success, failure, or in progress), and **aio_return()** to obtain the return values from the individual I/O operations. Until an AIO operation completes, its error status is **EINPROGRESS**. To cancel an AIO operation, call **aio_cancel()**. To force all I/O operations to the synchronized I/O completion state, use **aio_fsync()**.

### AIO with Periodic Checks for Completion

The following code uses a pipe for the asynchronous I/O operations. The example creates the pipe, submits an AIO read request, verifies that the read request is still in progress, and submits an AIO write request. Under normal circumstances, a synchronous read to an empty pipe blocks and the task does not execute the write, but in the case of AIO, we initiate the read request and continue. After the write...
request is submitted, the example task loops, checking the status of the AIO requests periodically until both the read and write complete. Because the AIO control blocks are on the stack, we must call `aio_return()` before returning from `aioExample()`.

Example 6-2  Asynchronous I/O

```c
/* aioEx.c - example code for using asynchronous I/O */

/* includes */
#include <vxWorks.h>
#include <aio.h>
#include <errno.h>

/* defines */
#define BUFFER_SIZE 200

/* This example shows the basic functions of the AIO library. */

STATUS aioExample (void)
{
    int fd;
    static char exFile [] = "/pipe/1stPipe";
    struct aiocb aiocb_read; /* read aiocb */
    struct aiocb aiocb_write; /* write aiocb */
    static char * test_string = "testing 1 2 3";
    char buffer [BUFFER_SIZE]; /* buffer for read aiocb */

    pipeDevCreate (exFile, 50, 100);

    if ((fd = open (exFile, O_CREAT | O_TRUNC | O_RDWR, 0666)) == ERROR)
    {
        printf ("aioExample: cannot open %s %s errno 0x%x
", exFile, errno);
        return (ERROR);
    }

    printf ("aioExample: Example file = %s	File descriptor = %d\n", exFile, fd);

    /* initialize read and write aiocbs */
    bzero ((char *) &aiocb_read, sizeof (struct aiocb));
    bzero ((char *) buffer, sizeof (buffer));
    aiocb_read.aio_fildes = fd;
    aiocb_read.aio_buf = buffer;
    aiocb_read.aio_nbytes = BUFFER_SIZE;
    aiocb_read.aio_reqprio = 0;

    /* initialize read and write aiocbs */
    bzero ((char *) &aiocb_write, sizeof (struct aiocb));
    bzero ((char *) buffer, sizeof (buffer));
    aiocb_write.aio_fildes = fd;
    aiocb_write.aio_buf = buffer;
    aiocb_write.aio_nbytes = BUFFER_SIZE;
    aiocb_write.aio_reqprio = 0;

    /* start a read and write */
    if (aio_read (fd, exFile, 0, ":\n", (struct aiocb *) &aiocb_read) == ERROR)
        return (ERROR);
    if (aio_write (fd, exFile, test_string, BUFFER_SIZE, (struct aiocb *) &aiocb_write) == ERROR)
        return (ERROR);

    printf ("aioExample: Read and write completed\n");

    status = aiodone (fd, (struct aiocb *) &aiocb_read);
    if (status == ERROR)
        return (ERROR);

    status = aiodone (fd, (struct aiocb *) &aiocb_write);
    if (status == ERROR)
        return (ERROR);

    printf ("aioExample: Read and write completed\n");

    printf ("aioExample: Example complete\n");

    return (OK);
}
```

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bzero ((char *) &aiocb_write, sizeof (struct aiocb));
    aiocb_write.aio_fildes = fd;
    aiocb_write.aio_buf = test_string;
    aiocb_write.aio_nbytes = strlen (test_string);
    aiocb_write.aio_reqprio = 0;

/* initiate the read */
if (aio_read (&aiocb_read) == -1)
    printf ("aioExample: aio_read failed\n");

/* verify that it is in progress */
if (aio_error (&aiocb_read) == EINPROGRESS)
    printf ("aioExample: read is still in progress\n");

/* wait till both read and write are complete */
while ((aio_error (&aiocb_read) == EINPROGRESS) ||
    (aio_error (&aiocb_write) == EINPROGRESS))
    taskDelay (1);

/* print out what was read */
printf ("aioExample: message = %s\n", buffer);

/* clean up */
if (aio_return (&aiocb_read) == -1)
    printf ("aioExample: aio_return for aiocb_read failed\n");
if (aio_return (&aiocb_write) == -1)
    printf ("aioExample: aio_return for aiocb_write failed\n");

close (fd);
return (OK);

Alternatives for Testing AIO Completion

A task can determine whether an AIO request is complete in any of the following ways:

- Check the result of aio_error() periodically, as in the previous example, until
  the status of an AIO request is no longer EINPROGRESS.
- Use aio_suspend() to suspend the task until the AIO request is complete.
- Use signals to be informed when the AIO request is complete.

The following example is similar to the preceding aioExample(), except that it
uses signals for notification that the write operation has finished. If you test this
from the shell, spawn the routine to run at a lower priority than the AIO system tasks to assure that the test routine does not block completion of the AIO request.

Example 6-3  Asynchronous I/O with Signals

/* aioExSig.c - example code for using signals with asynchronous I/O */

/* includes */
#include <vxWorks.h>
#include <aio.h>
#include <errno.h>

/* defines */
#define BUFFER_SIZE  200
#define LIST_SIZE  1
#define EXAMPLE_SIG_NO  25 /* signal number */

/* forward declarations */
void mySigHandler (int sig, struct siginfo * info, void * pContext);

/************************************************************************
* aioExampleSig - use AIO library.
*
* This example shows the basic functions of the AIO library.
* Note if this is run from the shell it must be spawned. Use:
* -> sp aioExampleSig
*
* RETURNS: OK if successful, otherwise ERROR.
*/

STATUS aioExampleSig (void)
{
    int    fd;
    static char    exFile []; = "/pipe/1stPipe";
    struct aiocb    aiocb_read;  /* read aiocb */
    static struct aiocb    aiocb_write;  /* write aiocb */
    struct sigaction    action;  /* signal info */
    static char     * test_string = "testing 1 2 3";
    char        buffer [BUFFER_SIZE];  /* aiocb read buffer */

    pipeDevCreate (exFile, 50, 100);

    if ((fd = open (exFile, O_CREAT | O_TRUNC| O_RDWR, 0666)) == ERROR)
    {
        printf ("aioExample: cannot open %s errno 0x%x\n", exFile, errno);
        return (ERROR);
    }

    printf ("aioExampleSig: Example file = %s\tFile descriptor = %d\n", exFile, fd);

    /* set up signal handler for EXAMPLE_SIG_NO */

    mySigHandler (EXAMPLE_SIG_NO, 0, 0);
action.sa_sigaction = mySigHandler;
action.sa_flags = SA_SIGINFO;
sigemptyset (&action.sa_mask);
sigaction (EXAMPLE_SIG_NO, &action, NULL);

/* initialize read and write aiocbs */
bzero ((char *) &aiocb_read, sizeof (struct aiocb));
bzero ((char *) &aiocb_write, sizeof (struct aiocb));
aiocb_read.aio_fildes = fd;
aiocb_read.aio_buf = buffer;
aiocb_read.aio_nbytes = BUFFER_SIZE;
aiocb_read.aio_reqprio = 0;

bzero ((char *) &aiocb_write, sizeof (struct aiocb));
aiocb_write.aio_fildes = fd;
aiocb_write.aio_buf = test_string;
aiocb_write.aio_nbytes = strlen (test_string);
aiocb_write.aio_reqprio = 0;

/* set up signal info */
aiocb_write.aio_sigevent.sigev_signo = EXAMPLE_SIG_NO;
aiocb_write.aio_sigevent.sigev_notify = SIGEV_SIGNAL;
aiocb_write.aio_sigevent.sigev_value.sival_ptr =
(void *) &aiocb_write;

/* initiate the read */
if (aio_read (&aiocb_read) == -1)
    printf ("aioExampleSig: aio_read failed\n");

/* verify that it is in progress */
if (aio_error (&aiocb_read) == EINPROGRESS)
    printf ("aioExampleSig: read is still in progress\n");

/* write to pipe - the read should be able to complete */
printf ("aioExampleSig: getting ready to initiate the write\n");
if (aio_write (&aiocb_write) == -1)
    printf ("aioExampleSig: aio_write failed\n");

/* clean up */
if (aio_return (&aiocb_read) == -1)
    printf ("aioExampleSig: aio_return for aiocb_read failed\n");
else
    printf ("aioExampleSig: aio read message = %s\n",
        aiocb_read.aio_buf);

close (fd);
return (OK);
void mySigHandler
{
  int     sig,
  struct siginfo * info,
  void *  pContext
}

{
  /* print out what was read */
  printf ("mySigHandler: Got signal for aio write\n");

  /* write is complete so let’s do cleanup for it here */
  if (aio_return (info->si_value.sival_ptr) == -1)
  {
    printf ("mySigHandler: aio_return for aioch_write failed\n");
    printErrno (0);
  }
}

6.7 Devices in VxWorks

The VxWorks I/O system is flexible, allowing specific device drivers to handle the seven basic I/O functions. All VxWorks device drivers follow the basic conventions outlined previously, but differ in specifics; this section describes those specifics.

<table>
<thead>
<tr>
<th>Device</th>
<th>Driver Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>tty</td>
<td>Terminal device</td>
</tr>
<tr>
<td>pty</td>
<td>Pseudo-terminal device</td>
</tr>
<tr>
<td>pipe</td>
<td>Pipe device</td>
</tr>
<tr>
<td>mem</td>
<td>Pseudo memory device</td>
</tr>
<tr>
<td>nfs</td>
<td>NFS client device</td>
</tr>
<tr>
<td>net</td>
<td>Network device for remote file access</td>
</tr>
<tr>
<td>ram</td>
<td>RAM device for creating a RAM disk</td>
</tr>
<tr>
<td>scsi</td>
<td>SCSI interface</td>
</tr>
</tbody>
</table>
6 I/O System
6.7 Devices in VxWorks

6.7.1 Serial I/O Devices: Terminal and Pseudo-Terminal Devices

VxWorks provides terminal and pseudo-terminal devices (tty and pty). The tty device is for actual terminals; the pty device is for processes that simulate terminals. These pseudo terminals are useful in applications such as remote login facilities.

VxWorks serial I/O devices are buffered serial byte streams. Each device has a ring buffer (circular buffer) for both input and output. Reading from a tty device extracts bytes from the input ring. Writing to a tty device adds bytes to the output ring. The size of each ring buffer is specified when the device is created during system initialization.

**NOTE:** For the remainder of this section, the term tty is used to indicate both tty and pty devices.

### tty Options

The tty devices have a full range of options that affect the behavior of the device. These options are selected by setting bits in the device option word using the ioctl() routine with the FIOSETOPTIONS function. For example, to set all the tty options except OPT_MON_TRAP:

```c
status = ioctl(fd, FIOSETOPTIONS, OPT_TERMINAL & ~OPT_MON_TRAP);
```

For more information, see I/O Control Functions, p.350.

Table 6-8 is a summary of the available options. The listed names are defined in the header file ioLib.h. For more detailed information, see the API reference entry for tyLib.
Raw Mode and Line Mode

A *tty* device operates in one of two modes: *raw mode* (unbuffered) or *line mode*. Raw mode is the default. Line mode is selected by the OPT_LINE bit of the device option word (see *tty Options*, p.347).

In *raw mode*, each input character is available to readers as soon as it is input from the device. Reading from a *tty* device in raw mode causes as many characters as possible to be extracted from the input ring, up to the limit of the user’s read buffer. Input cannot be modified except as directed by other *tty* option bits.

In *line mode*, all input characters are saved until a NEWLINE character is input; then the entire line of characters, including the NEWLINE, is made available in the ring at one time. Reading from a *tty* device in line mode causes characters up to the end of the next line to be extracted from the input ring, up to the limit of the user’s read buffer. Input can be modified by the special characters CTRL+H (backspace),
CTRL+U (line-delete), and CTRL+D (end-of-file), which are discussed in *tty Special Characters*, p.349.

**tty Special Characters**

The following special characters are enabled if the *tty* device operates in line mode, that is, with the OPT_LINE bit set:

- The backspace character, by default CTRL+H, causes successive previous characters to be deleted from the current line, up to the start of the line. It does this by echoing a backspace followed by a space, and then another backspace.

- The line-delete character, by default CTRL+U, deletes all the characters of the current line.

- The end-of-file (EOF) character, by default CTRL+D, causes the current line to become available in the input ring without a NEWLINE and without entering the EOF character itself. Thus if the EOF character is the first character typed on a line, reading that line returns a zero byte count, which is the usual indication of end-of-file.

The following characters have special effects if the *tty* device is operating with the corresponding option bit set:

- The software flow control characters CTRL+Q and CTRL+S (XON and XOFF). Receipt of a CTRL+S input character suspends output to that channel. Subsequent receipt of a CTRL+Q resumes the output. Conversely, when the VxWorks input buffer is almost full, a CTRL+S is output to signal the other side to suspend transmission. When the input buffer is empty enough, a CTRL+Q is output to signal the other side to resume transmission. The software flow control characters are enabled by OPT_TANDEM.

- The ROM monitor trap character, by default CTRL+X. This character traps to the ROM-resident monitor program. Note that this is drastic. All normal VxWorks functioning is suspended, and the computer system is controlled entirely by the monitor. Depending on the particular monitor, it may or may not be possible to restart VxWorks from the point of interruption. The monitor trap character is enabled by OPT_MON_TRAP.

- The special kernel shell abort character, by default CTRL+C. This character restarts the kernel shell if it gets stuck in an unfriendly routine, such as one that

---

1. It will not be possible to restart VxWorks if un-handled external interrupts occur during the boot countdown.
has taken an unavailable semaphore or is caught in an infinite loop. The kernel shell abort character is enabled by `OPT_ABORT`.

The characters for most of these functions can be changed using the `tyLib` routines shown in Table 6-9.

<table>
<thead>
<tr>
<th>Character</th>
<th>Description</th>
<th>Modifier</th>
</tr>
</thead>
<tbody>
<tr>
<td>CTRL+H</td>
<td>backspace (character delete)</td>
<td><code>tyBackspaceSet()</code></td>
</tr>
<tr>
<td>CTRL+U</td>
<td>line delete</td>
<td><code>tyDeleteLineSet()</code></td>
</tr>
<tr>
<td>CTRL+D</td>
<td>EOF (end of file)</td>
<td><code>tyEOFSet()</code></td>
</tr>
<tr>
<td>CTRL+C</td>
<td>kernel shell abort</td>
<td><code>tyAbortSet()</code></td>
</tr>
<tr>
<td>CTRL+X</td>
<td>trap to boot ROMs</td>
<td><code>tyMonitorTrapSet()</code></td>
</tr>
<tr>
<td>CTRL+S</td>
<td>output suspend</td>
<td>N/A</td>
</tr>
<tr>
<td>CTRL+Q</td>
<td>output resume</td>
<td>N/A</td>
</tr>
</tbody>
</table>

### I/O Control Functions

The `tty` devices respond to the `ioctl()` functions in Table 6-10, defined in `ioLib.h`. For more information, see the reference entries for `tyLib`, `ttyDrv`, and `ioctl()`.

<table>
<thead>
<tr>
<th>Function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>FIOBAUDRATE</td>
<td>Sets the baud rate to the specified argument.</td>
</tr>
<tr>
<td>FIOCANCEL</td>
<td>Cancels a read or write.</td>
</tr>
<tr>
<td>FIOFLUSH</td>
<td>Discards all bytes in the input and output buffers.</td>
</tr>
<tr>
<td>FIOGETNAME</td>
<td>Gets the filename of the file descriptor.</td>
</tr>
<tr>
<td>FIOGETOPTIONS</td>
<td>Returns the current device option word.</td>
</tr>
<tr>
<td>FIONREAD</td>
<td>Gets the number of unread bytes in the input buffer.</td>
</tr>
<tr>
<td>FIONWRITE</td>
<td>Gets the number of bytes in the output buffer.</td>
</tr>
</tbody>
</table>
6.7 Devices in VxWorks

6.7.2 Pipe Devices

Pipes are virtual devices by which tasks communicate with each other through the I/O system. Tasks write messages to pipes; these messages can then be read by other tasks. Pipe devices are managed by `pipeDrv` and use the kernel message queue facility to bear the actual message traffic.

Creating Pipes

Pipes are created by calling the pipe create routine:

```c
status = pipeDevCreate ("/pipe/name", maxMsgs, maxLength);
```

The new pipe can have at most `maxMsgs` messages queued at a time. Tasks that write to a pipe that already has the maximum number of messages queued are blocked until a message is dequeued. Each message in the pipe can be at most `maxLength` bytes long; attempts to write longer messages result in an error.

I/O Control Functions

Pipe devices respond to the `ioctl()` functions summarized in Table 6-11. The functions listed are defined in the header file `ioLib.h`. For more information, see the reference entries for `pipeDrv` and for `ioctl()` in `ioLib`.

---

Table 6-10 I/O Control Functions Supported by `tyLib` (cont'd)

<table>
<thead>
<tr>
<th>Function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>FIOSETOPTIONS</td>
<td>Sets the device option word.</td>
</tr>
</tbody>
</table>

⚠️ **CAUTION:** To change the driver’s hardware options (for example, the number of stop bits or parity bits), use the `ioctl()` function `SIO_HW_OPTS_SET`. Because this command is not implemented in most drivers, you may need to add it to your BSP serial driver, which resides in `installDir/vxworks-6.x/target/src/drv/sio`. The details of how to implement this command depend on your board’s serial chip. The constants defined in the header file `installDir/vxworks-6.x/target/h/sioLib.h` provide the POSIX definitions for setting the hardware options.
6.7.3 Pseudo Memory Devices

The `memDrv` driver allows the I/O system to access memory directly as a pseudo-I/O device. Memory location and size are specified when the device is created. This feature is useful when data must be preserved between boots of VxWorks or when sharing data between CPUs. This driver does not implement a file system, unlike `ramDrv`. The `ramDrv` driver must be given memory over which it has absolute control; whereas `memDrv` provides a high-level method of reading and writing bytes in absolute memory locations through I/O calls.

The `memDrv` driver is initialized automatically by the system with `memDrv()` when the `INCLUDE_USR_MEMDRV` component is included in the VxWorks kernel. The call for device creation must be made from the kernel:

```c
STATUS memDevCreate
    (char * name, char * base, int length)
```

Memory for the device is an absolute memory location beginning at `base`. The `length` parameter indicates the size of the memory.

For additional information on the memory driver, see the VxWorks API reference entries for `memDrv()`, `memDevCreate()`, and `memDevCreateDir()`, as well as the entry for `memdrvbuild` in the online Wind River Host Utilities API Reference.

### I/O Control Functions

The memory device responds to the `ioctl()` functions summarized in Table 6-12. The functions listed are defined in the header file `ioLib.h`.

<table>
<thead>
<tr>
<th>Function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>FIOFLUSH</td>
<td>Discards all messages in the pipe.</td>
</tr>
<tr>
<td>FIOGETNAME</td>
<td>Gets the pipe name of the file descriptor.</td>
</tr>
<tr>
<td>FIONMSGS</td>
<td>Gets the number of messages remaining in the pipe.</td>
</tr>
<tr>
<td>FIONREAD</td>
<td>Gets the size in bytes of the first message in the pipe.</td>
</tr>
</tbody>
</table>

Table 6-11 I/O Control Functions Supported by `pipeDrv`
Network File System (NFS) Devices

Network File System (NFS) devices allow files on remote hosts to be accessed with the NFS protocol. The NFS protocol specifies both client software, to read files from remote machines, and server software, to export files to remote machines.

The driver nfsDrv acts as a VxWorks NFS client to access files on any NFS server on the network. VxWorks also allows you to run an NFS server to export files to other systems; see Wind River Network Stack for VxWorks 6 Programmer's Guide.

Using NFS devices, you can create, open, and access remote files exactly as though they were on a file system on a local disk. This is called network transparency.

Mounting a Remote NFS File System from VxWorks

Access to a remote NFS file system is established by mounting that file system locally and creating an I/O device for it using nfsMount(). Its arguments are (1) the host name of the NFS server, (2) the name of the host file system, and (3) the local name for the file system.

For example, the following call mounts /usr of the host mars as /vxusr locally:

```c
nfsMount("mars", "/usr", "/vxusr");
```

This creates a VxWorks I/O device with the specified local name (/vxusr, in this example). If the local name is specified as NULL, the local name is the same as the remote name.

After a remote file system is mounted, the files are accessed as though the file system were local. Thus, after the previous example, opening the file /vxusr/foo opens the file /usr/foo on the host mars.

The remote file system must be exported by the system on which it actually resides. However, NFS servers can export only local file systems. Use the appropriate
command on the server to see which file systems are local. NFS requires authentication parameters to identify the user making the remote access. To set these parameters, use the routines `nfsAuthUnixSet()` and `nfsAuthUnixPrompt()`.

To include NFS client support, use the `INCLUDE_NFS` component.

The subject of exporting and mounting NFS file systems and authenticating access permissions is discussed in more detail in *Wind River Network Stack for VxWorks 6 Programmer’s Guide: File Access Applications*. See also the reference entries `nfsLib` and `nfsDrv`, and the NFS documentation from Sun Microsystems.

### I/O Control Functions for NFS Clients

NFS client devices respond to the `ioctl()` functions summarized in Table 6-13. The functions listed are defined in `ioLib.h`. For more information, see the reference entries for `nfsDrv`, `ioLib`, and `ioctl()`.

<table>
<thead>
<tr>
<th>Function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>FIOFSTATGET</td>
<td>Gets file status information (directory entry data).</td>
</tr>
<tr>
<td>FIOGETNAME</td>
<td>Gets the filename of the file descriptor.</td>
</tr>
<tr>
<td>FIONREAD</td>
<td>Gets the number of unread bytes in the file.</td>
</tr>
<tr>
<td>FIOREaddir</td>
<td>Reads the next directory entry.</td>
</tr>
<tr>
<td>FIOSEEK</td>
<td>Sets the current byte offset in the file.</td>
</tr>
<tr>
<td>FIOSYNC</td>
<td>Flushes data to a remote NFS file.</td>
</tr>
<tr>
<td>FIOWHERE</td>
<td>Returns the current byte position in the file.</td>
</tr>
</tbody>
</table>

### 6.7.5 Non-NFS Network Devices

VxWorks also supports network access to files on a remote host through the Remote Shell protocol (RSH) or the File Transfer Protocol (FTP).

These implementations of network devices use the driver `netDrv`, which is included in the Wind River Network Stack. Using this driver, you can open, read, write, and close files located on remote systems without needing to manage the details of the underlying protocol used to effect the transfer of information. (For
more information, see the *Wind River Network Stack for VxWorks 6 Programmer’s Guide: Working With Device Instances.*)

When a remote file is opened using RSH or FTP, the entire file is copied into local memory. As a result, the largest file that can be opened is restricted by the available memory. Read and write operations are performed on the memory-resident copy of the file. When closed, the file is copied back to the original remote file if it was modified.

In general, NFS devices are preferable to RSH and FTP devices for performance and flexibility, because NFS does not copy the entire file into local memory. However, NFS is not supported by all host systems.

**Creating Network Devices**

To access files on a remote host using either RSH or FTP, a network device must first be created by calling the routine `netDevCreate()`. The arguments to `netDevCreate()` are (1) the name of the device, (2) the name of the host the device accesses, and (3) which protocol to use: 0 (RSH) or 1 (FTP).

For example, the following call creates an RSH device called `mars:` that accesses the host `mars`. By convention, the name for a network device is the remote machine’s name followed by a colon (:)．

```
netDevCreate ("mars:", "mars", 0);
```

Files on a network device can be created, opened, and manipulated as if on a local disk. Thus, opening the file `mars:/usr/foo` actually opens `/usr/foo` on host `mars`.

Note that creating a network device allows access to any file or device on the remote system, while mounting an NFS file system allows access only to a specified file system.

For the files of a remote host to be accessible with RSH or FTP, permissions and user identification must be established on both the remote and local systems. Creating and configuring network devices is discussed in detail in *Wind River Network Stack for VxWorks 6 Programmer’s Guide: File Access Applications* and in the API reference entry for `netDrv`. 
I/O Control Functions

RSH and FTP devices respond to the same ioctl() functions as NFS devices except for FIOSYNC and FIOREADDIR. The functions are defined in the header file ioLib.h. For more information, see the API reference entries for netDrv and ioctl().

6.7.6 Sockets

In VxWorks, the underlying basis of network communications is sockets. A socket is an endpoint for communication between tasks; data is sent from one socket to another. Sockets are not created or opened using the standard I/O functions. Instead, they are created by calling socket(), and connected and accessed using other routines in sockLib. However, after a stream socket (using TCP) is created and connected, it can be accessed as a standard I/O device, using read(), write(), ioctl(), and close(). The value returned by socket() as the socket handle is in fact an I/O system file descriptor.

VxWorks socket routines are source-compatible with the BSD 4.4 UNIX socket functions and the Windows Sockets (Winsock 1.1) networking standard. Use of these routines is discussed in Wind River Network Stack for VxWorks 6 Programmer’s Guide.

6.7.7 Extended Block Device Facilities: XBD

The extended block device (XBD) component (INCLUDE_XBD) is a facility that mediates I/O activity between file systems and block devices, providing a standard interface between block drivers on the one hand, and file systems on the other. XBD provides support for removable file systems, automatic file system detection, and multiple file systems.

Note that XBD is required for some file systems (such as HRFS, dosFs, cdromFs, and rawFs), but not others (such as ROMFS).

For detailed information on XBD beyond what is provided in section, see the VxWorks Device Driver Developer’s Guide.

The XBD facility include several sub-components for special purposes:

INCLUDE_XBD_PART_LIB
Provides disk partitioning facilities. See XBD Disk Partition Manager, p.357.

INCLUDE_XBD_RAMDRV
Provides support for RAM disks. See XBD RAM Disk Component, p.358.
INCLUDE_XBD_BLK_DEV
Provides support for legacy block devices that were designed to work with the predecessor to XBD—the CBIO (cache block I/O) facility. These devices include floppy drives, SCSI, and TrueFFS (the disk-access emulator for flash). See XBD Block Device Wrapper Component, p.358.

INCLUDE_XBD_TRANS
This component is a transaction-based file system (TRFS) facility, which can be used with dosFs. It provides fault-tolerant file system consistency and fast recovery in response to power loss. See 6.7.8 Transaction-Based Reliable File System Facility: TRFS, p.359.

XBD Disk Partition Manager

It is commonplace to share fixed disks and removable cartridges between VxWorks target systems and PCs running Windows. VxWorks therefore provides support for PC-style disk partitioning with the INCLUDE_XBD_PART_LIB component. This component includes two modules: xbdPartition and partLib.

xbdPartition Module

The xbdPartition facility creates an XBD pseudo-device for each partition detected. Each detected partition in turn is probed by the file system monitor and an I/O device is added to VxWorks. This device being an instantiation of the file system found by the file system monitor (or rawFs if the file system is not recognized or detected). If no partitions are detected, a zero partition XBD is created to represent the entire media. There can be up to four partitions on a single media. For information about the file system monitor, see 7.2 File System Monitor, p.405.

Partition XBD also have names. They are derived the partition number and the base XBD device name. For example: For an ATA hard disk, the backing XBD device or base XBD, could be named /ata00. If the disk has four partitions each will be named as follows:

/ata00:1
/ata00:2
/ata00:3
/ata00:4

If no partitions exist on the media then the partition manager will instantiate an XBD called /ata00:0. This XBD maps the entire disk. Note that the XBD name is not necessarily the name that will appear in core I/O. The file system name mapping component can be employed to map XBD names to a more suitable name. For
example, the default ATA configlette maps partitions according to the parameter supplied to the configlette. By default, the master device on the first controller maps the first partition to /ata0a and if no partitions exists maps the entire disk to /ata00.

partLib Library

The partLib library provides facilities for creating PC-style partitions on media. It can create up to four primary partitions. Note that when partitions are created, any existing information on the media is lost. For more information see the VxWorks API reference for `xbdCreatePartition()`.

XBD RAM Disk Component

For some applications it is desirable to use a file system to organize and access data even when no disk or other traditional media is present. The `INCLUDE_XBD_RAMDRV` component allows the use of a file system to access data stored in RAM memory. RAM disks can be created using volatile as well a non-volatile RAM.

A RAM disk can be used with the HRFS, dosFs, and rawFs file systems.

XBD Block Device Wrapper Component

The `INCLUDE_XBD_BLKDEV` component provides support for legacy block devices that were designed to work the predecessor to XBD—the CBIO (cache block I/O) facility. It provides a wrapper XBD facility that converts the block I/O driver interface based on the BLK_DEV logical block device structure into an XBD API-compliant interface.

The Wind River devices that require the `INCLUDE_XBD_BLKDEV` component in addition to `INCLUDE_XBD` are floppy, SCSI, and TrueFFS (the disk-access emulator for flash) drivers. Any third-party device drivers based on the BLK_DEV interface also require `INCLUDE_XBD_BLKDEV`.

The Wind River drivers that do not require the `INCLUDE_XBD_BLK_DEV` component are USB block storage, ATA, and the XBD RAM disk.
The INCLUDE_XBD_TRANS component is an XBD-compatible transaction-based reliable file system (TRFS) facility. TRFS is an I/O facility that provides fault-tolerant file system layer for the dosFs file system. See 6.7.8 Transaction-Based Reliable File System Facility: TRFS, p.359 for more information.

XBD TRFS Component

The INCLUDE_XBD_TRANS component is an XBD-compatible transaction-based reliable file system (TRFS) facility. TRFS is an I/O facility that provides fault-tolerant file system layer for the dosFs file system. See 6.7.8 Transaction-Based Reliable File System Facility: TRFS, p.359 for more information.

6.7.8 Transaction-Based Reliable File System Facility: TRFS

The transaction-based reliable file system (TRFS) component (INCLUDE_XBD_TRANS) is an I/O facility that provides a fault-tolerant file system layer for the dosFs file system.

TRFS provides both file system consistency and fast recovery for the dosFs file system (DOS-compatible file systems are themselves neither reliable nor transaction-based). It is designed to operate with XBD-compliant device drivers for hard disks, floppy disks, compact flash media, TrueFFS flash devices, and so on. It can also be used with the XBD wrapper component for device drivers that are not XBD-compliant.

TRFS provides reliability in resistance to sudden power loss: files and data that are already written to media are unaffected, they will not be deleted or corrupted because data is always written either in its entirety or not at all.

Unlike some facilities that provide data integrity on a file-by-file basis, TRFS protects the medium as a whole. It is transactional for a file system, which means

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\[\text{www.vxworks.com} \]

\[\text{CAUTION: Depending on the implementation of the driver, the} \]

\[\text{INCLUDE_XBD_BLK_DEV component may not properly detect media insertion} \]

\[\text{and removal. It may, therefore remove the file system when the media is removed,} \]

\[\text{or not instantiate a file system when media is inserted.} \]

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\[\text{www.vxworks.com} \]
that setting transaction points will commit all files, not just the one used to set the transaction point.

NOTE: While TRFS is a I/O layer added to dosFs, it uses a modified on-media format that is not compatible with other FAT-based file systems, including Microsoft Windows and the VxWorks dosFs file system without the TRFS layer. It should not, therefore, be used when compatibility with other systems is a requirement.

For information about dosFs, see 7.4 MS-DOS-Compatible File System: dosFs, p.420.

Configuring VxWorks With TRFS

Configure VxWorks with the INCLUDE_XBD_TRANS component to provide TRFS functionality for your dosFs file system.

Creating a TRFS Shim Layer

A TRFS shim is created by a call to `devTransCreate()`. This routine takes a single argument (`subDev`), which can be either a XBD module or a block device structure (`BLK_DEV`). This shim may then format the underlying XBD with the TRFS low-level format, or access an already formatted device.

Formatting a Device for TRFS

TRFS low-level formatting is accomplished with the call:

```c
usrFormatTrans(device, overHead, type);
```

The arguments are:

- `device`
  - The volume name to format. For example, "/ata".

- `overHead`
  - An integer that identifies the portion of the disk to use as transactional workspace in parts-per-thousand of the disk.
type
An integer with the values of either FORMAT_REGULAR (0), which does not reserve any blocks from the disk; or FORMAT_TFFS (1), which reserves the first block.

TRFS is automatically detected and instantiated if the media has already been formatted for use with TRFS. There is no need to explicitly call a creation function.

Once the TRFS format is complete, a dosFs file system can be created by calling the dosFs formatter on the same volume.

When a FAT file system is created using the function dosFsVolFormat() in conjunction with TRFS, a transaction point is automatically inserted following the format. One cannot, therefore, unformat by rolling back a transaction point.

Using the TRFS in Applications

Once TRFS and dosFs are created, the dosFs file system may be used with the ordinary file creation and manipulation commands. No changes to the file system become permanent, however, until TRFS is used to commit them.

It is important to note that the entire dosFs file system—and not individual files—are committed. The entire disk state must therefore be consistent before executing a commit; that is, there must not be a file system operation in progress (by another task, for example) when the file system is committed. If multiple tasks update the file system, care must be taken to ensure the file data is in a known state before setting a transaction point.

There are two ways to commit the file system:

- Using the volume name of the device formatted for TRFS.
- Using a file descriptor which is open on TRFS.

The function usrTransCommit() takes the volume name of the TRFS device and causes it to commit.

The function usrTransCommitFd() takes a file descriptor open on TRFS and causes a commit of the entire file system.

TRFS Code Examples

The following code examples illustrate creating a file system and setting a transaction point.
Creating a New TRFS Layer and dosFs File System

```c
void createTrfs
(
    void
)
{
    /* Create an XBD RAM disk with 512 byte sized sectors and 1024 sectors.*/
    if (xbdRamDiskDevCreate (512, 1024 * 512, 0, "/trfs") == NULL)
    {
        printf ("Could not create XBD RAM disk\n");
        return;
    }
    /* Put the TRFS shim on the RAM disk */
    /* Use 10% of the disk as overhead */
    if (usrFormatTrans ("/trfs", 100, 0) != OK)
    {
        printf ("Could not format for TRFS\n");
        return;
    }
    /* Now put dosFs on TRFS */
    if (dosFsVolFormat ("/trfs", DOS_OPT_BLANK, 0) != OK)
    {
        printf ("Could not format for dos\n");
        return;
    }

    Setting a Transaction Point

    void transTrfs
    (
        void
    )
    {
        /* This assumes a TRFS with DosFs on "/trfs" */

        ... /* Perform file operations here */
        usrTransCommit ("/trfs");

        ... /* Perform more file operations here */
        usrTransCommit ("/trfs");
    }
```
6.7.9 Block Devices

A block device is a device that is organized as a sequence of individually accessible blocks of data. The most common type of block device is a disk. In VxWorks, the term block refers to the smallest addressable unit on the device. For most disk devices, a VxWorks block corresponds to a sector, although terminology varies.

Block devices in VxWorks have a slightly different interface than other I/O devices. Rather than interacting directly with the I/O system, the I/O activity of block device drivers is mediated by the extended block device (XBD) facility and a file system. XBD provides a standard interface for block drivers on the one hand, and for file systems on the other.

Figure 6-2 shows a layered model of I/O for both block and non-block (character) devices. This architecture allows the same block device driver to be used with different file systems, and reduces the number of I/O functions that must be supported in the driver.

For information about XBD, see 6.7.7 Extended Block Device Facilities: XBD, p.356. For information about low-level drivers for block devices, see 6.9.4 Block Device Drivers, p.399.
SCSI Drivers

SCSI is a standard peripheral interface that allows connection with a wide variety of hard disks, optical disks, floppy disks, tape drives, and CD-ROM devices. SCSI
block drivers are compatible with the dosFs libraries, and offer several advantages for target configurations. They provide:

- local mass storage in non-networked environments
- faster I/O throughput than Ethernet networks

The SCSI-2 support in VxWorks supersedes previous SCSI support, although it offers the option of configuring the original SCSI functionality, now known as SCSI-1. With SCSI-2 enabled, the VxWorks environment can still handle SCSI-1 applications, such as file systems created under SCSI-1. However, applications that directly used SCSI-1 data structures defined in `scsiLib.h` may require modifications and recompilation for SCSI-2 compatibility.

The VxWorks SCSI implementation consists of two modules, one for the device-independent SCSI interface and one to support a specific SCSI controller. The `scsiLib` library provides routines that support the device-independent interface; device-specific libraries provide configuration routines that support specific controllers. There are also additional support routines for individual targets in `sysLib.c`.

**Configuring SCSI Drivers**

Components associated with SCSI drivers are listed in Table 6-14.

<table>
<thead>
<tr>
<th>Component</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>INCLUDE_SCSI</td>
<td>Includes SCSI interface.</td>
</tr>
<tr>
<td>INCLUDE_SCSI2</td>
<td>Includes SCSI-2 extensions.</td>
</tr>
<tr>
<td>INCLUDE_SCSI_DMA</td>
<td>Enables DMA for SCSI.</td>
</tr>
<tr>
<td>INCLUDE_SCSI_BOOT</td>
<td>Allows booting from a SCSI device.</td>
</tr>
<tr>
<td>SCSI_AUTO_CONFIG</td>
<td>Auto-configures and locates all targets on a SCSI bus.</td>
</tr>
<tr>
<td>INCLUDE_DOSFS</td>
<td>Includes the dosFs file system.</td>
</tr>
<tr>
<td>INCLUDE_HRFS</td>
<td>Includes the HRFS file system.</td>
</tr>
<tr>
<td>INCLUDE_CDROMFS</td>
<td>Includes CD-ROM file system support.</td>
</tr>
</tbody>
</table>
To include SCSI-1 functionality in VxWorks, use the `INCLUDE_SCSI` component. To include SCSI-2 functionality, you must use `INCLUDE_SCSI2` in addition to `INCLUDE_SCSI`.

Auto-configuration, DMA, and booting from a SCSI device are defined appropriately for each BSP. If you need to change these settings, see the reference for `sysScsiConfig()` and the source file `installDir/vxworks-6.x/target/src/config/usrScsi.c`.

⚠️ **CAUTION:** Including SCSI-2 in your VxWorks image can significantly increase the image size.

### Configuring the SCSI Bus ID

Each board in a SCSI-2 environment must define a unique SCSI bus ID for the SCSI initiator. SCSI-1 drivers, which support only a single initiator at a time, assume an initiator SCSI bus ID of 7. However, SCSI-2 supports multiple initiators, up to eight initiators and targets at one time. Therefore, to ensure a unique ID, choose a value in the range 0-7 to be passed as a parameter to the driver’s initialization routine (for example, `ncr710CtrlInitScsi2()`) by the `sysScsiInit()` routine in `sysScsi.c`. For more information, see the reference entry for the relevant driver initialization routine. If there are multiple boards on one SCSI bus, and all of these boards use the same BSP, then different versions of the BSP must be compiled for each board by assigning unique SCSI bus IDs.

### ROM Size Adjustment for SCSI Boot

If the `INCLUDE_SCSI_BOOT` component is included, larger ROMs may be required for some boards.

### Structure of the SCSI Subsystem

The SCSI subsystem supports libraries and drivers for both SCSI-1 and SCSI-2. It consists of the following six libraries which are independent of any SCSI controller:

- **scsiLib**
  
  Routines that provide the mechanism for switching SCSI requests to either the SCSI-1 library (`scsi1Lib`) or the SCSI-2 library (`scsi2Lib`), as configured by the board support package (BSP).

- **scsi1Lib**
  
  SCSI-1 library routines and interface, used when only `INCLUDE_SCSI` is used (see *Configuring SCSI Drivers*, p.365).
**scsi2Lib**

SCSI-2 library routines and all physical device creation and deletion routines.

**scsiCommonLib**

commands common to all types of SCSI devices.

**scsiDirectLib**

routines and commands for direct access devices (disks).

**scsiSeqLib**

routines and commands for sequential access block devices (tapes).

Controller-independent support for the SCSI-2 functionality is divided into **scsi2Lib**, **scsiCommonLib**, **scsiDirectLib**, and **scsiSeqLib**. The interface to any of these SCSI-2 libraries can be accessed directly. However, **scsiSeqLib** is designed to be used in conjunction with tapeFs, while **scsiDirectLib** works with dosFs and rawFs. Applications written for SCSI-1 can be used with SCSI-2; however, SCSI-1 device drivers cannot.

VxWorks targets using SCSI interface controllers require a controller-specific device driver. These device drivers work in conjunction with the controller-independent SCSI libraries, and they provide controller configuration and initialization routines contained in controller-specific libraries. For example, the Western Digital WD33C93 SCSI controller is supported by the device driver libraries **wd33c93Lib**, **wd33c93Lib1**, and **wd33c93Lib2**. Routines tied to SCSI-1 (such as **wd33c93CtrlCreate()**) and SCSI-2 (such as **wd33c93CtrlCreateScsi2()**) are segregated into separate libraries to simplify configuration. There are also additional support routines for individual targets in **sysLib.c**.

**Booting and Initialization**

When VxWorks is built with the `INCLUDE_SCSI` component, the system startup code initializes the SCSI interface by executing **sysScsiInit()** and **usrScsiConfig()**. The call to **sysScsiInit()** initializes the SCSI controller and sets up interrupt handling. The physical device configuration is specified in **usrScsiConfig()**, which is in `installDir/vxworks-6.x/target/src/config/usrScsi.c`. The routine contains an example of the calling sequence to declare a hypothetical configuration, including:

- definition of physical devices with **scsiPhysDevCreate()**
- creation of logical partitions with **scsiBlkDevCreate()**
- creation of an XBD block wrapper driver with **xbdBlkDevCreate()**.
If a recognized file system exists on the SCSI media, it is instantiated automatically when `xbdBlkDevCreate()` returns. If not, the file system formatter needs to be called to create the file system. See the `dosFsVolFormat()` API reference for information about creating a dosFs file system; see the `hrfsFormat()` API reference for creating an HRFS file system.

If you are not using `SCSI_AUTO_CONFIG`, modify `usrScsiConfig()` to reflect your actual configuration. For more information on the calls used in this routine, see the reference entries for `scsiPhysDevCreate()`, `scsiBlkDevCreate()`, and `xbdBlkDevCreate()`.

### Device-Specific Configuration Options

The SCSI libraries have the following default behaviors enabled:

- SCSI messages
- disconnects
- minimum period and maximum REQ/ACK offset
- tagged command queuing
- wide data transfer

Device-specific options do not need to be set if the device shares this default behavior. However, if you need to configure a device that diverges from these default characteristics, use `scsiTargetOptionsSet()` to modify option values. These options are fields in the `SCSI_OPTIONS` structure, shown below.

`SCSI_OPTIONS` is declared in `scsi2Lib.h`. You can choose to set some or all of these option values to suit your particular SCSI device and application.

```c
typedef struct /* SCSI_OPTIONS - programmable options */
{
    UINT selTimeOut; /* device selection time-out (us) */
    BOOL messages; /* FALSE => do not use SCSI messages */
    BOOL disconnect; /* FALSE => do not use disconnect */
    UINT8 maxOffset; /* max sync xfer offset (0 => async.) */
    UINT8 minPeriod; /* min sync xfer period (x 4 ns) */
    SCSI_TAG_TYPE tagType; /* default tag type */
    UINT maxTags; /* max cmd tags available (0 => untag */
    UINT8 xferWidth; /* wide data trnsfr width in SCSI units */
} SCSI_OPTIONS;
```

There are numerous types of SCSI devices, each supporting its own mix of SCSI-2 features. To set device-specific options, define a `SCSI_OPTIONS` structure and assign the desired values to the structure’s fields. After setting the appropriate fields, call `scsiTargetOptionsSet()` to effect your selections. Example 6-5 illustrates one possible device configuration using `SCSI_OPTIONS`. 
Call `scsiTargetOptionsSet()` after initializing the SCSI subsystem, but before initializing the SCSI physical device. For more information about setting and implementing options, see the reference entry for `scsiTargetOptionsSet()`.

**WARNING:** Calling `scsiTargetOptionsSet()` after the physical device has been initialized may lead to undefined behavior.

The SCSI subsystem performs each SCSI command request as a SCSI transaction. This requires the SCSI subsystem to select a device. Different SCSI devices require different amounts of time to respond to a selection; in some cases, the `selTimeOut` field may need to be altered from the default.

If a device does not support SCSI messages, the boolean field `messages` can be set to FALSE. Similarly, if a device does not support disconnect/reconnect, the boolean field `disconnect` can be set to FALSE.

The SCSI subsystem automatically tries to negotiate synchronous data transfer parameters. However, if a SCSI device does not support synchronous data transfer, set the `maxOffset` field to 0. By default, the SCSI subsystem tries to negotiate the maximum possible REQ/ACK offset and the minimum possible data transfer period supported by the SCSI controller on the VxWorks target. This is done to maximize the speed of transfers between two devices. However, speed depends upon electrical characteristics, like cable length, cable quality, and device termination; therefore, it may be necessary to reduce the values of `maxOffset` or `minPeriod` for fast transfers.

The `tagType` field defines the type of tagged command queuing desired, using one of the following macros:

- `SCSI_TAG_UNTAGGED`
- `SCSI_TAG_SIMPLE`
- `SCSI_TAG_ORDERED`
- `SCSI_TAG_HEAD_OF_QUEUE`

For more information about the types of tagged command queuing available, see the ANSI X3T9-I/O Interface Specification *Small Computer System Interface (SCSI-2)*.

The `maxTags` field sets the maximum number of command tags available for a particular SCSI device.

Wide data transfers with a SCSI target device are automatically negotiated upon initialization by the SCSI subsystem. Wide data transfer parameters are always negotiated before synchronous data transfer parameters, as specified by the SCSI ANSI specification, because a wide negotiation resets any prior negotiation of synchronous parameters. However, if a SCSI device does not support wide
parameters and there are problems initializing that device, you must set the xferWidth field to 0. By default, the SCSI subsystem tries to negotiate the maximum possible transfer width supported by the SCSI controller on the VxWorks target in order to maximize the default transfer speed between the two devices. For more information on the actual routine call, see the reference entry for scsiTargetOptionsSet().

**SCSI Configuration Examples**

The following examples show some possible configurations for different SCSI devices. Example 6-4 is a simple block device configuration setup. Example 6-5 involves selecting special options and demonstrates the use of scsiTargetOptionsSet(). Example 6-6 configures a SCSI device for synchronous data transfer. Example 6-7 shows how to configure the SCSI bus ID. These examples can be embedded either in the usrScsiConfig() routine or in a user-defined SCSI configuration function.

**Example 6-4 Configuring SCSI Drivers**

In the following example, usrScsiConfig() was modified to reflect a new system configuration. The new configuration has a SCSI disk with a bus ID of 4 and a Logical Unit Number (LUN) of 0 (zero). The disk is configured with a dosFs file system (with a total size of 0x20000 blocks) and a rawFs file system (spanning the remainder of the disk).

The following usrScsiConfig() code reflects this modification.

```c
/* configure Winchester at busId = 4, LUN = 0 */
if ((pSpd40 = scsiPhysDevCreate (pSysScsiCtrl, 4, 0, 0, NONE, 0, 0, 0))
    == (SCSI_PHYS_DEV *) NULL)
{
    SCSI_DEBUG_MSG ("usrScsiConfig: scsiPhysDevCreate failed.\n");
} else
{
    /* create block devices - one for dosFs and one for rawFs */
    if (((pSbd0 = scsiBlkDevCreate (pSpd40, 0x20000, 0)) == NULL) ||
        ((pSbd1 = scsiBlkDevCreate (pSpd40, 0, 0x20000)) == NULL))
    {
        return (ERROR);
    }
    /* initialize both dosFs and rawFs file systems */
    if ((xdbBlkDevCreate (pSbd0, "/sd0") == NULL) ||
```
If problems with your configuration occur, insert the following lines at the beginning of \texttt{usrScsiConfig()} to obtain further information on SCSI bus activity.

\begin{verbatim}
#if FALSE
scsiDebug = TRUE;
scsiIntsDebug = TRUE;
#endif
\end{verbatim}

Do not declare the global variables \texttt{scsiDebug} and \texttt{scsiIntsDebug} locally. They can be set or reset from the shell.

\textbf{Example 6-5} \hspace{1cm} \textbf{Configuring a SCSI Disk Drive with Asynchronous Data Transfer and No Tagged Command Queuing}

In this example, a SCSI disk device is configured without support for synchronous data transfer and tagged command queuing. The \texttt{scsiTargetOptionsSet()} routine is used to turn off these features. The SCSI ID of this disk device is 2, and the LUN is 0:

```c
int which;
SCSI_OPTIONS option;
int devBusId;
devBusId = 2;
which = SCSI_SET_OPT_XFER_PARAMS | SCSI_SET_OPT_TAG_PARAMS;
option.maxOffset = SCSI_SYNC_XFER_ASYNC_OFFSET; /* => 0 defined in scsi2Lib.h */
option.minPeriod = SCSI_SYNC_XFER_MIN_PERIOD; /* defined in scsi2Lib.h */
option.tagType = SCSI_TAG_UNTAGGED; /* defined in scsi2Lib.h */
option.maxTag = SCSI_MAX_TAGS;
if (scsiTargetOptionsSet (pSysScsiCtrl, devBusId, &option, which) == ERROR)
{
    SCSIDEBUG_MSG ("usrScsiConfig: could not set options\n", 0, 0, 0, 0);
    return (ERROR);
}
/* configure SCSI disk drive at busId = devBusId, LUN = 0 */
if ((pSpd20 = scsiPhysDevCreate (pSysScsiCtrl, devBusId, 0, 0, NONE, 0, 0, 0)) == (SCSI_PHYS_DEV *))
{
    SCSI_DEBUG_MSG ("usrScsiConfig: scsiPhysDevCreate failed.\n");
    return (ERROR);
}
```

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Example 6-6  Configuring a SCSI Disk for Synchronous Data Transfer with Non-Default Offset and Period Values

In this example, a SCSI disk drive is configured with support for synchronous data transfer. The offset and period values are user-defined and differ from the driver default values. The chosen period is 25, defined in SCSI units of 4 ns. Thus, the period is actually $4 \times 25 = 100$ ns. The synchronous offset is chosen to be 2. Note that you may need to adjust the values depending on your hardware environment.

```c
int
SCSI_OPTIONS
int
devBusId;

devBusId = 2;

which = SCSI_SET_IPT_XFER_PARAMS;
option.maxOffset = 2;
option.minPeriod = 25;

if (scsiTargetOptionsSet (pSysScsiCtrl, devBusId &option, which) == ERROR)
{
    SCSI_DEBUG_MSG ("usrScsiConfig: could not set options\n",
        0, 0, 0, 0, 0, 0)
    return (ERROR);
}

/* configure SCSI disk drive at busId = devBusId, LUN = 0 */

if ((pSpd20 = scsiPhysDevCreate (pSysScsiCtrl, devBusId, 0, 0, NONE,
        0, 0, 0)) == (SCSI_PHYS_DEV *) NULL)
{
    SCSI_DEBUG_MSG ("usrScsiConfig: scsiPhysDevCreate failed.\n")
    return (ERROR);
}
```

Example 6-7  Changing the Bus ID of the SCSI Controller

To change the bus ID of the SCSI controller, modify `sysScsiInit()` in `sysScsi.c`. Set the SCSI bus ID to a value between 0 and 7 in the call to `xxxCtrlInitScsi2()`, where `xxx` is the controller name. The default bus ID for the SCSI controller is 7.

Troubleshooting

- Incompatibilities Between SCSI-1 and SCSI-2

Applications written for SCSI-1 may not execute for SCSI-2 because data structures in `scsi2Lib.h`, such as `SCSI_TRANSACTION` and `SCSI_PHYS_DEV`, have changed. This applies only if the application used these structures directly.
If this is the case, you can choose to configure only the SCSI-1 level of support, or you can modify your application according to the data structures in `scsi2Lib.h`. In order to set new fields in the modified structure, some applications may simply need to be recompiled, and some applications will have to be modified and then recompiled.

- **SCSI Bus Failure**

  If your SCSI bus hangs, it could be for a variety of reasons. Some of the more common are:
  
  - Your cable has a defect. This is the most common cause of failure.
  
  - The cable exceeds the cumulative maximum length of 6 meters specified in the SCSI-2 standard, thus changing the electrical characteristics of the SCSI signals.
  
  - The bus is not terminated correctly. Consider providing termination power at both ends of the cable, as defined in the SCSI-2 ANSI specification.
  
  - The minimum transfer period is insufficient or the REQ/ACK offset is too great. Use `scsiTargetOptionsSet()` to set appropriate values for these options.
  
  - The driver is trying to negotiate wide data transfers on a device that does not support them. In rejecting wide transfers, the device-specific driver cannot handle this phase mismatch. Use `scsiTargetOptionsSet()` to set the appropriate value for the `xferWidth` field for that particular SCSI device.

### 6.8 Differences Between VxWorks and Host System I/O

Most commonplace uses of I/O in VxWorks are completely source-compatible with I/O in UNIX and Windows. However, note the following differences:

- **Device Configuration**

  In VxWorks, device drivers can be installed and removed dynamically. But only in the kernel space.
**File Descriptors**

In VxWorks, file descriptors are unique to the kernel and to each process—as in UNIX and Windows. The kernel and each process has its own universe of file descriptors, distinct from each other. When the process is created, its universe of file descriptors is initially populated by duplicating the file descriptors of its creator. (This applies only when the creator is a process. If the creator is a kernel task, only the three standard I/O descriptors 0, 1 and 2 are duplicated.) Thereafter, all open, close, or `dup` activities affect only that process’ universe of descriptors.

In kernel and in each process, file descriptors are global to that entity, meaning that they are accessible by any task running in it.

In the kernel, however, standard input, standard output, and standard error (0, 1, and 2) can be made task specific.

For more information see 6.3.1 File Descriptors, p.322 and 6.3.3 Standard I/O Redirection, p.324.

**I/O Control**

The specific parameters passed to `ioctl()` functions can differ between UNIX and VxWorks.

**Driver Routines**

In UNIX, device drivers execute in system mode and cannot be preempted. In VxWorks, driver routines can be preempted because they execute within the context of the task that invoked them.

### 6.9 Internal I/O System Structure

The VxWorks I/O system differs from most I/O systems in the way that the work of performing user I/O requests is distributed between the device-independent I/O system and the device drivers themselves.

In many systems, the device driver supplies a few routines to perform low-level I/O functions such as reading a sequence of bytes from, or writing them to, character-oriented devices. The higher-level protocols, such as communications protocols on character-oriented devices, are implemented in the
device-independent part of the I/O system. The user requests are heavily processed by the I/O system before the driver routines get control.

While this approach is designed to make it easy to implement drivers and to ensure that devices behave as much alike as possible, it has several drawbacks. The driver writer is often seriously hampered in implementing alternative protocols that are not provided by the existing I/O system. In a real-time system, it is sometimes desirable to bypass the standard protocols altogether for certain devices where throughput is critical, or where the device does not fit the standard model.

In the VxWorks I/O system, minimal processing is done on user I/O requests before control is given to the device driver. The VxWorks I/O system acts as a switch to route user requests to appropriate driver-supplied routines. Each driver can then process the raw user requests as appropriate to its devices. In addition, however, several high-level subroutine libraries are available to driver writers that implement standard protocols for both character- and block-oriented devices. Thus the VxWorks I/O system provides the best of both worlds: while it is easy to write a standard driver for most devices with only a few pages of device-specific code, driver writers are free to execute the user requests in nonstandard ways where appropriate.

There are two fundamental types of device: block and character (or non-block; see Figure 6-2). Block devices are used for storing file systems. They are random access devices where data is transferred in blocks. Examples of block devices include hard and floppy disks. Character devices are typically of the tty/sio type.

As discussed in earlier sections, the three main elements of the VxWorks I/O system are drivers, devices, and files. The following sections describe these elements in detail. The discussion focuses on character drivers; however, much of it is applicable to block devices. Because block drivers must interact with VxWorks file systems, they use a slightly different organization; see 6.9.4 Block Device Drivers, p.399.

NOTE: This discussion is designed to clarify the structure of VxWorks I/O facilities and to highlight some considerations relevant to writing I/O drivers for VxWorks. For detailed information about writing device drivers, see the VxWorks Device Driver Developer’s Guide.

Example 6-8 shows the abbreviated code for a hypothetical driver that is used as an example throughout the following discussions. This example driver is typical of drivers for character-oriented devices.
In VxWorks, each driver has a short, unique abbreviation, such as net or tty, which is used as a prefix for each of its routines. The abbreviation for the example driver is xx.

Example 6-8 Hypothetical Driver

```c
/*
 * xxDrv - driver initialization routine
 * xxDrv() initi's the driver. It installs the driver via iosDrvInstall.
 * It may allocate data structures, connect ISRs, and initialize hardware
 */
STATUS xxDrv ()
{
    xxDrvNum = iosDrvInstall (xxCreat, 0, xxOpen, 0, xxRead, xxWrite, xxIoctl);
    (void) intConnect (intvec, xxInterrupt, ...);
    ...
}

/*****************************************************************************************/
/*
 * xxDevCreate - device creation routine
 * Called to add a device called <name> to be svced by this driver. Other
 * driver-dependent arguments may include buffer sizes, device addresses.
 * The routine adds the device to the I/O system by calling iosDevAdd.
 * It may also allocate and initialize data structures for the device,
 * initialize semaphores, initialize device hardware, and so on.
 */
STATUS xxDevCreate (name, ...)
char * name;
...
{
    status = iosDevAdd (xxDev, name, xxDrvNum);
    ...
}

/*
 * The following routines implement the basic I/O functions.
 * The xxOpen() return value is meaningful only to this driver,
 * and is passed back as an argument to the other I/O routines.
 */
int xxOpen (xxDev, remainder, mode)
XXDEV * xxDev;
char * remainder;
int mode;
{
    /* serial devices should have no file name part */
```
if (remainder[0] != 0)
    return (ERROR);
else
    return ((int) xxDev);
}

int xxRead (xxDev, buffer, nBytes)
XXDEV * xxDev;
char * buffer;
int nBytes;
...
int xxWrite (xxDev, buffer, nBytes)
...
int xxIoctl (xxDev, requestCode, arg)
...

/*
 * xxInterrupt - interrupt service routine
 * *
 * Most drivers have routines that handle interrupts from the devices
 * serviced by the driver. These routines are connected to the interrupts
 * by calling intConnect (usually in xxDrv above). They can receive a
 * single argument, specified in the call to intConnect (see intLib).
 */

VOID xxInterrupt (arg)
...

6.9.1 Drivers

A driver for a non-block device generally implements the seven basic I/O functions—\texttt{creat()}, \texttt{remove()}, \texttt{open()}, \texttt{close()}, \texttt{read()}, \texttt{write()}, and \texttt{ioctl()}—for a particular kind of device. The driver implements these general functions with corresponding device-specific routines that are installed with \texttt{iosDrvInstall()}. Not all of the general I/O functions are implemented if they are not supported by a particular device. For example, \texttt{remove()} is usually not supported for devices that are not used with file systems. If any of the seven basic I/O routines are not implemented by a driver, a null function pointer should be used for the corresponding \texttt{iosDrvInstall()} parameter when the driver is installed. Any call to a routine that is not supported will then fail and return an \texttt{ENOTSUP} error.

Drivers may (optionally) allow tasks to wait for activity on multiple file descriptors. This functionality is implemented with the driver’s \texttt{ioctl()} routine; see \textit{Implementing select()}, p.392.
A driver for a block device interfaces with a file system, rather than directly with
the I/O system. The file system in turn implements most I/O functions. The driver
need only supply routines to read and write blocks, reset the device, perform I/O
control, and check device status. Drivers for block devices have a number of
special requirements that are discussed in 6.9.4 Block Device Drivers, p.399.

When an application invokes one of the basic I/O functions, the I/O system routes
the request to the appropriate routine of a specific driver, as described in the
following sections. The driver’s routine runs in the calling task’s context, as though
it were called directly from the application. Thus, the driver is free to use any
facilities normally available to tasks, including I/O to other devices. This means
that most drivers have to use some mechanism to provide mutual exclusion to
critical regions of code. The usual mechanism is the semaphore facility provided
in semLib.

In addition to the routines that implement the seven basic I/O functions, drivers
also have three other routines:

- An initialization routine that installs the driver in the I/O system, connects to
any interrupts used by the devices serviced by the driver, and performs any
necessary hardware initialization. This routine is typically named xxDrv().

- A routine to add devices that are to be serviced by the driver to the I/O system.
This routine is typically named xxDevCreate().

- Interrupt-level routines that are connected to the interrupts of the devices
serviced by the driver.

The Driver Table and Installing Drivers

The function of the I/O system is to route user I/O requests to the appropriate
routine of the appropriate driver. The I/O system does this by maintaining a table
that contains the address of each routine for each driver. Drivers are installed
dynamically by calling the I/O system internal routine iosDrvInstall(). The
arguments to this routine are the addresses of the seven I/O routines for the new
driver. The iosDrvInstall() routine enters these addresses in a free slot in the
driver table and returns the index of this slot. This index is known as the driver
number and is used subsequently to associate particular devices with the driver.

Null (0) addresses can be specified for any of the seven basic I/O routines that are
not supported by a device. For example, remove() is usually not supported for
non-file-system devices, and a null is specified for the driver’s remove function.
When a user I/O call matches a null driver routine, the call fails and an `ENOTSUP` error is returned.

VxWorks file systems (such as `dosFsLib`) contain their own entries in the driver table, which are created when the file system library is initialized.

**Example of Installing a Driver**

Figure 6-3 shows the actions taken by the example driver and by the I/O system when the initialization routine `xxDrv()` runs.

The driver calls `iosDrvInstall()`, specifying the addresses of the driver’s routines for the seven basic I/O functions. Then, the I/O system:

1. Locates the next available slot in the driver table, in this case slot 2.
2. Enters the addresses of the driver routines in the driver table.
3. Returns the slot number as the driver number of the newly installed driver.
6.9.2 Devices

Some drivers are capable of servicing many instances of a particular kind of device. For example, a single driver for a serial communications device can often handle many separate channels that differ only in a few parameters, such as device address.

In the VxWorks I/O system, devices are defined by a data structure called a device header (DEV_HDR). This data structure contains the device name string and the driver number for the driver that services this device. The device headers for all the devices in the system are kept in a memory-resident linked list called the device list. The device header is the initial part of a larger structure determined by the individual drivers. This larger structure, called a device descriptor, contains additional device-specific data such as device addresses, buffers, and semaphores.

The Device List and Adding Devices

Non-block devices are added to the I/O system dynamically by calling the internal I/O routine `iosDevAdd()`. The arguments to `iosDevAdd()` are the address of the device descriptor for the new device, the device’s name, and the driver number of the driver that services the device. The device descriptor specified by the driver can contain any necessary device-dependent information, as long as it begins with a device header. The driver does not need to fill in the device header, only the device-dependent information. The `iosDevAdd()` routine enters the specified device name and the driver number in the device header and adds it to the system device list.

To add a block device to the I/O system, call the device initialization routine for the file system required on that device—for example, `dosFsDevCreate()` . The device initialization routine then calls `iosDevAdd()` automatically.

The routine `iosDevFind()` can be used to locate the device structure (by obtaining a pointer to the DEV_HDR, which is the first member of that structure) and to verify that a device name exists in the table.
The following is an example using `iosDevFind()`:

```c
char * pTail; /* pointer to tail of devName */
char devName[6] = "DEV1:"; /* name of device */
DOS_VOLUME_DESC * pDosVolDesc; /* first member is DEV_HDR */
...
pDosVolDesc = iosDevFind(devName, (char**)&pTail);
if (NULL == pDosVolDesc)
    /* ERROR: device name does not exist and no default device */
else
    /*
    * pDosVolDesc is a valid DEV_HDR pointer
    * and pTail points to beginning of devName.
    * Check devName against pTail to determine if it is
    * the default name or the specified devName.
    */
```

**Example of Adding Devices**

In Figure 6-4, the example driver’s device creation routine `xxDevCreate()` adds devices to the I/O system by calling `iosDevAdd()`.
Deleting Devices

A device can be deleted with `iosDevDelete()` and the associated driver removed with `iosDrvRemove()`.

Note that a device-deletion operation causes the file descriptors that are open on the device to be *invalidated*, but not closed. The file descriptors can only be closed by an explicit act on the part of an application. If this were not the case, and file descriptors were closed automatically by the I/O system, the descriptors could be reassigned to new files while they were still being used by an application that was unaware of the deletion of the device. The new files could then be accessed unintentionally by an application attempting to use the files associated with the
deleted device, as well as by an application that was correctly using the new files. This would result in I/O errors and possible device data corruption.

Because the file descriptors of a device that has been deleted are invalid, any subsequent I/O calls that use them—except `close()`—will fail. The behavior of the I/O routines in this regard is as follows:

- `close()` releases the file descriptor at I/O system level and the driver close routine is not called.
- `read()`, `write()`, and `ioctl()` fail with error ENXIO (no such device or address).
- While `open()`, `remove()`, and `create()` do not take an open file descriptor as input, they fail because the device name is no longer in the device list.

Note that even if a device is deleted and immediately added again with the same device name, the file descriptors that were invalidated with the deletion are not restored to valid status. The behavior of the I/O calls on the associated file descriptors is the same as if the device had not been added again.

Applications that are likely to encounter device deletion should be sure to check for ENXIO errors from `read()`, `write()`, and `ioctl()` calls, and to then close the relevant file descriptors.

**Using Callback Routines to Manage Device Deletion**

For situations in which devices are dynamically installed and deleted, the `iosDevDelCallback()` routine provides the means for calling a post-deletion handler after all driver invocations are exited.

A common use of a device deletion callback is to prevent a race condition that would result from a device descriptor being deleted in one thread of execution while it was still being used in another.

A device descriptor belongs to an application, and the I/O system cannot control its creation and release. It is a user data structure with `DEV_HDR` data structure embedded at the front of it, followed by any specific member of the device. Its pointer can be used to pass into any `iosDevXyz()` routine as a `DEV_HDR` pointer, or used as the device descriptor for user device handling.

When a device is deleted, an application should not immediately release the device descriptor memory after `iosDevDelete()` and `iosDrvRemove()` calls because a driver invocation of the deleted device might still be in process. If the device descriptor is deleted while it is still in use by a driver routine, serious errors could occur.
For example, the following would produce a race condition: task A invokes the
driver routine \texttt{xyzOpen()} by a call to \texttt{open()} and the \texttt{xyzOpen()} call does not
return before task B deletes the device and releases the device descriptor.

However, if descriptor release is not performed by task B, but by a callback
function installed with \texttt{iosDevDelCallback()}, then the release occurs only after
task A’s invocation of the driver routine has finished.

A device callback routine is called immediately when a device is deleted with
\texttt{iosDevDelete()} or \texttt{iosDrvRemove()} as long as no invocations of the associated
driver are operative (that is, the device driver reference counter is zero).
Otherwise, the callback routine is not executed until the last driver call exits (and
the device driver reference counter reaches zero).

A device deletion callback routine should be called with only one parameter, the
pointer to the \texttt{DEV_HDR} data structure of the device in question. For example:
\begin{verbatim}
    devDeleteCallback(pDevHdr)
\end{verbatim}

The callback should be installed with \texttt{iosDevDelCallback()} after the
\texttt{iosDevAdd()} call.

The following code fragments illustrate callback use. The file system device
descriptor \texttt{pVolDesc} is installed into the I/O device list. Its device deletion
callback, \texttt{fsVolDescRelease()} performs the post-deletion processing, including
releasing memory allocated for the device volume descriptor.
\begin{verbatim}
void fsVolDescRelease
{
    FS_VOLUME_DESC * pVolDesc
}
{

    free (pVolDesc->pFsemList);
    free (pVolDesc->pFhdlList);
    free (pVolDesc->pFdList);

    . . . . . .
}

STATUS fsDevCreate
{
    char * pDevName, /* device name */
    device_t device, /* underlying block device */
    u_int maxFiles, /* max no. of simultaneously open files */
    u_int devCreateOptions /* write option & volume integrity */

    {
        FS_VOLUME_DESC *pVolDesc = NULL; /* volume descriptor ptr */

        pVolDesc = (FS_VOLUME_DESC *) malloc (sizeof (*pVolDesc));
        pVolDesc->device = device;
\end{verbatim}
if (iosDevAdd((void *)pVolDesc, pDevName, fsDrvNum ) == ERROR) {
    pVolDesc->magic = NONE;
    goto error_iosadd;
}
/* Device deletion callback installed to release memory resource. */
iosDevDelCallback((DEV_HDR *) pVolDesc, (FUNCPTR) fsVolDescRelease);

STATUS fsDevDelete
{
    FS_VOLUME_DESC *pVolDesc /* pointer to volume descriptor */
    {
        /* Delete the file system device from I/O device list. Callback
        * fsVolDescRelease will be called from now on at a *
        * safe time by I/O system. */
        iosDevDelete((DEV_HDR *) pVolDesc);

        The application should check the error returned by a deleted device, as follows:
        if (write (fd, (char *)buffer, nbytes) == ERROR)
        {
            if (errno == ENXIO)
                /* Device is deleted. fd must be closed by application. */
                close(fd);
            else
                /* write failure due to other reason. Do some error dealing. */
        }
}

6.9.3 File Descriptors

Several file descriptors can be open to a single device at one time. A device driver can maintain additional information associated with a file descriptor beyond the I/O system’s device information. In particular, devices on which multiple files can be open at one time have file-specific information (for example, file offset) associated with each file descriptor. You can also have several file descriptors open to a non-block device, such as a tty; typically there is no additional information, and thus writing on any of the file descriptors produces identical results.
File Descriptor Table

Files are opened with `open()` or `creat()`. The I/O system searches the device list for a device name that matches the filename (or an initial substring) specified by the caller. If a match is found, the I/O system uses the driver number contained in the corresponding device header to locate and call the driver’s open routine in the driver table.

The I/O system must establish an association between the file descriptor used by the caller in subsequent I/O calls, and the driver that services it. Additionally, the driver must associate some data structure per descriptor. In the case of non-block devices, this is usually the device descriptor that was located by the I/O system.

The I/O system maintains these associations in a table called the file descriptor table. This table contains the driver number and an additional driver-determined 4-byte value. The driver value is the internal descriptor returned by the driver’s open routine, and can be any value the driver requires to identify the file. In subsequent calls to the driver’s other I/O functions (`read()`, `write()`, `ioctl()`, and `close()`), this value is supplied to the driver in place of the file descriptor in the application-level I/O call.

Example of Opening a File

In Figure 6-5 and Figure 6-6, a user calls `open()` to open the file `/xx0`. The I/O system takes the following series of actions:

1. It searches the device list for a device name that matches the specified filename (or an initial substring). In this case, a complete device name matches.

2. It reserves a slot in the file descriptor table and creates a new file descriptor object, which is used if the open is successful.

3. It then looks up the address of the driver’s open routine, `xxOpen()`, and calls that routine. Note that the arguments to `xxOpen()` are transformed by the I/O system from the user’s original arguments to `open()`. The first argument to `xxOpen()` is a pointer to the device descriptor the I/O system located in the full filename search. The next parameter is the remainder of the filename specified by the user, after removing the initial substring that matched the device name. In this case, because the device name matched the entire filename, the remainder passed to the driver is a null string. The driver is free to interpret this remainder in any way it wants. In the case of block devices, this remainder is the name of a file on the device. In the case of non-block devices like this one, it is usually an error for the remainder to be anything but
the null string. The third parameter is the file access flag, in this case \texttt{O_RDONLY}; that is, the file is opened for reading only. The last parameter is the mode, as passed to the original \texttt{open()} routine.

4. It executes $xx$\texttt{Open()}, which returns a value that subsequently identifies the newly opened file. In this case, the value is the pointer to the device descriptor. This value is supplied to the driver in subsequent I/O calls that refer to the file being opened. Note that if the driver returns only the device descriptor, the driver cannot distinguish multiple files opened to the same device. In the case of non-block device drivers, this is usually appropriate.

5. The I/O system then enters the driver number and the value returned by $xx$\texttt{Open()} in the new file descriptor object.

   Again, the value entered in the file descriptor object has meaning only for the driver, and is arbitrary as far as the I/O system is concerned.

6. Finally, it returns to the user the index of the slot in the file descriptor table, in this case 3.
Figure 6-5  Example: Call to I/O Routine open() [Part 1]

USER CALL:

```c
fd = open("/xx0", O_RDONLY, 0);
```

DRIVER CALL:

```c
xxdev = xxOpen(xxdev, "", O_RDONLY, 0);
```

[1] I/O system finds name in device list.

[2] I/O system reserves a slot in the file descriptor table.

[3] I/O system calls driver’s open routine with pointer to device descriptor.

FILE DESCRIPTOR TABLE:

```
<table>
<thead>
<tr>
<th>pDevHdr value</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
</tr>
<tr>
<td>1</td>
</tr>
<tr>
<td>2</td>
</tr>
<tr>
<td>3</td>
</tr>
<tr>
<td>4</td>
</tr>
</tbody>
</table>
```

DEVICE LIST:

```
<table>
<thead>
<tr>
<th>device-dependent data</th>
</tr>
</thead>
<tbody>
<tr>
<td>/dk0/</td>
</tr>
<tr>
<td>1</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>device-dependent data</th>
</tr>
</thead>
<tbody>
<tr>
<td>/xx0/</td>
</tr>
<tr>
<td>2</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>device-dependent data</th>
</tr>
</thead>
<tbody>
<tr>
<td>/xx1/</td>
</tr>
<tr>
<td>2</td>
</tr>
</tbody>
</table>
```

DRIVER TABLE:

```
<table>
<thead>
<tr>
<th>create</th>
<th>delete</th>
<th>open</th>
<th>close</th>
<th>read</th>
<th>write</th>
<th>ioctl</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
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<td></td>
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<td></td>
<td></td>
</tr>
<tr>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>
```

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Figure 6-6  Example: Call to I/O Routine open( ) [Part 2]

USER CALL:

```c
fd = open ("/xx0", O_RDONLY, 0);
```

DRIVER CALL:

```c
xxdev = xxOpen (xxdev, ",", O_RDONLY, 0);
```


[5] I/O system enters driver number and identifying value in reserved table slot.

[4] Driver returns any identifying value, in this case the pointer to the device descriptor.

FILE DESCRIPTOR TABLE:

```
<TABLE>
<table>
<thead>
<tr>
<th>drvnum</th>
<th>value</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td></td>
</tr>
<tr>
<td>1</td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>xxdev</td>
</tr>
<tr>
<td>3</td>
<td></td>
</tr>
<tr>
<td>4</td>
<td></td>
</tr>
</tbody>
</table>
</TABLE>
```

DEVICE LIST:

```
<TABLE>
<table>
<thead>
<tr>
<th>Device</th>
</tr>
</thead>
<tbody>
<tr>
<td>/dk0</td>
</tr>
<tr>
<td>/xx0</td>
</tr>
<tr>
<td>/xx1</td>
</tr>
<tr>
<td>1</td>
</tr>
<tr>
<td>2</td>
</tr>
</tbody>
</table>
</TABLE>
```

DRIVER TABLE:

```
<TABLE>
<table>
<thead>
<tr>
<th>create</th>
<th>delete</th>
<th>open</th>
<th>close</th>
<th>read</th>
<th>write</th>
<th>ioctl</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
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</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>
</TABLE>
```
Example of Reading Data from the File

In Figure 6-7, the user calls read() to obtain input data from the file. The specified file descriptor is the index into the file descriptor table for this file. The I/O system uses the driver number contained in the table to locate the driver’s read routine, xxRead(). The I/O system calls xxRead(), passing it the identifying value in the file descriptor table that was returned by the driver’s open routine, xxOpen(). Again, in this case the value is the pointer to the device descriptor. The driver’s read routine then does whatever is necessary to read data from the device. The process for user calls to write() and ioctl() follow the same procedure.
I/O system transforms the user’s I/O routine calls into driver routine calls replacing the `fd` with the value returned by the driver’s `open` routine, `xxOpen()`.

**Figure 6-7  Example: Call to I/O Routine read( )**

**USER CALL:**

```c
n = read (fd, buf, len);
```

**DRIVER CALL:**

```c
n = xxRead (xxdev, buf, len);
```

**FILE DESCRIPTOR TABLE:**

<table>
<thead>
<tr>
<th>drvnum</th>
<th>value</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td></td>
</tr>
<tr>
<td>1</td>
<td></td>
</tr>
<tr>
<td>2</td>
<td></td>
</tr>
<tr>
<td>3</td>
<td>2</td>
</tr>
<tr>
<td>4</td>
<td></td>
</tr>
</tbody>
</table>

**DEVICE LIST:**

- `/dk0/`
- `/xx0`
- `/xx1`

- 1
- 2

- device-dependent data

**DRIVER TABLE:**

<table>
<thead>
<tr>
<th></th>
<th>create</th>
<th>remove</th>
<th>open</th>
<th>close</th>
<th>read</th>
<th>write</th>
<th>ioctl</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>x</td>
<td>x</td>
<td>x</td>
<td>x</td>
<td>x</td>
<td>x</td>
<td>x</td>
</tr>
<tr>
<td>1</td>
<td>x</td>
<td>x</td>
<td>x</td>
<td>x</td>
<td>x</td>
<td>x</td>
<td>x</td>
</tr>
<tr>
<td>2</td>
<td>x</td>
<td>x</td>
<td>x</td>
<td>x</td>
<td>x</td>
<td>x</td>
<td>x</td>
</tr>
<tr>
<td>3</td>
<td>x</td>
<td>x</td>
<td>x</td>
<td>x</td>
<td>x</td>
<td>x</td>
<td>x</td>
</tr>
<tr>
<td>4</td>
<td>x</td>
<td>x</td>
<td>x</td>
<td>x</td>
<td>x</td>
<td>x</td>
<td>x</td>
</tr>
</tbody>
</table>
Example of Closing a File

The user terminates the use of a file by calling `close()` As in the case of `read()`, the I/O system uses the driver number contained in the file descriptor table to locate the driver’s close routine. In the example driver, no close routine is specified; thus no driver routines are called. Instead, the I/O system marks the slot in the file descriptor table as being available. Any subsequent references to that file descriptor cause an error. Subsequent calls to `open()` can reuse that slot.

Implementing select()

Supporting `select()` in your driver allows tasks to wait for input from multiple devices or to specify a maximum time to wait for the device to become ready for I/O. Writing a driver that supports `select()` is simple, because most of the functionality is provided in `selectLib`. You might want your driver to support `select()` if any of the following is appropriate for the device:

- The tasks want to specify a timeout to wait for I/O from the device. For example, a task might want to time out on a UDP socket if the packet never arrives.
- The driver supports multiple devices, and the tasks want to wait simultaneously for any number of them. For example, multiple pipes might be used for different data priorities.
- The tasks want to wait for I/O from the device while also waiting for I/O from another device. For example, a server task might use both pipes and sockets.

To implement `select()`, the driver must keep a list of tasks waiting for device activity. When the device becomes ready, the driver unblocks all the tasks waiting on the device.

For a device driver to support `select()`, it must declare a `SEL_WAKEUP_LIST` structure (typically declared as part of the device descriptor structure) and initialize it by calling `selWakeupListInit()`. This is done in the driver’s `xxDevCreate()` routine. When a task calls `select()`, `selectLib` calls the driver’s `ioctl()` routine with the function FIOSELECT or FIONUSELECT. If `ioctl()` is called with FIOSELECT, the driver must do the following:

1. Add the `SEL_WAKEUP_NODE` (provided as the third argument of `ioctl()`) to the `SEL_WAKEUP_LIST` by calling `selNodeAdd()`.
2. Use the routine `selWakeupType()` to check whether the task is waiting for data to read from the device (SELREAD) or if the device is ready to be written (SELWRITE).

3. If the device is ready (for reading or writing as determined by `selWakeupType()`), the driver calls the routine `selWakeup()` to make sure that the `select()` call in the task does not pend. This avoids the situation where the task is blocked but the device is ready.

If `ioctl()` is called with FIOUNSELECT, the driver calls `selNodeDelete()` to remove the provided SEL_WAKEUP_NODE from the wakeup list.

When the device becomes available, `selWakeupAll()` is used to unblock all the tasks waiting on this device. Although this typically occurs in the driver’s ISR, it can also occur elsewhere. For example, a pipe driver might call `selWakeupAll()` from its `xxRead()` routine to unblock all the tasks waiting to write, now that there is room in the pipe to store the data. Similarly the pipe’s `xxWrite()` routine might call `selWakeupAll()` to unblock all the tasks waiting to read, now that there is data in the pipe.
Example 6-9  Driver Code Using the Select Facility

/* This code fragment shows how a driver might support select(). In this
example, the driver unblocks tasks waiting for the device to become ready
in its interrupt service routine. */

/* myDrvLib.h - header file for driver */

typedef struct /* MY_DEV */
{
  DEV_HDR devHdr; /* device header */
  BOOL myDrvDataAvailable; /* data is available to read */
  BOOL myDrvRdyForWriting; /* device is ready to write */
  SEL_WAKEUP_LIST selWakeupList; /* list of tasks pended in select */
} MY_DEV;

/* myDrv.c - code fragments for supporting select() in a driver */

#include <vxWorks.h>
#include <selectLib.h>

/* First create and initialize the device */

STATUS myDrvDevCreate
{
      char * name, /* name of device to create */

      MY_DEV * pMyDrvDev; /* pointer to device descriptor*/
      ... additional driver code ... 

      /* allocate memory for MY_DEV */
pMyDrvDev = (MY_DEV *) malloc (sizeof MY_DEV);
      ... additional driver code ... 

      /* initialize MY_DEV */
pMyDrvDev->myDrvDataAvailable=FALSE
pMyDrvDev->myDrvRdyForWriting=FALSE

      /* initialize wakeup list */
      selWakeupListInit (&pMyDrvDev->selWakeupList);
      ... additional driver code ... 

    }

    /* ioctl function to request reading or writing */

    STATUS myDrvIoctl
    {
      MY_DEV * pMyDrvDev, /* pointer to device descriptor */
int request, /* ioctl function */
int arg /* where to send answer */
{
    ... additional driver code ...

    switch (request)
    {
        ... additional driver code ...

        case FIOSELECT:
            /* add node to wakeup list */
            selNodeAdd (&pMyDrvDev->selWakeupList, (SEL_WAKEUP_NODE *) arg);

            if (selWakeupType ((SEL_WAKEUP_NODE *) arg) == SELREAD
                && pMyDrvDev->myDrvDataAvailable)
                { /* data available, make sure task does not pend */
                selWakeup ((SEL_WAKEUP_NODE *) arg);
            }

            if (selWakeupType ((SEL_WAKEUP_NODE *) arg) == SELWRITE
                && pMyDrvDev->myDrvRdyForWriting)
                { /* device ready for writing, make sure task does not pend */
                selWakeup ((SEL_WAKEUP_NODE *) arg);
            }

            break;

        case FIONUNSELECT:
            /* delete node from wakeup list */
            selNodeDelete (&pMyDrvDev->selWakeupList, (SEL_WAKEUP_NODE *) arg);

            break;

            ... additional driver code ...

    }

    /* code that actually uses the select() function to read or write */
    void myDrvIsr
    {
        MY_DEV * pMyDrvDev;
    }

    ... additional driver code ...

    /* if there is data available to read, wake up all pending tasks */
    if (pMyDrvDev->myDrvDataAvailable)
        selWakeupAll (&pMyDrvDev->selWakeupList, SELREAD);
/* if the device is ready to write, wake up all pending tasks */

if (pMyDrvDev->myDrvRdyForWriting)
    selWakeupAll (&pMyDrvDev->selWakeupList, SELWRITE);

**Cache Coherency**

Drivers written for boards with caches must guarantee *cache coherency*. Cache coherency means data in the cache must be in sync, or coherent, with data in RAM. The data cache and RAM can get out of sync any time there is asynchronous access to RAM (for example, DMA device access or VMEbus access). Data caches are used to increase performance by reducing the number of memory accesses. Figure 6-8 shows the relationships between the CPU, data cache, RAM, and a DMA device.

Data caches can operate in one of two modes: *write-through* and *copyback*. Write-through mode writes data to both the cache and RAM; this guarantees cache coherency on output but not input. Copyback mode writes the data only to the cache; this makes cache coherency an issue for both input and output of data.

![Figure 6-8 Cache Coherency](image)

If a CPU writes data to RAM that is destined for a DMA device, the data can first be written to the data cache. When the DMA device transfers the data from RAM, there is no guarantee that the data in RAM was updated with the data in the cache. Thus, the data output to the device may not be the most recent—the new data may still be sitting in the cache. This data incoherence can be solved by making sure the data cache is flushed to RAM before the data is transferred to the DMA device.

If a CPU reads data from RAM that originated from a DMA device, the data read can be from the cache buffer (if the cache buffer for this data is not marked invalid)
and not the data just transferred from the device to RAM. The solution to this data
incoherence is to make sure that the cache buffer is marked invalid so that the data
is read from RAM and not from the cache.

Drivers can solve the cache coherency problem either by allocating cache-safe
buffers (buffers that are marked non-cacheable) or flushing and invalidating cache
entries any time the data is written to or read from the device. Allocating
cache-safe buffers is useful for static buffers; however, this typically requires MMU
support. Non-cacheable buffers that are allocated and freed frequently (dynamic
buffers) can result in large amounts of memory being marked non-cacheable. An
alternative to using non-cacheable buffers is to flush and invalidate cache entries
manually; this allows dynamic buffers to be kept coherent.

The routines `cacheFlush()` and `cacheInvalidate()` are used to manually flush and
invalidate cache buffers. Before a device reads the data, flush the data from the
cache to RAM using `cacheFlush()` to ensure the device reads current data. After
the device has written the data into RAM, invalidate the cache entry with `cacheInvalidate()`.
This guarantees that when the data is read by the CPU, the

Example 6-10 DMA Transfer Routine

```c
/* This is a sample DMA transfer routine. Before programming the device
 * to output the data to the device, it flushes the cache by calling
 * cacheFlush(). On a read, after the device has transferred the data,
 * the cache entry must be invalidated using cacheInvalidate().
 */

#include <vxWorks.h>
#include <cacheLib.h>
#include <fcntl.h>
#include "example.h"

void exampleDmaTransfer /* 1 = READ, 0 = WRITE */
(  
  UINT8 *pExampleBuf,
  int exampleBufLen,
  int xferDirection
)  
{
  if (xferDirection == 1)
    {  
    myDevToBuf (pExampleBuf);
    cacheInvalidate (DATA_CACHE, pExampleBuf, exampleBufLen);
  }  
}  
```
else
{
    cacheFlush (DATA_CACHE, pExampleBuf, exampleBufLen);
    myBufToDev (pExampleBuf);
}

It is possible to make a driver more efficient by combining cache-safe buffer allocation and cache-entry flushing or invalidation. The idea is to flush or invalidate a cache entry only when absolutely necessary. To address issues of cache coherency for static buffers, use *cacheDmaMalloc*(). This routine initializes a *CACHE_FUNCS* structure (defined in *cacheLib.h*) to point to flush and invalidate routines that can be used to keep the cache coherent.

The macros *CACHE_DMA_FLUSH* and *CACHE_DMA_INVALIDATE* use this structure to optimize the calling of the flush and invalidate routines. If the corresponding function pointer in the *CACHE_FUNCS* structure is NULL, no unnecessary flush/invalidate routines are called because it is assumed that the buffer is cache coherent (hence it is not necessary to flush/invalidate the cache entry manually).

The driver code uses a virtual address and the device uses a physical address. Whenever a device is given an address, it must be a physical address. Whenever the driver accesses the memory, it must use the virtual address.

The device driver should use *CACHE_DMA_VIRT_TO_PHYS* to translate a virtual address to a physical address before passing it to the device. It may also use *CACHE_DMA_PHYS_TO_VIRT* to translate a physical address to a virtual one, but this process is time-consuming and non-deterministic, and should be avoided whenever possible.

**Example 6-11 Address-Translation Driver**

/* The following code is an example of a driver that performs address translations. It attempts to allocate a cache-safe buffer, fill it, and then write it out to the device. It uses *CACHE_DMA_FLUSH* to make sure the data is current. The driver then reads in new data and uses *CACHE_DMA_INVALIDATE* to guarantee cache coherency. */

#include <vxWorks.h>
#include <cacheLib.h>
#include "myExample.h"
STATUS myDmaExample (void)
{
    void * pMyBuf;
    void * pPhysAddr;
/* allocate cache safe buffers if possible */
if ((pMyBuf = cacheDmaMalloc (MY_BUF_SIZE)) == NULL)
    return (ERROR);

... fill buffer with useful information ...

/* flush cache entry before data is written to device */
CACHE_DMA_FLUSH (pMyBuf, MY_BUF_SIZE);

/* convert virtual address to physical */
pPhysAddr = CACHE_DMA_VIRT_TO_PHYS (pMyBuf);

/* program device to read data from RAM */
myBufToDev (pPhysAddr);
... wait for DMA to complete ...
... ready to read new data ...

/* program device to write data to RAM */
myDevToBuf (pPhysAddr);
... wait for transfer to complete ...

/* convert physical to virtual address */
pMyBuf = CACHE_DMA_PHYS_TO_VIRT (pPhysAddr);

/* invalidate buffer */
CACHE_DMA_INVALIDATE (pMyBuf, MY_BUF_SIZE);
... use data ...

/* when done free memory */
if (cacheDmaFree (pMyBuf) == ERROR)
    return (ERROR);
return (OK);

6.9.4 Block Device Drivers

In VxWorks, block devices have a different interface than other I/O devices. Rather than interacting directly with the I/O system, the I/O activity of block device drivers is mediated by the XBD facility and a file system. XBD provides a standard interface for block drivers on the one hand, and for file systems on the other. For an illustration of the relationship between the I/O system, file systems, XBD, and drivers, see Figure 6-2. For information about XBD, see 6.7.7 Extended Block Device Facilities: XBD, p.356.
6.10 PCMCIA

A PCMCIA card can be plugged into notebook computers to connect devices such as modems and external hard drives. VxWorks provides PCMCIA facilities for pcPentium, pcPentium2, and pcPentium3 BSPs and PCMCIA drivers that allow VxWorks running on these targets to support PCMCIA hardware.

PCMCIA support is at the PCMCIA Release 2.1 level. It does not include socket services or card services, which are not required by VxWorks. It does include chip drivers and libraries. The PCMCIA libraries and drivers are also available in source code form for VxWorks systems based on CPU architectures other than Intel Pentium.

To include PCMCIA support in your system, configure VxWorks with the INCLUDE_PCMCIA component. For information about PCMCIA facilities, see the API references for pcmciaLib and pcmciaShow.

6.11 Peripheral Component Interconnect: PCI

Peripheral Component Interconnect (PCI) is a bus standard for connecting peripherals to a PC, and is used in Pentium systems, among others. PCI includes buffers that de-couple the CPU from relatively slow peripherals, allowing them to operate asynchronously.

For information about PCI facilities, see the API references for pciAutoConfigLib, pciConfigLib, pciInitLib, and pciConfigShow.

---

2. PCMCIA stands for Personal Computer Memory Card International Association, and refers to both the association and the standards that it has developed.
Local File Systems

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7.3 Highly Reliable File System: HRFS 408
7.4 MS-DOS-Compatible File System: dosFs 420
7.5 Raw File System: rawFs 445
7.6 CD-ROM File System: cdromFs 449
7.7 Read-Only Memory File System: ROMFS 455
7.8 Target Server File System: TSFS 457
7.1 Introduction

VxWorks provides a variety of file systems that are suitable for different types of applications. The file systems can be used simultaneously, and in most cases in multiple instances, for a single VxWorks system.

Most VxWorks file systems rely on the extended block device (XBD) facility for a standard I/O interface between the file system and device drivers. This standard interface allows you to write your own file system for VxWorks, and freely mix file systems and device drivers.

File systems used for removable devices make use of the file system monitor for automatic detection of device insertion and instantiation of the appropriate file system on the device.

The relationship between applications, file systems, I/O facilities, device drivers and hardware devices is illustrated in Figure 7-1. Note that this illustration is relevant for the HRFS, dosFs, rawFs, and cdromFs file systems. The dotted line indicates the elements that need to be configured and instantiated to create a specific, functional run-time file system.
7 Local File Systems
7.1 Introduction

Figure 7-1  File Systems in a VxWorks System

- Application
- I/O System
- File System
  - HRFS, dosFs, rawFs, cdromFs
- XBD Facility
  - XBD Device
- Block Device
  - SCSI, ATA, RAM disk, Floppy, TrueFFS, and so on
- Hardware
This chapter discusses the file system monitor and the following VxWorks file systems, describing how they are organized, configured, and used:

- **HRFS**
  A transactional file system designed for real-time use of block devices (disks) and POSIX compliant. Can be used on flash memory in conjunction with TrueFFS and the XBD block wrapper component.
  See 7.3 Highly Reliable File System: HRFS, p. 408.

- **dosFs**
  Designed for real-time use of block devices (disks), and compatible with the MS-DOS file system. Can be used with flash memory in conjunction with the TrueFFS. Can also be used with the transaction-based reliable file system (TRFS) facility. See 7.4 MS-DOS-Compatible File System: dosFs, p. 420.

- **rawFS**
  Provides a simple raw file system that treats an entire disk as a single large file. See 7.5 Raw File System: rawFs, p. 445.

- **cdromFs**
  Allows applications to read data from CD-ROMs formatted according to the ISO 9660 standard file system. See 7.6 CD-ROM File System: cdromFs, p. 449.

- **ROMFS**
  Designed for bundling applications and other files with a VxWorks system image. No storage media is required beyond that used for the VxWorks boot image. See 7.7 Read-Only Memory File System: ROMFS, p. 455.

- **TSFS**
  Uses the host target server to provide the target with access to files on the host system. See 7.8 Target Server File System: TSFS, p. 457.

For information about the XBD facility, see 6.7.7 Extended Block Device Facilities: XBD, p. 356).

**File Systems and Flash Memory**

VxWorks can be configured with file-system support for flash memory devices using TrueFFS and the dosFs or HRFS file system. For more information, see 7.4 MS-DOS-Compatible File System: dosFs, p. 420 and 8. Flash File System Support with TrueFFS.
7.2 File System Monitor

The file system monitor provides for automatic detection of device insertion, and instantiation of the appropriate file system on the device. The monitor is required for all file systems that are used with the extended block device (XBD) I/O facility. It is provided with the INCLUDE_FS_MONITOR component.

The file systems that require both the XBD and the file system monitor components are HRFS, dosFs, rawFs, and cdromFs.

The process by which devices are detected, and file systems created, is as follows:

1. When file systems are initialized (at boot time), they register probe routines and instantiation routines with the file system monitor.

2. When a device is inserted or detected (for example, when a driver is initialized or media is inserted into an existing XBD device) the XBD device associated with it generates a primary insertion event. (See Device Insertion Events, p.406.)

3. In response to the primary insertion event, the file system monitor creates an XBD partition manager if the device can support partitions.

4. If the partition manager finds partitions on the device, it creates an XBD device for each partition; and whether or not partitions are found, the manager generates a secondary insertion event.

5. When the file system monitor receives a secondary event, all the registered file system probe functions are run.

6. When a file system’s probe routine returns success, that file system’s instantiation routine is executed. If none of the probes are successful, or if the file system instantiation routine fails, a rawFs file system is created on the device by default.
When a device is removed, the following occurs:

1. The XBD device detects the removal of the hardware device associated with it and generates a removal event.
2. The XBD device removes itself, freeing all its resources.
3. The file system associated with the XBD device removes itself from core I/O, invalidating its file handles.
4. The file system removes itself, freeing all its resources.

Device Insertion Events

The types of device insertion events to which the file system monitor responds are described in more detail below.

XBD Primary Insertion Event

An XBD device generates a primary insertion event when media that can support partitions is inserted (that is, if a partition table is found). In response, the file system monitor creates a partition manager, which in turn generates secondary insertion events for each partition that it finds on the media (see below).

Note that an XBD device based on the XBD wrapper component (INCLUDE_XBD_BLK_DEV) always generates a primary insertion event, regardless of the media. Wrapper XBD devices are not associated with a particular type of device, and therefore cannot know if the device might include partitions. For example, the device could be a hard disk—for which partitions are expected—or it could be a floppy device.

Note also that a RAM disk device can generate a primary insertion event, depending on the parameters used when it was created. See the XbdRamDisk API reference entry for more information (also see 6.7.5 Non-NFS Network Devices, p.354).

XBD Secondary Insertion Event

A secondary insertion event is generated by either by an XBD device whose media does not support partitions, or by an XBD partition manager. The secondary event signals the file system manager to run the probe routines that identify the file system on the device. If a probe routine returns OK, the associated file system creation routine is executed. If none of the probe routines identifies a file system, or if a file system creation routine fails, the rawFs file system is created by default.
XBD Soft Insert Event
Unlike the other events, an XBD soft insert event is produced by application directive rather than by physical media being swapped. When \texttt{ioctl()} is called with the \texttt{XBD\_SOFT\_EJECT} command it tells the file system manager that the current file system has been removed, and that a rawFs file system should be created. This call causes the system to bypass the usual file system detection operations, and ensures that rawFs is instantiated in place of the current file system.

XBD Name Mapping Facility
The file system monitor name mapping facility allows XBD names to be mapped to a more suitable name. It's primary use is for the partition manager which appends :x to the base \texttt{xbd} name when it detects a partition. By using the \texttt{fsm} name facility you can map the partition names to something more useful. For example, the floppy drive configlette uses the name component to map the supplied floppy name plus the :0 the partition manager will add to /fd\texttt{x}. Where \texttt{x} represents the floppy drive number. If this was not done one would see usual device names in the devs list. For more information see the API references for \texttt{fsmNameInstall()}, \texttt{fsmNameMap()}, and \texttt{fsmNameUninstall()}. 
7.3 Highly Reliable File System: HRFS

The Highly Reliable File System (HRFS) is a transactional file system for real-time systems. The primary features of the file system are:

- Fault tolerance. The file system is never in an inconsistent state, and is therefore able to recover quickly from unexpected loses of power.
- Transactional operations on a file basis, rather than the whole disk.
- Hierarchical file and directory system, allowing for efficient organization of files on a volume.
- Compatibility with a widely available storage devices.
- POSIX conformance.

For more information about the HRFS libraries see the VxWorks API references for hrfsFormatLib, hrFsLib, and hrfsChkDskLib.

HRFS and Flash Memory

For information about using HRFS with flash memory, see 8. Flash File System Support with TrueFFS.

7.3.1 Configuring VxWorks for HRFS

To include HRFS support in VxWorks, configure the kernel with the appropriate required and optional components.

Required Components

Either the INCLUDE_HRFS or the INCLUDE_HRFS_READONLY component is required. As its name indicates, the latter is a read-only version of the main HRFS component. The libraries it provides are smaller as it provides no facilities for disk modifications.

In addition, you need to include the appropriate component for your block device; for example, INCLUDE_SCSI or INCLUDE_ATA.

Optional HRFS Components

The INCLUDE_HRFS_FORMAT component (HRFS formatter) and the INCLUDE_HRFS_CHKDSK (HRFS consistency checker) are optional components.
Optional XBD Components

Optional XBD components are:

- INCLUDE_XBD_PART_LIB: disk partitioning facilities
- INCLUDE_XBD_BLK_DEV: XBD wrapper component for device drivers that have not been ported to XBD.
- INCLUDE_XBD_RAMDRV: RAM disk facility

⚠️ CAUTION: If you are using a device for which the driver has not been ported to XBD, you must use the INCLUDE_XBD_BLK_DEV wrapper component in addition to INCLUDE_XBD. See XBD Block Device Wrapper Component, p.358 for more information.

For information about the XBD facility, see 6.7.7 Extended Block Device Facilities: XBD, p.356).

7.3.2 Creating an HRFS File System

This section describes the process of creating an HRFS file system. It first provides a summary overview and then a detailed description of each step. 7.3.3 HRFS and RAM Disk Examples, p.411 provides examples of these steps.

Overview of HRFS File System Creation

For information operating system configuration, see 7.3.1 Configuring VxWorks for HRFS, p.408. Note that the file system is initialized automatically at boot time.

The steps involved in creating an HRFS file system are as follows:

1. Create the appropriate block device. See Step 1: Create a Block Device, p.410.
4. If you are not using pre-formatted disks, format the volumes. See Step 5: Formatting the Volume, p.411.
HRFS File System Creation Steps

Before any other operations can be performed, the HRFS file system library, hrFsLib, must be initialized. This happens automatically, triggered by the required HRFS components that were included in the system.

Initializing HRFS involves the creation of a Vnode layer. HRFS installs an number of internal Vnode operators into the Vnode layer. The Vnode layer invokes iosDrvInstall() when media is detected, which adds the driver to the I/O driver table. The driver number assigned to Vnodes and therefore HRFS, is recorded in a global variable, vnodeAffDriverNumber. The table specifies entry points for the Vnode file operations that are accessing devices using HRFS.

Step 1: Create a Block Device
Create one or more block devices. To create the device, call the routine appropriate for that device driver. The format for this routine is xxxDevCreate() where xxx represents the device driver type; for example, scsiBlkDevCreate() or ataDevCreate().

The driver routine returns a pointer to a block device descriptor structure, BLK_DEV. This structure describes the physical attributes of the device and specifies the routines that the device driver provides. The pointer returned is used to create an XBD device.

For more information on block devices, see 6.9.4 Block Device Drivers, p.399.

Step 2: Create an XBD Device
Create an XBD device for each block device using xbdBlkDevCreate().

After the XBD device is created the device is automatically probed for a file system and partitions. If a disk is already formatted, the disk is mounted. If a a file system is found, it is mounted. The file system may not be HRFS, so formatting may be required.

Step 3: Create a dosFs File System
If a dosFs file system is not detected on a device you can create a new file system programmatically or interactively. If no file system is detected, a rawFs file system is instantiated automatically.
Step 4: Create Partitions

If you have included the INCLUDE_XBD_PART_LIB component in your system, you can create partitions on a disk and mount volumes atop the partitions. Use the xbdCreatePartition() routine to create partitions.

This step should only be performed once, when the disk is first initialized. If partitions are already written to the disk, this step should not be performed since it destroys data.

Step 5: Formatting the Volume

If you are using unformatted disk or wish to replace the current file system on the disk, format the disk by calling hrFsFormat(). For more information, see the VxWorks API reference for this routine.

⚠️ **CAUTION:** Reformatting a disk destroys any data that may be on it.

### 7.3.3 HRFS and RAM Disk Examples

This section provides examples of the steps discussed in the preceding section. These examples use a variety of configurations and device types. They are meant to be relatively generic and applicable to most block devices. The examples illustrate the following:

- Creating and working with an HRFS file system on an ATA disk with commands from the shell.
- Code that creates and formats partitions.
- Code that creates and formats a RAM disk volume.

⚠️ **CAUTION:** Because device names are recognized by the I/O system using simple substring matching, file systems should not use a slash (/) alone as a name; unexpected results may occur.

Example 7-1 Create HRFS for an ATA Disk

This example demonstrates how to initialize an ATA disk with HRFS from the shell. While these steps use an ATA XBD device, they are applicable to other block devices.
1. Create an XBD ATA device that controls the master ATA hard disk (drive zero) on the primary ATA controller (controller zero). This XBD device uses the entire disk.

```
-> xbd = ataXbdDevCreate(0,0,0,0,"/ata")
New symbol "xbd" added to kernel symbol table.
Instantiating /ata:0 as rawFs
xbd = 0xca4fe0: value = 262145 = 0x40001
```

*xbd* is a **device_t**. A value of zero would indicate an error in *ataXbdDevCreate()*. Such an error usually indicates a BSP configuration or hardware configuration error.

2. Display information about devices, and note that the new ata driver /ata:0 is listed. The zero in the name indicates that no partitions were detected.

```
-> devs
drv name
0 /null
1 /tyCo/0
1 /tyCo/1
8 yow-grand:
9 /vio
4 /ata:0
value = 25 = 0x19
```

Note that if no file system is detected on the device, the rawFs file system is instantiated and appears the device list.

3. Create two partitions on this disk device, specifying 50% of the disk space for the second partition, leaving 50% for the first partition. This step should only be performed once, when the disk is first initialized. If partitions are already written to the disk, this step should not be performed since it destroys data.

```
-> xbdCreatePartition("/ata:0", 2, 50, 0, 0)
value = 0 = 0x0
```

4. Then list the devices to display information about the new partitions.

```
-> devs
drv name
0 /null
1 /tyCo/0
1 /tyCo/1
8 yow-grand:
9 /vio
3 /ata:1
3 /ata:2
```
Note that /ata:0 does not appear in this list, and two new devices, /ata:1 and /ata:2, have been added to represent the new partitions. Each volume has rawfs instantiated in it as they are new and unformatted.

5. Format the volumes for HRFS. This step need only be done once, when the volumes are first initialized. If the volumes have already been initialized (formatted), then omit this step. This example formats the file system volumes with default options.

   -> hrfsFormat ("/ata:1", 0ll, 0, 0)
   Formatting /ata:1 for HRFS
   Instantiating /ata:1 as rawFs
   Formatting...OK.
   value = 0 = 0x0

   -> hrfsFormat ("/ata:2", 0ll, 0, 0)
   Formatting /ata:2 for HRFS
   Instantiating /ata:2 as rawFs
   Formatting...OK.
   value = 0 = 0x0

   For more information, see the API reference for hrFsFmtLib.

6. Display information about the HRFS volumes.

   -> ll "/ata:1"
   Listing Directory /ata:1:
   drwxrwxrwx 1 0 0 8192 Jan 1 00:13 ./
   drwxrwxrwx 1 0 0 8192 Jan 1 00:13 ../
   value = 0 = 0x0

   -> ll "/ata:2"
   Listing Directory /ata:2:
   drwxrwxrwx 1 0 0 8192 Jan 1 00:13 ./
   drwxrwxrwx 1 0 0 8192 Jan 1 00:13 ../
   value = 0 = 0x0

   If you are working with an ATA hard disk or a CD-ROM file system from an ATAPI CD-ROM drive, you can, alternatively, use usrAtaConfig(). This routine processes several steps at once. For more information, see the API reference.

Example 7-2 Creating and Partitioning a Disk and Creating Volumes

This code takes the name of the XBD device previously created, creates three partitions, creates the partition handler for these partitions, and creates the HRFS device handler for them. Then it formats the partitions using hrfsFormat().

STATUS usrPartDiskFsInit
char * xbdName /* device name used during creation of XBD */
{
    const char * devNames[] = { "/sd0a", "/sd0b", "/sd0c" };
    devname_t xbdPartName;
    int i;

    /* Map partition names */
    for (i = 1; i <= 3; i++)
    {
        sprintf (xbdPartName, "%s:d", devNames[i], i);
        fsmNameInstall (devNames[i], xbdPartName);
    }

    /* create partitions */
    if((xbdCreatePartition (xbdName, 3, 50, 45)) == ERROR)
        return ERROR;

    /* Formatting the first partition */
    if(hrfsFormat (devNames[0], 0ll, 0, 0) == ERROR)
        return ERROR;

    /* Formatting the second partition */
    if(hrfsFormat (devNames[1], 0ll, 0, 0) == ERROR)
        return ERROR;

    /* Formatting the third partition */
    if(hrfsFormat (devNames[2], 0ll, 0, 0) == ERROR)
        return ERROR;

    return OK;
}

Example 7-3 Creating and Formatting a RAM Disk Volume

The following code creates an XBD RAM disk and formats it for use with the HRFS file system. Note that for HRFS the minimum sector or block size is 512 bytes. The RAM disk must therefore be created with a block size of at least 512 bytes. See the API reference guide for more information.

STATUS usrRamDiskFsInit
{
    char * xbdName /* Name to give the XBD RAM disk */
}
{
    device_t xbd;
    STATUS   retVal;
/* 512 byte sectors, 1024 sectors, no partition support */
xbd = xbdRamDiskDevCreate (512, 512 * 1024, 0, xbdName);
if (xbd == NULL)
    return ERROR;

/* Format the entire RAM disk. Allow for 100 files */
retVal = hrfsFormat (xbdName, 0ll, 100);

return (retVal);
}

7.3.4 Transactionality

HRFS is a transactional based file system. It is transactional on a file or directory basis. This is unlike TRFS where the whole disk is considered.

Transactions are committed to disk automatically when modifying or deleting a file or directory. That is, upon successful completion of a function that modifies the disk means that the modifications are committed. There is no need for application interaction to commit.

Example functions that cause modifications to disk:
- write()
- remove()
- delete()
- mkdir()
- rmdir()
- link()
- unlink()
- truncate()
- truncated()
- ioctl() where the supplied command requires modifying the disk.

7.3.5 Maximum Number of Files and Directories

Files and directories are stored on disk in data structures called inodes. During formatting the maximum number of inodes is specified as a parameter to the formatter. See API reference for more details. This means that the combination of files and directories can never be more than there are inodes. It is fixed at the time of formatting. Trying to create a file or directory when all the inodes are exhausted will generate an error. Deleting a file or directory returns frees its corresponding inode.
7.3.6 Working with Directories

This section discusses creating and removing directories, and reading directory entries.

Creating Subdirectories

You can create as many subdirectories as there are inodes. Subdirectories can be created in the following ways

1. Using open(): To create a directory, the O_CREAT option must be set in the flags parameter and the S_IFDIR or FSTAT_DIR option must be set in the mode parameter. The open() call returns a file descriptor that describes the new directory. The file descriptor can only be used for reading only and should be closed when no longer needed.

2. Use mkdir(), usrFsLib.

When creating a directory using any of the above methods, the new directory name must be specified. This name can be either a full pathname or a pathname relative to the current working directory.

Removing Subdirectories

A directory that is to be deleted must be empty (except for the “.” and “..” entries). The root directory can never be deleted. Subdirectories can be removed in the following ways:

- Using ioctl() with the FIORMDIR function, specifying the name of the directory. Again, the file descriptor used can refer to any file or directory on the volume, or to the entire volume itself.

- Using the remove() function, specifying the name of the directory.

- Use rmdir(), usrFsLib.

Reading Directory Entries

You can programmatically search directories on HRFS volumes using the opendir(), readdir(), rewinddir(), and closedir() routines.
To obtain more detailed information about a specific file, use the `fstat()` or `stat()` routine. Along with standard file information, the structure used by these routines also returns the file-attribute byte from a directory entry.

For more information, see the API reference for `dirLib`.

### 7.3.7 Working with Files

This section discusses file I/O and file attributes.

**File I/O Routines**

Files on an HRFS file system device are created, deleted, written, and read using the standard VxWorks I/O routines: `creat()`, `remove()`, `write()`, and `read()`. For more information, see 6.3 Basic I/O, p.322, and the `ioLib` API references.

Note that `delete()` and `remove()` are synonymous with `unlink()` for HRFS.

**File Linking and Unlinking**

When a link is created an inode is not used. Another directory entry is created at the location specified by the parameter to `link()`. In addition, a reference count to the linked file is stored in the file's corresponding inode. When unlinking a file, this reference count is decremented. If the reference count is zero when `unlink()` is called, the file is deleted except if there are open file descriptors open on the file. In this case the directory entry is removed but the file still exists on the disk. This prevents tasks and processes (RTPs) from opening the file. When the final open file descriptor is closed the file is fully deleted freeing its inode.

Note that you cannot create a link to a subdirectory only to a regular file.

**File Permissions**

Unlike dosfs, files on HRFS do not have attributes. They instead have POSIX style permission bits. You can change these bits using the `chmod()` and `fchmod()` routines. See the API references for more information.
7.3.8 Crash Recovery and Volume Consistency

HRFS is a transactional based file system that is designed to be consistent at all times.

Crash Recovery

If a system should lose power or crash unexpectedly, HRFS is designed to rollback to the last set transaction point on restart. This is done automatically when the file system is mounted. All modifications made during the last incomplete transaction will be lost. However the disk will be in a consistent state.

Consistency Checking

Although it has been stated that an HRFS file system is always in a consistent state, this statement is made on the assumption that the underlying hardware is working correctly and never writes an incomplete sector or physical block. This assumption holds for most media including hard drives. It does not necessarily hold for a RAM disk as sector writing is simply a copy of one memory location to another and this maybe interrupted part way through if the system were to unexpected crash or lose power.

To assist with dealing with these issues, a consistency checker is provided: hrfsChkDsk(). See the API reference. This utility is designed to check for inconsistencies in the file system. No option to fix errors is provided but the utility will attempt to diagnose where the problem(s) exist.

The execution of the disk checker is not automatic. When the disk checker is run it unmounts the file system, causing all open file descriptors on the file system to become invalid.

7.3.9 I/O Control Functions Supported by HRFS

The HRFS file system supports the ioctl() functions. These functions are defined in the header file ioLib.h along with their associated constants.

For more information, see the API reference for ioctl() in ioLib.
### I/O Control Functions Supported by HRFS

<table>
<thead>
<tr>
<th>Function</th>
<th>Decimal Value</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>FIODISKCHANGE</td>
<td>13</td>
<td>Announces a media change.</td>
</tr>
<tr>
<td>FIODISKFORMAT</td>
<td>5</td>
<td>Formats the disk (device driver function).</td>
</tr>
<tr>
<td>FIODISKINIT</td>
<td>6</td>
<td>Initializes a file system on a disk volume.</td>
</tr>
<tr>
<td>FIOFLUSH</td>
<td>2</td>
<td>Flushes the file output buffer.</td>
</tr>
<tr>
<td>FIOFSTATGET</td>
<td>38</td>
<td>Gets file status information (directory entry data).</td>
</tr>
<tr>
<td>FIOGETNAME</td>
<td>18</td>
<td>Gets the filename of the <code>fd</code>.</td>
</tr>
<tr>
<td>FIOMOVE</td>
<td>47</td>
<td>Moves a file (does not rename the file).</td>
</tr>
<tr>
<td>FIONFREE</td>
<td>30</td>
<td>Gets the number of free bytes on the volume.</td>
</tr>
<tr>
<td>FIONREAD</td>
<td>1</td>
<td>Gets the number of unread bytes in a file.</td>
</tr>
<tr>
<td>FIOREADDIR</td>
<td>37</td>
<td>Reads the next directory entry.</td>
</tr>
<tr>
<td>FIORENAME</td>
<td>10</td>
<td>Renames a file or directory.</td>
</tr>
<tr>
<td>FIORMDIR</td>
<td>32</td>
<td>Removes a directory.</td>
</tr>
<tr>
<td>FIOSEEK</td>
<td>7</td>
<td>Sets the current byte offset in a file.</td>
</tr>
<tr>
<td>FIOSYNC</td>
<td>21</td>
<td>Same as <code>FIOFLUSH</code>, but also re-reads buffered file data.</td>
</tr>
<tr>
<td>FIOTRUNC</td>
<td>42</td>
<td>Truncates a file to a specified length.</td>
</tr>
<tr>
<td>FIOUNMOUNT</td>
<td>39</td>
<td>Un-mounts a disk volume.</td>
</tr>
<tr>
<td>FIOWHERE</td>
<td>8</td>
<td>Returns the current byte position in a file.</td>
</tr>
<tr>
<td>FIONCONTIG64</td>
<td>50</td>
<td>Gets the maximum contiguous disk space into a 64-bit integer.</td>
</tr>
<tr>
<td>FIONFREE64</td>
<td>51</td>
<td>Gets the number of free bytes into a 64-bit integer.</td>
</tr>
<tr>
<td>FIONREAD64</td>
<td>52</td>
<td>Gets the number of unread bytes in a file into a 64-bit integer.</td>
</tr>
</tbody>
</table>
The dosFs file system is an MS-DOS-compatible file system that offers considerable flexibility appropriate to the multiple demands of real-time applications. The primary features are:

- Hierarchical files and directories, allowing efficient organization and an arbitrary number of files to be created on a volume.
- A choice of contiguous or non-contiguous files on a per-file basis.
- Compatible with widely available storage and retrieval media (diskettes, hard drives, and so on).
- The ability to boot VxWorks from a dosFs file system.
- Support for VFAT (Microsoft VFAT long file names)
- Support for FAT12, FAT16, and FAT32 file allocation table types.

For information about dosFs libraries, see the VxWorks API references for dosFsLib and dosFsFmtLib.

For information about the MS-DOS file system, please see the Microsoft documentation.

---

### Table 7-1  
**I/O Control Functions Supported by HRFS**  
(cont’d)

<table>
<thead>
<tr>
<th>Function</th>
<th>Decimal Value</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>FIOSEEK64</td>
<td>53</td>
<td>Sets the current byte offset in a file from a 64-bit integer.</td>
</tr>
<tr>
<td>FLOWHERE64</td>
<td>54</td>
<td>Gets the current byte position in a file into a 64-bit integer.</td>
</tr>
<tr>
<td>FIOTRUNC64</td>
<td>55</td>
<td>Set the file's size from a 64-bit integer.</td>
</tr>
</tbody>
</table>
7 Local File Systems

7.4 MS-DOS-Compatible File System: dosFs

NOTE: The discussion in this chapter of the dosFs file system uses the term sector to refer to the minimum addressable unit on a disk. This definition of the term follows most MS-DOS documentation. However, in VxWorks, these units on the disk are normally referred to as blocks, and a disk device is called a block device.

dosFs and Flash Memory

For information about using dosFs with flash memory, see 8. Flash File System Support with TrueFFS.

dosFs and the Transaction-Based Reliable File System Facility

For information about using dosFs with the transaction-based reliable file system (TRFS) facility, see 6.7.8 Transaction-Based Reliable File System Facility: TRFS, p.359.

7.4.1 Configuring VxWorks for dosFs

To include dosFs support in VxWorks, configure the kernel with the appropriate required and optional components.

Required Components

The following components are required:

- INCLUDE_DOSFS_MAIN dosFsLib
- INCLUDE_DOSFS_FAT dosFs FAT12/16/32 FAT handler
- INCLUDE_XBD XBD component

And, either one or both of the following components are required:

- INCLUDE_DOSFS_DIR_VFAT Microsoft VFAT direct handler
- INCLUDE_DOSFS_DIR_FIXED Strict 8.3 & VxLongNames directory handler

In addition, you need to include the appropriate component for your block device; for example, INCLUDE_SCSI or INCLUDE_ATA.
Note that you can use INCLUDE_DOSFS to automatically include the following components:

- INCLUDE_DOSFS_MAIN
- INCLUDE_DOSFS_DIR_VFAT
- INCLUDE_DOSFS_DIR_FIXED
- INCLUDE_DOSFS_FAT
- INCLUDE_DOSFS_CHKDSK
- INCLUDE_DOSFS_FMT

Optional dosFs Components

The optional dosFs components are:

- INCLUDE_DOSFS_CACHE: disk cache facility (for rotational media)
- INCLUDE_DOSFS_FMT: dosFs file system formatting module
- INCLUDE_DOSFS_CHKDSK: file system integrity checking
- INCLUDE_DISK_UTIL: standard file system operations, such as ls, cd, mkdir, xcopy, and so on
- INCLUDE_TAR: the tar utility

Optional XBD Components

Optional XBD components are:

- INCLUDE_XBD_PART_LIB: disk partitioning facilities
- INCLUDE_XBD_BLK_DEV: XBD wrapper component for device drivers that have not been ported to XBD.
- INCLUDE_XBD_TRANS: TRFS support facility
- INCLUDE_XBD_RAMDRV: RAM disk facility

⚠️ CAUTION: If you are using a device for which the driver has not been ported to XBD, you must use the INCLUDE_XBD_BLK_DEV wrapper component in addition to INCLUDE_XBD. See XBD Block Device Wrapper Component, p.358 for more information.

For information about the XBD facility, see 6.7.7 Extended Block Device Facilities: XBD, p.356).

7.4.2 Creating a dosFs File System

This section describes the process of creating a dosFs file system. It first provides a summary overview and then a detailed description of each step. 7.3.3 HRFS and RAM Disk Examples, p.411 provides examples of these steps.
Overview of dosFs File System Creation

For information operating system configuration, see 7.4.1 Configuring VxWorks for dosFs, p.421. Note that The file system is initialized automatically at boot time.

The steps involved in creating a dosFs file system are as follows:

1. Create the appropriate block device. See Step 1: Create a Block Device, p.410.
4. If you are not using pre-formatted disks, format the volumes. See Step 5: Formatting the Volume, p.411.

dosFs File System Creation Steps

Before any other operations can be performed, the dosFs file system library, dosFsLib, must be initialized. This happens automatically, triggered by the required dosFs components that were included in the system.

Initializing the file system invokes iosDrvInstall(), which adds the driver to the I/O system driver table. The driver number assigned to the dosFs file system is recorded in a global variable, dosFsDrvNum. The table specifies the entry points for the dosFs file operations that are accessed by the devices using dosFs.

Step 1: Create a Block Device

Create one or more block devices. To create the device, call the routine appropriate for that device driver. The format for this routine is xxxDevCreate() where xxx represents the device driver type; for example, scsiBlkDevCreate() or ataDevCreate().

The driver routine returns a pointer to a block device descriptor structure, BLK_DEV. This structure describes the physical attributes of the device and specifies the routines that the device driver provides. The pointer returned is used to create an XBD device.

For more information on block devices, see 6.7.9 Block Devices, p.363.
**Step 2: Create an XBD Device**

Create an XBD device for each block device using `xbdBlkDevCreate()`.

After the XBD device is created, the device is automatically probed for a file system and partitions. If a disk is already formatted, the disk is mounted. If a file system is found, it is mounted. The file system may not be HRFS, so formatting may be required.

**Step 3: Create Partitions**

If you have included the `INCLUDE_XBD_PART_LIB` component in your system, you can create partitions on a disk and mount volumes atop the partitions. Use the `xbdCreatePartition()` routine to create partitions.

This step should only be performed once, when the disk is first initialized. If partitions are already written to the disk, this step should not be performed since it destroys data.

**Step 4: Formatting the Volume**

If you are using unformatted disk or wish to replace the current file system on the disk, format the disk by calling `dosFsVolFormat()`. For more information, see the VxWorks API reference for this routine.

The MS-DOS and dosFs file systems provide options for the format of the File Allocation Table (FAT) and the format of the directory. These options, described below, are completely independent.

⚠️ **CAUTION:** Reformatting a disk destroys any data that may be on it.

**File Allocation Table (FAT) Formats**

A volume FAT format is set during disk formatting, according to either the volume size (by default), or the per-user defined settings passed to `dosFsVolFormat()`. FAT options are summarized in Table 7-2:

<table>
<thead>
<tr>
<th>Format</th>
<th>FAT Table Entry Size</th>
<th>Usage</th>
<th>Size</th>
</tr>
</thead>
<tbody>
<tr>
<td>FAT12</td>
<td>12 bits per cluster number</td>
<td>Appropriate for very small devices with up to 4,084 KB clusters.</td>
<td>Typically, each cluster is two sectors large.</td>
</tr>
<tr>
<td>FAT16</td>
<td>16 bits per cluster number</td>
<td>Appropriate for small disks of up to 65,524 KB clusters.</td>
<td>Typically, used for volumes up to 2 GB; can support up to 8 GB.</td>
</tr>
</tbody>
</table>
Directory Formats

The options for the directory format are:

- **MSFT Long Names (VFAT)**
  
  Uses case-insensitive long filenames, with up to 254 characters. This format accepts disks created with short names. MSFT Long Names\(^1\) is the default directory format.

- **Short Names (8.3)**
  
  Case-insensitive MS-DOS-style filenames (8.3), with eight uppercase characters for the name itself and three for the extension.

Step 5: Change the Disk Cache Size

If you have included the `INCLUDE_DOSFS_CACHE` component, a read cache is automatically created when the file system is instantiated. The parameter, `DOSFS_DEFAULT_CACHE_SIZE` specifies the size of the cache for dosFs. It applies to all instantiations of the file system.

You can change the size of the cache for a particular instantiation of the file system by first destroying the cache with `dosFsCacheDelete()` and then re-creating the cache with `dosFsCacheCreate()`. For more information see the VxWorks API references for these routines.

A disk cache is intended to reduce the number of accesses to the media. It is not intended to be used on RAM disks. The cache can be safely used on TrueFFS devices as it is for read accesses only.

Step 6: Check Disk Volume Integrity

Optionally, check the disk for volume integrity using `dosFsChkDsk()`. Disk checking large disks can be time-consuming. The parameters you pass to `dosFsDevCreate()` determine whether disk checking happens automatically. For details, see the VxWorks API reference for `dosFsDevCreate()`.

---

\(^1\) The MSFT Long Names (VFAT) format supports 32-bit file size fields, limiting the file size to a 4 GB maximum.
Note that the DOSFS_DEFAULT_CREATE_OPTIONS parameter (of the INCLUDE_DOSFS_MAIN component) can be used to automatically determine if consistency checking should be done when the file system is instantiated.

7.4.3 dosFs and RAM Disk Examples

This section provides examples of the steps discussed in the preceding section. These examples use a variety of configurations and device types. They are meant to be relatively generic and applicable to most block devices. The examples illustrate the following:

- Creating and working with a dosFs file system on an ATA disk with commands from the shell.
- Code that creates and formats partitions.
- Code that creates and formats a RAM disk volume.

⚠️ CAUTION: Because device names are recognized by the I/O system using simple substring matching, file systems should not use a slash (/) alone as a name; unexpected results may occur.

Example 7-4 Create dosFs for an ATA Disk

This example demonstrates how to initialize an ATA disk with dosFs from the shell. While these steps use an ATA XBD device type, they are applicable to any XBD device.

1. Create an XBD device that controls the master ATA hard disk (drive zero) on the primary ATA controller (controller zero). This XBD device uses the entire disk.

   -> xbd = ataXbdDevCreate(0,0,0,0,"/ata")
   New symbol "xbd" added to kernel symbol table.
   Instantiating /ata:0 as rawFs
   xbd = 0xca4fe0: value = 262145 = 0x40001

   xbd is a device_t. A value of zero would indicate an error in ataXbdDevCreate(). Such an error usually indicates a BSP configuration or hardware configuration error.

2. Display information about devices, and note that the new ata driver /ata:0 is listed. The zero in the name indicates that no partitions were detected.

   -> devs
drv name
   0 /null
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1 /tyCo/0
1 /tyCo/1
8 yow-grand:
9 /vio:
4 /ata:0
value = 25 = 0x19

Note that if no file system is detected on the device, the rawFs file system is instantiated and appears the device list.

3. Create two partitions on this disk device, specifying 50% of the disk space for the second partition, leaving 50% for the first partition. This step should only be performed once, when the disk is first initialized. If partitions are already written to the disk, this step should not be performed since it destroys data.

-> xbdCreatePartition ("/ata:0", 2, 50, 0, 0)
value = 0 = 0x0

4. Then list the devices to display information about the new partitions.

-> devs
drv name
 0 /null
 1 /tyCo/0
 1 /tyCo/1
 8 yow-grand:
 9 /vio:
 3 /ata:1
 3 /ata:2

Note that /ata:0 does not appear in this list, and two new devices, /ata:1 and /ata:2, have been added to represent the new partitions. Each volume has rawfs instantiated in it as they are new and unformatted.
5. Format the volumes for dosFs. This step need only be done once, when the volumes are first initialized. If the volumes have already been initialized (formatted), then omit this step. This example formats the file system volumes with default options.

```c
-> dosFsVolFormat (/ata:1", 0, 0)
Formatting /ata:1 for DOSFS
Instantiating /ata:1 as rawFs
Formatting...Retrieved old volume params with %100 confidence:
Volume Parameters: FAT type: FAT32, sectors per cluster 8
  2 FAT copies, 0 clusters, 38425 sectors per FAT
  Sectors reserved 32, hidden 0, FAT sectors 76850
  Root dir entries 0, sysId (null), serial number 3a80000
  Label:
  "...
Disk with 40149184 sectors of 512 bytes will be formatted with:
Volume Parameters: FAT type: FAT32, sectors per cluster 8
  2 FAT copies, 5008841 clusters, 39209 sectors per FAT
  Sectors reserved 32, hidden 0, FAT sectors 78418
  Root dir entries 0, sysId VX5DOS32, serial number 3a80000
  Label:
  "...
OK.
value = 0 = 0x0
-> dosFsVolFormat (/ata:2, 0, 0)
Formatting /ata:2 for DOSFS
Instantiating /ata:2 as rawFs
Formatting...Retrieved old volume params with %100 confidence:
Volume Parameters: FAT type: FAT32, sectors per cluster 8
  2 FAT copies, 0 clusters, 19602 sectors per FAT
  Sectors reserved 32, hidden 0, FAT sectors 39204
  Root dir entries 0, sysId (null), serial number c78ff000
  Label:
  "...
Disk with 40144000 sectors of 512 bytes will be formatted with:
Volume Parameters: FAT type: FAT32, sectors per cluster 8
  2 FAT copies, 5008195 clusters, 39204 sectors per FAT
  Sectors reserved 32, hidden 0, FAT sectors 78408
  Root dir entries 0, sysId VX5DOS32, serial number c78ff000
  Label:
  "...
OK.
value = 0 = 0x0
```

For more information, see the API reference for dosFsFmtLib.

6. If the INCLUDE_DOSFS_CACHE component is included in VxWorks, a 32K cache is created by default. The size of the cache can be changed by removing it and creating a new one.

```c
-> dosFsCacheDelete "/ata:1"
value = 0 = 0x0
-> dosFsCacheCreate "/ata:1", 0, 1024*1024
value = 0 = 0x0
```

This creates a one MB cache for the first partition.
7. Display information about the dosFs volumes.

-> ll "/ata:1"
Listing Directory /ata:1:
value = 0 = 0x0
-> ll "/ata:2"
Listing Directory /ata:2:
value = 0 = 0x0
-> dosFsShow "/ata:2"

volume descriptor ptr (pVolDesc): 0xc7c358
auto disk check on mount: NOT ENABLED
volume write mode: copyback (DOS_WRITE)
max # of simultaneously open files: 22
file descriptors in use: 0
# of different files in use: 0
# of descriptors for deleted files: 0
# of obsolete descriptors: 0

current volume configuration:
- volume label: NO LABEL ; (in boot sector: )
- volume Id: 0xc78ff000
- total number of sectors: 40,144,000
- bytes per sector: 512
- # of sectors per cluster: 8
- # of reserved sectors: 32
- FAT entry size: FAT32
- # of sectors per FAT copy: 39,204
- # of FAT table copies: 2
- # of hidden sectors: 0
- first cluster is in sector # 78,440
- Update last access date for open-read-close = FALSE
- directory structure: VFAT
- file name format: 8-bit (extended-ASCII)
- root dir start cluster: 2

FAT handler information:
------------------------
- allocation group size: 501 clusters
- free space on volume: 20,513,562,620 bytes
value = 0 = 0x0

Above, we can see the Volume parameters for the /ata:2 volume. The file system volumes are now mounted and ready to be used.

If you are working with an ATA hard disk or a CD-ROM file system from an ATAPI CD-ROM drive, you can, alternatively, use usrAtaConfig(). This routine processes several steps at once. For more information, see the API reference.
Example 7-5  Creating and Partitioning a Disk and Creating Volumes

This code example takes a pointer to a block device, creates three partitions, creates the partition handler for these partitions, and creates the dosFs device handler for them. Then, it formats the partitions using `dosFsVolFormat()`.

```c
STATUS usrPartDiskFsInit
(char * xbdName /* device name used during creation of XBD */ )
{
    const char * devNames[] = { "/sd0a", "/sd0b", "/sd0c" };   
    devname_t xbdPartName;
    int newCacheSize = 0x30000
    int i;

    /* Map partition names */
    for (i = 1; i <= 3; i++)
    {
        sprintf (xbdPartName, "%s:d", devNames[i], i);
        fsmNameInstall (devNames[i], xbdPartName);
    }

    /* create partitions */
    if((xbdCreatePartition (xbdName, 3, 50, 45)) == ERROR)
        return ERROR;

    /* Formatting the first partition */
    if(dosFsVolFormat (devNames[0], 2, 0) == ERROR)
        return ERROR;

    /* Re-configure the cache for the first partition */
    if(dosFsCacheDelete (devNames[0]) == ERROR)
        return ERROR;
    if(dosFsCacheCreate (devNames[0], NULL, newCacheSize) == ERROR)
        return ERROR;

    /* Formatting the second partition */
    if(dosFsVolFormat (devNames[1], 2, 0) == ERROR)
        return ERROR;

    /* Formatting the third partition */
    if(dosFsVolFormat (devNames[2], 2, 0) == ERROR)
        return ERROR;

    return OK;
}
```
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Example 7-6 Creating and Formatting a RAM Disk Volume

The following code creates a RAM disk and formats it for use with the dosFs file system.

```c
STATUS usrRamDiskInit
{
    void /* no argument */
}
{ int ramDiskSize = 128 * 1024 ; /* 128KB, 128 bytes per sector */
    char *ramDiskDevName = "/ram0" ;
device_t  xbd;
    /* 128 byte/sec, no partition support */
    xbd = xbdRamDiskDevCreate (128, ramDiskSize, 0, ramDiskName);
    if( xbd == NULL )
        return ERROR ;
    /* format the RAM disk, ignore memory contents */
    dosFsVolFormat( ramDiskName, DOS_OPT_BLANK | DOS_OPT_QUIET, NULL );
    return OK;
}
```

7.4.4 Working with Volumes and Disks

This section discusses accessing volume configuration information and synchronizing volumes. For information about ioctl() support functions, see 7.4.9 I/O Control Functions Supported by dosFsLib, p.441.

Accessing Volume Configuration Information

The dosFsShow() routine can be used to display volume configuration information from the shell. The dosFsVolDescGet() routine can be used programmatically obtain or verify a pointer to the DOS_VOLUME_DESC structure. For more information, see the API references for these routines.

Synchronizing Volumes

When a disk is synchronized, all modified buffered data is physically written to the disk, so that the disk is up to date. This includes data written to files, updated
directory information, and the FAT. To avoid loss of data, a disk should be synchronized before it is removed. For more information, see the API references for `close()` and `dosFsVolUnmount()`.

### 7.4.5 Working with Directories

This section discusses creating and removing directories, and reading directory entries.

#### Creating Subdirectories

For FAT32, subdirectories can be created in any directory at any time. For FAT12 and FAT16, subdirectories can be created in any directory at any time, except in the root directory once it reaches its maximum entry count. Subdirectories can be created in the following ways:

1. Using `ioctl()` with the FIOMKDIR function: The name of the directory to be created is passed as a parameter to `ioctl()`.
2. Using `open()`: To create a directory, the O_CREAT option must be set in the flags parameter to open, and the FSTAT_DIR option must be set in the mode parameter. The `open()` call returns a file descriptor that describes the new directory. Use this file descriptor for reading only and close it when it is no longer needed.
3. Use `mkdir()`, `usrFsLib`.

When creating a directory using any of the above methods, the new directory name must be specified. This name can be either a full pathname or a pathname relative to the current working directory.

#### Removing Subdirectories

A directory that is to be deleted must be empty (except for the “.” and “..” entries). The root directory can never be deleted. Subdirectories can be removed in the following ways:

- Using `ioctl()` with the FIORMDIR function, specifying the name of the directory. Again, the file descriptor used can refer to any file or directory on the volume, or to the entire volume itself.
- Using the `remove()` function, specifying the name of the directory.
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- Use rmdir(), usrFsLib.

Reading Directory Entries

You can programmatically search directories on dosFs volumes using the opendir(), readdir(), rewinddir(), and closedir() routines.

To obtain more detailed information about a specific file, use the fstat() or stat() routine. Along with standard file information, the structure used by these routines also returns the file-attribute byte from a directory entry.

For more information, see the API reference for dirLib.

7.4.6 Working with Files

This section discusses file I/O and file attributes.

File I/O Routines

Files on a dosFs file system device are created, deleted, written, and read using the standard VxWorks I/O routines: creat(), remove(), write(), and read(). For more information, see 6.3 Basic I/O, p.322, and the ioLib API references.

File Attributes

The file-attribute byte in a dosFs directory entry consists of a set of flag bits, each indicating a particular file characteristic. The characteristics described by the file-attribute byte are shown in Table 7-3.
DOS_ATTR_RDONLY
If this flag is set, files accessed with open() cannot be written to. If the
O_WRONLY or O_RDWR flags are set, open() returns ERROR, setting errno to
S_dosFsLib_READ_ONLY.

DOS_ATTR_HIDDEN
This flag is ignored by dosFslib and produces no special handling. For
example, entries with this flag are reported when searching directories.

DOS_ATTR_SYSTEM
This flag is ignored by dosFslib and produces no special handling. For
example, entries with this flag are reported when searching directories.

DOS_ATTR_VOL_LABEL
This is a volume label flag, which indicates that a directory entry contains the
dosFs volume label for the disk. A label is not required. If used, there can be
only one volume label entry per volume, in the root directory. The volume
label entry is not reported when reading the contents of a directory (using
readdir()). It can only be determined using the ioctl() function FIOLABELGET.
The volume label can be set (or reset) to any string of 11 or fewer characters,
using the ioctl() function FIOLABELSET. Any file descriptor open to the
volume can be used during these ioctl() calls.

DOS_ATTRDIRECTORY
This is a directory flag, which indicates that this entry is a subdirectory, and
not a regular file.

DOS_ATTR_ARCHIVE
This is an archive flag, which is set when a file is created or modified. This flag
is intended for use by other programs that search a volume for modified files

<table>
<thead>
<tr>
<th>VxWorks Flag Name</th>
<th>Hex Value</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>DOS_ATTR_RDONLY</td>
<td>0x01</td>
<td>read-only file</td>
</tr>
<tr>
<td>DOS_ATTR_HIDDEN</td>
<td>0x02</td>
<td>hidden file</td>
</tr>
<tr>
<td>DOS_ATTR_SYSTEM</td>
<td>0x04</td>
<td>system file</td>
</tr>
<tr>
<td>DOS_ATTR_VOL_LABEL</td>
<td>0x08</td>
<td>volume label</td>
</tr>
<tr>
<td>DOS_ATTR_DIRECTORY</td>
<td>0x10</td>
<td>subdirectory</td>
</tr>
<tr>
<td>DOS_ATTR_ARCHIVE</td>
<td>0x20</td>
<td>file is subject to archiving</td>
</tr>
</tbody>
</table>
and selectively archive them. Such a program must clear the archive flag, since
VxWorks does not.

All the flags in the attribute byte, except the directory and volume label flags, can
be set or cleared using the ioctl() function FIOATTRIBSET. This function is called
after the opening of the specific file with the attributes to be changed. The
attribute-byte value specified in the FIOATTRIBSET call is copied directly; to
preserve existing flag settings, determine the current attributes using stat() or
fstat(), then change them using bitwise AND and OR operations.

Example 7-7 Setting DosFs File Attributes

This example makes a dosFs file read-only, and leaves other attributes intact.

```c
STATUS changeAttributes
    (void
     )
    {
        int fd;
        struct stat statStruct;
/* open file */
        if ((fd = open ("file", O_RDONLY, 0)) == ERROR)
            return (ERROR);
/* get directory entry data */
        if (fstat (fd, &statStruct) == ERROR)
            return (ERROR);
/* set read-only flag on file */
        if (ioctl (fd, FIOATTRIBSET, (statStruct.st_attrib | DOS_ATTR_RDONLY))
            == ERROR)
            return (ERROR);
/* close file */
        close (fd);
        return (OK);
    }
```

**NOTE:** You can also use the attrib() routine to change file attributes. For more
information, see the entry in usrFsLib.
7.4.7 Disk Space Allocation Options

The dosFs file system allocates disk space using one of the following methods. The first two methods are selected based upon the size of the write operation. The last method must be manually specified.

- **single cluster allocation**

  *Single cluster allocation* uses a single cluster, which is the minimum allocation unit. This method is automatically used when the write operation is smaller than the size of a single cluster.

- **cluster group allocation (nearly contiguous)**

  *Cluster group allocation* uses adjacent (contiguous) groups of clusters, called *extents*. Cluster group allocation is nearly contiguous allocation and is the default method used when files are written in units larger than the size of a disk’s cluster.

- **absolutely contiguous allocation**

  *Absolutely contiguous allocation* uses only absolutely contiguous clusters. Because this type of allocation is dependent upon the existence of such space, it is specified under only two conditions: immediately after a new file is created and when reading from a file assumed to have been allocated to a contiguous space. Using this method risks disk fragmentation.

For any allocation method, you can deallocate unused reserved bytes by using the POSIX-compatible routine `ftruncate()` or the `ioctl()` function `FIOTRUNC`.

Choosing an Allocation Method

Under most circumstances, cluster group allocation is preferred to absolutely contiguous file access. Because it is nearly contiguous file access, it achieves a nearly optimal access speed. Cluster group allocation also significantly minimizes the risk of fragmentation posed by absolutely contiguous allocation.

Absolutely contiguous allocation attains raw disk throughput levels, however this speed is only slightly faster than nearly contiguous file access. Moreover, fragmentation is likely to occur over time. This is because after a disk has been in use for some period of time, it becomes impossible to allocate contiguous space. Thus, there is no guarantee that new data, appended to a file created or opened with absolutely continuous allocation, will be contiguous to the initially written data segment.
It is recommended that for a performance-sensitive operation, the application regulate disk space utilization, limiting it to 90% of the total disk space. Fragmentation is unavoidable when filling in the last free space on a disk, which has a serious impact on performance.

### Using Cluster Group Allocation

The dosFs file system defines the size of a cluster group based on the media’s physical characteristics. That size is fixed for each particular media. Since seek operations are an overhead that reduces performance, it is desirable to arrange files so that sequential portions of a file are located in physically contiguous disk clusters. Cluster group allocation occurs when the cluster group size is considered sufficiently large so that the seek time is negligible compared to the read/write time. This technique is sometimes referred to as nearly contiguous file access because seek time between consecutive cluster groups is significantly reduced. Because all large files on a volume are expected to have been written as a group of extents, removing them frees a number of extents to be used for new files subsequently created. Therefore, as long as free space is available for subsequent file storage, there are always extents available for use. Thus, cluster group allocation effectively prevents fragmentation (where a file is allocated in small units spread across distant locations on the disk). Access to fragmented files can be extremely slow, depending upon the degree of fragmentation.

### Using Absolutely Contiguous Allocation

A contiguous file is made up of a series of consecutive disk sectors. Absolutely contiguous allocation is intended to allocate contiguous space to a specified file (or directory) and, by so doing, optimize access to that file. You can specify absolutely contiguous allocation either when creating a file, or when opening a file previously created in this manner.

For more information on the ioctl() functions, see 7.4.9 I/O Control Functions Supported by dosFsLib, p.441.
Allocating Contiguous Space for a File

To allocate a contiguous area to a newly created file, follow these steps:

1. First, create the file in the normal fashion using `open()` or `creat()`.
2. Then, call `ioctl()`. Use the file descriptor returned from `open()` or `creat()` as the file descriptor argument. Specify `FIOCONTIG` as the function code argument and the size of the requested contiguous area, in bytes, as the third argument.

The FAT is then searched for a suitable section of the disk. If found, this space is assigned to the new file. The file can then be closed, or it can be used for further I/O operations. The file descriptor used for calling `ioctl()` should be the only descriptor open to the file. Always perform the `ioctl()` `FIOCONTIG` operation before writing any data to the file.

To request the largest available contiguous space, use `CONTIG_MAX` for the size of the contiguous area. For example:

```
status = ioctl(fd, FIOCONTIG, CONTIG_MAX);
```

Allocating Space for Subdirectories

Subdirectories can also be allocated a contiguous disk area in the same manner:

- If the directory is created using the `ioctl()` function `FIOMKDIR`, it must be subsequently opened to obtain a file descriptor to it.
- If the directory is created using options to `open()`, the returned file descriptor from that call can be used.

A directory must be empty (except for the “.” and “..” entries) when it has contiguous space allocated to it.

Opening and Using a Contiguous File

Fragmented files require following cluster chains in the FAT. However, if a file is recognized as contiguous, the system can use an enhanced method that improves performance. This applies to all contiguous files, whether or not they were explicitly created using `FIOCONTIG`. Whenever a file is opened, it is checked for contiguity. If it is found to be contiguous, the file system registers the necessary information about that file to avoid the need for subsequent access to the FAT table. This enhances performance when working with the file by eliminating seek operations.

When you are opening a contiguous file, you can explicitly indicate that the file is contiguous by specifying the `DOS_O_CONTIG_CHK` flag with `open()`. This
prompts the file system to retrieve the section of contiguous space, allocated for this file, from the FAT table.

To find the maximum contiguous area on a device, you can use the `ioctl()` function with the `FIONCONTIG` flag. This information can also be displayed by `dosFsConfigShow()`.

Example 7-8 Finding the Maximum Contiguous Area on a DosFs Device

In this example, the size (in bytes) of the largest contiguous area is copied to the integer pointed to by the third parameter to `ioctl()` (`count`).

```c
STATUS contigTest
(void) /* no argument */
{
    int count; /* size of maximum contiguous area in bytes */
    int fd; /* file descriptor */

    /* open device in raw mode */
    if ((fd = open("/DEV1/", O_RDONLY, 0)) == ERROR)
        return (ERROR);

    /* find max contiguous area */
    ioctl(fd, FIONCONTIG, &count);

    /* close device and display size of largest contiguous area */
    close(fd);
    printf("largest contiguous area = %d\n", count);
    return (OK);
}
```

7.4.8 Crash Recovery and Volume Consistency

The DOS file system is inherently susceptible to data structure inconsistencies that result from interruptions during certain types of disk updates. These types of interruptions include power failures, inadvertent system crashes for fixed disks, and the manual removal of a disk.

**NOTE:** The DOS file system is not considered a fault-tolerant file system. The VxWorks dosFs file system, however, can be used in conjunction with the Transaction-Based Reliable File System facility; see 6.7.8 Transaction-Based Reliable File System Facility: TRFS, p.359.
The inconsistencies occur because the file system data for a single file is stored in three separate regions of the disk. The data stored in these regions are:

- The file chain in the File Allocation Table (FAT), located in a region near the beginning of the disk.
- The directory entry, located in a region anywhere on the disk.
- File clusters containing file data, located anywhere on the disk.

Since all three regions cannot be always updated before an interruption, dosFs includes an optional integrated consistency-checking mechanism to detect and recover from inconsistencies. For example, if a disk is removed when a file is being deleted, a consistency check completes the file deletion operation. Or, if a file is being created when an interruption occurs, then the file is un-created. In other words, the consistency checker either rolls forward or rolls back the operation that has the inconsistency, making whichever correction is possible.

To use consistency checking set one of the following flags in the `dosDevCreateOptions` parameter of `dosDevCreate()`:

- **DOS_CHK_ONLY**
  - Perform consistency check only; do not repair.
- **DOS_CHK_REPAIR**
  - Perform consistency check and repair
- **DOS_CHK_NONE**
  - Do not perform consistency check.

Note that if none of these are used, **DOS_CHK_REPAIR | DOS_CHK_VERB_1** is the default.

A verbosity level may be set: **DOS_CHK_VERB_SILENT, DOS_CHK_VERB_1** or **DOS_CHK_VERB_2**.

Consistency checking is suppressed on disks that are marked clean. To force a consistency check use **DOS_CHK_FORCE**.

If configured, consistency checking is invoked under the following conditions:

- When a new volume is mounted.
- Once at system initialization time for fixed disks.
- Every time a new cartridge is inserted for removable disks.

**NOTE:** Consistency checking slows a system down, particularly when a a disk is first accessed.
7.4.9 I/O Control Functions Supported by dosFsLib

The dosFs file system supports the `ioctl()` functions. These functions are defined in the header file `ioLib.h` along with their associated constants.

For more information, see the API references for `dosFsLib` and for `ioctl()` in `ioLib`.

<table>
<thead>
<tr>
<th>Function</th>
<th>Decimal Value</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>FIOATTRIBSET</td>
<td>35</td>
<td>Sets the file-attribute byte in the dosFs directory entry.</td>
</tr>
<tr>
<td>FIOCONTIG</td>
<td>36</td>
<td>Allocates contiguous disk space for a file or directory.</td>
</tr>
<tr>
<td>FIODISKCHANGE</td>
<td>13</td>
<td>Announces a media change.</td>
</tr>
<tr>
<td>FIODISKFORMAT</td>
<td>5</td>
<td>Formats the disk (device driver function).</td>
</tr>
<tr>
<td>FIODISKINIT</td>
<td>6</td>
<td>Initializes a dosFs file system on a disk volume.</td>
</tr>
<tr>
<td>FIOFLUSH</td>
<td>2</td>
<td>Flushes the file output buffer.</td>
</tr>
<tr>
<td>FIOFSTATGET</td>
<td>38</td>
<td>Gets file status information (directory entry data).</td>
</tr>
<tr>
<td>FIOGETNAME</td>
<td>18</td>
<td>Gets the filename of the <code>fd</code>.</td>
</tr>
<tr>
<td>FIOLABELGET</td>
<td>33</td>
<td>Gets the volume label.</td>
</tr>
<tr>
<td>FIOLABELSET</td>
<td>34</td>
<td>Sets the volume label.</td>
</tr>
<tr>
<td>FIOMKDIR</td>
<td>31</td>
<td>Creates a new directory.</td>
</tr>
<tr>
<td>FIOMOVE</td>
<td>47</td>
<td>Moves a file (does not rename the file).</td>
</tr>
<tr>
<td>FIONCONTIG</td>
<td>41</td>
<td>Gets the size of the maximum contiguous area on a device.</td>
</tr>
<tr>
<td>FIONFREE</td>
<td>30</td>
<td>Gets the number of free bytes on the volume.</td>
</tr>
<tr>
<td>FIONREAD</td>
<td>1</td>
<td>Gets the number of unread bytes in a file.</td>
</tr>
<tr>
<td>FIOREADDIR</td>
<td>37</td>
<td>Reads the next directory entry.</td>
</tr>
<tr>
<td>FIORENAME</td>
<td>10</td>
<td>Renames a file or directory.</td>
</tr>
<tr>
<td>FIORMDIR</td>
<td>32</td>
<td>Removes a directory.</td>
</tr>
<tr>
<td>Function</td>
<td>Decimal Value</td>
<td>Description</td>
</tr>
<tr>
<td>-------------------</td>
<td>---------------</td>
<td>------------------------------------------------------------------------------</td>
</tr>
<tr>
<td>FIOSEEK</td>
<td>7</td>
<td>Sets the current byte offset in a file.</td>
</tr>
<tr>
<td>FIOSYNC</td>
<td>21</td>
<td>Same as FIOFLUSH, but also re-reads buffered file data.</td>
</tr>
<tr>
<td>FIOTRUNC</td>
<td>42</td>
<td>Truncates a file to a specified length.</td>
</tr>
<tr>
<td>FIOUNMOUNT</td>
<td>39</td>
<td>Un-mounts a disk volume.</td>
</tr>
<tr>
<td>FIOWHERE</td>
<td>8</td>
<td>Returns the current byte position in a file.</td>
</tr>
<tr>
<td>FIOCONTIG64</td>
<td>49</td>
<td>Allocates contiguous disk space using a 64-bit size.</td>
</tr>
<tr>
<td>FIONCONTIG64</td>
<td>50</td>
<td>Gets the maximum contiguous disk space into a 64-bit integer.</td>
</tr>
<tr>
<td>FIONFREE64</td>
<td>51</td>
<td>Gets the number of free bytes into a 64-bit integer.</td>
</tr>
<tr>
<td>FIONREAD64</td>
<td>52</td>
<td>Gets the number of unread bytes in a file into a 64-bit integer.</td>
</tr>
<tr>
<td>FIOSEEK64</td>
<td>53</td>
<td>Sets the current byte offset in a file from a 64-bit integer.</td>
</tr>
<tr>
<td>FIOWHERE64</td>
<td>54</td>
<td>Gets the current byte position in a file into a 64-bit integer.</td>
</tr>
<tr>
<td>FIOTRUNC64</td>
<td>55</td>
<td>Set the file's size from a 64-bit integer.</td>
</tr>
</tbody>
</table>
7.4.10 Booting from a Local dosFs File System Using SCSI

VxWorks can be booted from a local SCSI device (such as a hard drive in the target system). Before you can boot from SCSI, you must make a new boot loader that contains the SCSI library. Configure VxWorks with the INCLUDE_SCSI, INCLUDE_SCSI_BOOT, and SYS_SCSI_CONFIG components.

After creating the SCSI boot loader ROM, you can prepare the dosFs file system for use as a boot device. The simplest way to do this is to partition the SCSI device so that a dosFs file system starts at block 0. You can then make the new system image, place it on your SCSI boot device, and boot the new VxWorks system. These steps are shown in more detail below.

⚠️ WARNING: For use as a boot device, the directory name for the dosFs file system must begin and end with slashes (as with /sd0/ used in the following example). This is an exception to the usual naming convention for dosFs file systems and is incompatible with the NFS requirement that device names not end in a slash.

Step 1: Create the SCSI Device

Create the SCSI device using scsiPhysDevCreate( ) and initialize the disk with a dosFs file system. Modify the file installDir/vxworks-6.x/target/bspName/sysScsi.c to reflect your SCSI configuration.

Step 2: Rebuild Your System

Rebuild your system.

Step 3: Copy the VxWorks Runtime Image

Copy the file vxWorks to the drive. Below, a VxWorks task spawns the copy( ) routine, passing it two arguments.

The first argument is the source file for the copy( ) command. The source file is the VxWorks runtime image, vxWorks. The source host name is tiamat, the source filename is C:/vxWorks. These are passed to copy() in concatenated form, as the string “tiamat:C:/vxWorks.”

The second argument is the destination file for the copy( ) command. The dosFs file system, on the local target SCSI disk device, is named /sd0, and the target file name is vxWorks. These are, similarly, passed to copy() in concatenated form, as the string “/sd0/vxWorks.” When booting the target from the SCSI device, the boot loader image should specify the runtime file as “/sd0/vxWorks”.

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Step 4: Copy the System Symbol Table

Depending upon image configuration, the \texttt{vxWorks.sym} file for the system symbol table may also be needed. Therefore, in similar fashion, copy the \texttt{vxWorks.sym} file. The runtime image, \texttt{vxWorks}, downloads the \texttt{vxWorks.sym} file from the same location.

\begin{verbatim}
-> sp (copy, "tiamat:c:/vxWorks", "/sd0/vxWorks"")
task spawned: id = 0x3f2a200, name = t2
value = 66232832 = 0x3f2a200

Copy OK: 1065570 bytes copied
\end{verbatim}

Step 5: Test the Copying

Now, list the files to ensure that the files were correctly copied.

\begin{verbatim}
-> sp (ll, "/sd0")
task spawned: id = 0x3f2a1bc, name = t3
value = 66232764 = 0x3f2a1bc

Listing Directory /sd0:
-rwxrwxrwx  1 0       0          1065570 Oct 26 2001 vxWorks
-rwxrwxrwx  1 0       0           147698 Oct 26 2001 vxWorks.sym
\end{verbatim}

Step 6: Reboot and Change Parameters

Reboot the system, and then change the boot loader parameters. Boot device parameters for SCSI devices follow this format:

\begin{verbatim}
scsi=id,lun
\end{verbatim}

where \texttt{id} is the SCSI ID of the boot device, and \texttt{lun} is its Logical Unit Number (LUN). To enable use of the network, include the on-board Ethernet device (for example, \texttt{ln} for LANCE) in the \texttt{other} field.

The following example boots from a SCSI device with a SCSI ID of 2 and a LUN of 0.

\begin{verbatim}
boot device : scsi=2,0
processor number : 0
host name : host
file name : /sd0/vxWorks
inet on ethernet (e) : 147.11.1.222:ffffff00
\end{verbatim}
7.5 Raw File System: rawFs

VxWorks provides a raw file system (rawFs) for use in systems that require only the most basic disk I/O functions. The rawFs file system, implemented with rawFsLib, treats the entire disk volume much like a single large file.

Although the dosFs file system provides this ability to varying degrees, the rawFs file system offers advantages in size and performance if more complex functions are not required.

The rawFs file system imposes no organization of the data on the disk. It maintains no directory information; and there is therefore no division of the disk area into specific files. All open() operations on rawFs devices specify only the device name; no additional filenames are possible.

The entire disk area is treated as a single file and is available to any file descriptor that is open for the device. All read and write operations to the disk use a byte-offset relative to the start of the first block on the disk.

A rawFs file system is created by default if inserted media does not contain a recognizable file system.

7.5.1 Configuring VxWorks for rawFs

To use the rawFs file system, configure VxWorks with the INCLUDE_RAWFS and INCLUDE_XBD components.

⚠️ CAUTION: If you are using a device for which the driver has not been ported to XBD, you must use the INCLUDE_XBD_BLK_DEV wrapper component in addition to INCLUDE_XBD. See XBD Block Device Wrapper Component, p.358 for more information.

Set the NUM_RAWFS_FILES parameter of the INCLUDE_RAWFS component to the desired maximum open file descriptor count. For information about using
multiple file descriptors with what is essentially a single large file, see 7.5.4 rawFs File I/O, p.448.

7.5.2 Creating a rawFs File System

Unlike dosFs and HRFS, rawFs does not have a formatter. rawFs is the default file system when VxWorks cannot instantiate a known file system such as dosFs, HRFS or even cdromFs. There are no particular data structures on the media that signify the disk as being raw. To create a rawFs file system manually, the current file system must be uninstantiated and replaced with rawFs. Having two or more file systems on the same media can produce instabilities in the VxWorks system. Hence, when instantiating a new file system the previous one must be removed. See Example 7-9Creating a rawFs File System, p.447 for code that illustrates how this can be done. (See 7.2 File System Monitor, p.405 for information about default creation of rawFs.)

Before any other operations can be performed, the rawFs library, rawFsLib, must be initialized by calling rawFsInit(). This routine takes a single parameter, the maximum number of rawFs file descriptors that can be open at one time. This count is used to allocate a set of descriptors; a descriptor is used each time a rawFs device is opened.

The rawFsInit() routine also makes an entry for the rawFs file system in the I/O system driver table (with iosDrvInstall()). This entry specifies the entry points for rawFs file operations, for all devices that use the rawFs file system. The driver number assigned to the rawFs file system is placed in a global variable, rawFsDrvNum.

The rawFsInit() routine is normally called by the usrRoot() task after starting the VxWorks system.

After the rawFs file system is initialized, the next step is to create one or more devices. Devices are created by the device driver’s device creation routine (xxDevCreate()). The driver routine returns a pointer to a block device descriptor structure (BLK_DEV). The BLK_DEV structure describes the physical aspects of the device and specifies the routines in the device driver that a file system can call.

For more information about block devices, see 6.9.4 Block Device Drivers, p.399.

Immediately after its creation, the block device has neither a name nor a file system associated with it. To initialize a block device for use with rawFs, the already-created block device must be associated with rawFs and a name must be assigned to it. This is done with the rawFsDevInit() routine. Its parameters are the
name to be used to identify the device and a pointer to the block device descriptor structure (BLK_DEV):

```c
RAW_VOL_DESC *pVolDesc;
BLK_DEV *pBlkDev;
pVolDesc = rawFsDevInit("DEV1:“, pBlkDev);
```

The rawFsDevInit() call assigns the specified name to the device and enters the device in the I/O system device table (with iosDevAdd()). It also allocates and initializes the file system’s volume descriptor for the device. It returns a pointer to the volume descriptor to the caller; this pointer is used to identify the volume during certain file system calls.

Note that initializing the device for use with rawFs does not format the disk. That is done using an ioctl() call with the FIODISKFORMAT function.

**NOTE**: No disk initialization (FIODISKINIT) is required, because there are no file system structures on the disk. Note, however, that rawFs accepts that ioctl() function code for compatibility with other file systems; in such cases, it performs no action and always returns OK.

**Example 7-9 Creating a rawFs File System**

This example illustrates creating a rawFs file system.

```c
int fd;
device_t xbd;

/* Map some XBD names. Use :0 and :1 since the disk may or may not have partitions */
fsmNameMap("/ata:0", "/rawfs");
fsmNameMap("/ata:1", "/rawfs");
xbd = ataXbdDevCreate(0,0,0,0, "/ata");

/* Get an file descriptor to the current file system */
fd = open("/rawfs", 0, 0);

/* Register on the path instantiator event */

/* The ejection of the current file system is asynchronous and is handled by another task. Depending on relative priorities this may not happen immediately so the path wait even facility is used. Each file system will trip this event when they instantiate to let waiting task that it is ready. */
fsPathAddedEventSetup(&waitData, "/rawfs");
```

```c
fd = open("/rawfs", 0, 0);
```
/* Eject the current file system and put rawfs in its place */
ioctl (fd, XBD_SOFT_EJECT, (int)XBD_TOPO);

/* Our FD is now invalid */
/* Wait for the path to instantiate */
fsWaitForPath(&waitData);

Once the call to fsWaitForPath() returns the rawfs file system is ready.

7.5.3 Mounting rawFs Volumes

A disk volume is mounted automatically, generally during the first open() or creat() operation. (Certain ioctl() functions also cause the disk to be mounted.) The volume is again mounted automatically on the first disk access following a ready-change operation.

⚠️ CAUTION: Because device names are recognized by the I/O system using simple substring matching, file systems should not use a slash (/) alone as a name or unexpected results may occur.

7.5.4 rawFs File I/O

To begin I/O operations upon a rawFs device, first open the device using the standard open() routine (or the creat() routine). Data on the rawFs device is written and read using the standard I/O routines write() and read(). For more information, see 6.3 Basic I/O, p.322.

The character pointer associated with a file descriptor (that is, the byte offset where the read and write operations take place) can be set by using ioctl() with the FIOSEEK function.

Multiple file descriptors can be open simultaneously for a single device. These must be carefully managed to avoid modifying data that is also being used by another file descriptor. In most cases, such multiple open descriptors use FIOSEEK to set their character pointers to separate disk areas.

7.5.5 I/O Control Functions Supported by rawFsLib

The rawFs file system supports the ioctl() functions shown in Table 7-5. The functions listed are defined in the header file ioLib.h. For more information, see the API references for rawFsLib and for ioctl() in ioLib.
7 Local File Systems
7.6 CD-ROM File System: cdromFs

The VxWorks CD-ROM file system, cdromFs, allows applications to read data from CDs formatted according to the ISO 9660 standard file system with or without the Joliet extensions. This section describes how cdromFs is organized, configured, and used.

The cdromFs library, cdromFsLib, lets applications read any CD-ROMs, CD-Rs, or CD-RWs (collectively called CDs) that are formatted in accordance with the ISO 9660 file system standard, with or without the Joliet extensions. ISO 9660 interchange level 3, implementation level 2, is supported. Note that multi-extent files, interleaved files, and files with extended attribute records are supported.

The following CD features and ISO 9660 features are not supported:

- Multi-volume sets
- Record format files
- CDs with a sector size that is not a power of two
- Multi-session CD-R or CD-RW

Table 7-5  I/O Control Functions Supported by rawFsLib

<table>
<thead>
<tr>
<th>Function</th>
<th>Decimal Value</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>FIODISKCHANGE</td>
<td>13</td>
<td>Announces a media change.</td>
</tr>
<tr>
<td>FIODISKFORMAT</td>
<td>5</td>
<td>Formats the disk (device driver function).</td>
</tr>
<tr>
<td>FIOFLUSH</td>
<td>2</td>
<td>Same as FIOSYNC.</td>
</tr>
<tr>
<td>FIOGETNAME</td>
<td>18</td>
<td>Gets the device name of the fd.</td>
</tr>
<tr>
<td>FIONREAD</td>
<td>1</td>
<td>Gets the number of unread bytes on the device.</td>
</tr>
<tr>
<td>FIOSEEK</td>
<td>7</td>
<td>Sets the current byte offset on the device.</td>
</tr>
<tr>
<td>FIOSYNC</td>
<td>21</td>
<td>Writes out all modified file descriptor buffers.</td>
</tr>
<tr>
<td>FIOUNMOUNT</td>
<td>39</td>
<td>Un-mounts a disk volume.</td>
</tr>
<tr>
<td>FIOWHERE</td>
<td>8</td>
<td>Returns the current byte position on the device.</td>
</tr>
</tbody>
</table>

7.6 CD-ROM File System: cdromFs

The VxWorks CD-ROM file system, cdromFs, allows applications to read data from CDs formatted according to the ISO 9660 standard file system with or without the Joliet extensions. This section describes how cdromFs is organized, configured, and used.

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The following CD features and ISO 9660 features are not supported:

- Multi-volume sets
- Record format files
- CDs with a sector size that is not a power of two
- Multi-session CD-R or CD-RW

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After initializing cdromFs and mounting it on a CD-ROM block device, you can access data on that device using the standard POSIX I/O calls: open(), close(), read(), ioctl(), readdir(), and stat(). The write() call always returns an error.

The cdromFs utility supports multiple drives, multiple open files, and concurrent file access. When you specify a pathname, cdromFS accepts both forward slashes (/) and back slashes (\) as path delimiters. However, the backslash is not recommended because it might not be supported in future releases.

cdromFs provides access to CD file systems using any standard BLK_DEV structure. The basic initialization sequence is similar to installing a dosFs file system on a SCSI or ATA device, with a few significant differences: Create the CD file system device directly on the BLK_DEV. CBIO drivers are not used.

After you have created the CD file system device (7.6.2 Creating and Using cdromFs, p.451), use ioctl() to set file system options. The files system options are described below:

**CDROMFS_DIR_MODE_SET/GET**

These options set and get the directory mode. The directory mode controls whether a file is opened with the Joliet extensions, or without them. The directory mode can be set to any of the following:

- **MODE_ISO9660**
  Do not use the Joliet extensions.

- **MODE_JOLIET**
  Use the Joliet extensions.

- **MODE_AUTO**
  Try opening the directory first without Joliet, and then with Joliet.

⚠️ **CAUTION:** Changing the directory mode un-mounts the file system. Therefore, any open file descriptors are marked as obsolete.

**CDROMFS_STRIP_SEMICOLON**

This option sets the readdir() strip semicolon setting to FALSE if arg is 0, and to TRUE otherwise. If TRUE, readdir() removes the semicolon and following version number from the directory entries retrieved.

2. Therefore, mode 2/form 2 sectors are not supported, as they have 2324 bytes of user data per sector. Both mode 1/form 1 and mode 2/form 1 sectors are supported, as they have 2048 bytes of user data per sector.

3. The first session (that is, the earliest session) is always read. The most commonly desired behavior is to read the last session (that is, the latest session).
7 Local File Systems

7.6 CD-ROM File System: cdromFs

CDROMFS_GET_VOL_DESC
This option returns, in arg, the primary or supplementary volume descriptor by which the volume is mounted. arg must be of type T_ISO_PVD_SVD_ID, as defined in cdromFsLib.h. The result is the volume descriptor, adjusted for the endianness of the processor (not the raw volume descriptor from the CD). This result can be used directly by the processor. The result also includes some information not in the volume descriptor, such as which volume descriptor is in use.

For information on using cdromFs(), see the API reference for cdromFsLib.

7.6.1 Configuring VxWorks for cdromFs

To configure VxWorks with cdromFs, add the INCLUDE_CDROMFS and INCLUDE_XBD components to the kernel. Add other required components (such as SCSI or ATA) depending on the type of device).

CAUTION: If you are using a device for which the driver has not been ported to XBD, you must use the INCLUDE_XBD_BLK_DEV wrapper component in addition to INCLUDE_XBD. See XBD Block Device Wrapper Component, p.358 for more information.

If you are using an ATAPI device, make appropriate modifications to the ataDrv, ataResources[ ] structure array (if needed). This must be configured appropriately for your hardware platform.

7.6.2 Creating and Using cdromFs

This section describes the steps for creating a block device for the CD-ROM, creating a cdromFsLib device, mounting the file system, and accessing the media. The steps are performed from the shell, and shell show routines are used to display information.

Step 1: Create an XBD Device

Based on the type of device, create either a native XBD device or the combination of a block device with the XBD wrapper. The following is an example for an ATAPI master device upon the secondary ATA controller:

```c
-> xbd = ataXbdDevCreate(1, 0, 0, "/cdrom")
New symbol "xbd" added to kernel symbol table.
xbd = 0xc4fe0: value = 262145 = 0x4000
```
CDROMFS file system will automatically be created in a CD is present in the drive.

**Step 2: Verify cdromFs is instantiated**

With the cdromFs component, INCLUDE_CDROMFS, included, cdromfs will instantiate automatically when the XBD driver completes successfully and a CD is present in the drive.

```
-> devs
drv name
  0 /null
  1 /tyCo/0
  1 /tyCo/1
  4 /fd0
  5 /ata0a
  9 yow-grand:
 10 /vio
  3 /cdrom:0
```

**Step 3: Open the Root Directory**

This step is optional. It is only required if you plan to execute Step 5 or Step 8.

```
-> fd = open("/cdrom:0", 0, 0)
New symbol "fd" added to kernel symbol table.
fd = 0x18cef98: value = 4 = 0x4
```

In the command-line sequence above, the first 0 is the value of `O_RDONLY` in `fcntl.h`.

**Step 4: Set readdir( ) To Omit Version Numbers from Its Output**

This step is optional.

The strip semicolon mode controls whether `readdir( )` returns version numbers. After mounting, the strip semicolon mode defaults to `FALSE`, meaning that version numbers will be returned. If you do not want version numbers, enter the following:

```
-> ioctl(fd, 0x740002, 1)
```

In the command-line sequence above, 0x740002 is the value of `CDROMFS_STRIP_SEMICOLON` in `cdromFsLib.h`.

**Step 5: Set Which Volume Descriptor To Use**

This step is optional.

The directory mode controls which volume descriptor is used to open a file or directory. After mounting, the directory mode defaults to `MODE_AUTO`, meaning that all volume descriptors will be tried. The directory mode can be changed to
MODE_ISO9660 to use only the ISO 9660 volume descriptors, or to MODE_JOLIET to use only the Joliet volume descriptors. If either of these modes is selected, and if the CD does not contain the selected volume descriptor, an S_cdromFsLib_UNKNOWN_FILE_SYSTEM error is recorded.

```
-> ioctl (fd, 0x740000, 0)
```

In the command-line sequence above, 0x740000 is the value of CDROMFS_DIR_MODE_SET, and 0 is the value of MODE_ISO9660. Both are located in cdromFsLib.h.

**Step 6: Close the Root Directory**

This step is optional. It is only required if you plan to execute Step 8 or Step 7.

```
-> close (fd)
```

**Step 7: Check the Configuration**

You can check the CD-ROM configuration using cdromFsVolConfigShow():

```
-> cdromFsVolConfigShow "/cdrom:0"
```

```
device config structure ptr  0x18d4dd8
device name                  /cdrom:0
bytes per physical sector    2048

Primary directory hierarchy:

volume descriptor number      :1
descriptor logical sector     :16
descriptor offset in sector   :0
standard ID                   :CD001
volume descriptor version     :1
UCS unicode level (0=ISO9660) :0
system ID                     :
volume ID                     :
volume size                   :37773312 = 36 MB
number of logical blocks      :18444 = 0x480c
volume set size               :1
volume sequence number        :1
logical block size            :2048
path table memory size (bytes):364
path table size on CD (bytes) :364
path table entries            :21
volume set ID                 :

volume publisher ID           :

volume data preparer ID       :
```
7.6.3 I/O Control Functions Supported by cdromFsLib

The cdromFs file system supports the ioctl() functions. These functions, and their associated constants, are defined in the header files ioLib.h and cdromFsLib.h. Table 7-6 describes the ioctl() functions that cdromFsLib supports. For more information, see the API references for cdromFsLib and for ioctl() in ioLib.

<table>
<thead>
<tr>
<th>Function Constant</th>
<th>Decimal</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>CDROMFS_DIR_MODE_GET</td>
<td>7602176</td>
<td>Gets the volume descriptor(s) used to open files.</td>
</tr>
<tr>
<td>CDROMFS_DIR_MODE_SET</td>
<td>7602177</td>
<td>Sets the volume descriptor(s) used to open files.</td>
</tr>
<tr>
<td>CDROMFS_GET_VOL_DESC</td>
<td>7602179</td>
<td>Gets the volume descriptor that is currently in use.</td>
</tr>
<tr>
<td>CDROMFS_STRIP_SEMICOLON</td>
<td>7602178</td>
<td>Sets the readdir() strip version number setting.</td>
</tr>
<tr>
<td>FIOFSTATGET</td>
<td>38</td>
<td>Gets file status information (directory entry data).</td>
</tr>
<tr>
<td>FIOGETNAME</td>
<td>18</td>
<td>Gets the filename of the file descriptor.</td>
</tr>
<tr>
<td>FIOLABELGET</td>
<td>33</td>
<td>Gets the volume label.</td>
</tr>
<tr>
<td>FIONREAD</td>
<td>1</td>
<td>Gets the number of unread bytes in a file.</td>
</tr>
<tr>
<td>FIONREAD64</td>
<td>52</td>
<td>Gets the number of unread bytes in a file (64-bit version).</td>
</tr>
<tr>
<td>FIOREADDIR</td>
<td>37</td>
<td>Reads the next directory entry.</td>
</tr>
<tr>
<td>FIOSEEK</td>
<td>7</td>
<td>Sets the current byte offset in a file.</td>
</tr>
<tr>
<td>FIOSEEK64</td>
<td>53</td>
<td>Sets the current byte offset in a file (64-bit version).</td>
</tr>
</tbody>
</table>
7 Local File Systems

7.7 Read-Only Memory File System: ROMFS

7.6.4 Version Numbers

\texttt{cdromFsLib} has a 4-byte version number. The version number is composed of four parts, from most significant byte to least significant byte:

- major number
- minor number
- patch level
- build

The version number is returned by \texttt{cdromFsVersionNumGet()} and displayed by \texttt{cdromFsVersionNumDisplay()}.

7.7 Read-Only Memory File System: ROMFS

ROMFS is a simple, read-only file system that represents and stores files and directories in a linear way (similar to the tar utility). It is installed in RAM with the VxWorks system image at boot time. The name ROMFS stands for Read Only Memory File System; it does not imply any particular relationship to ROM media.

ROMFS provides the ability to bundle VxWorks applications—or any other files for that matter—with the operating system. No local disk or network connection to a remote disk is required for executables or other files. When VxWorks is configured with the ROMFS component, files of any type can be included in the operating system image simply by adding them to a ROMFS directory on the host system, and then rebuilding VxWorks. The build produces a single system image that includes both the VxWorks and the files in the ROMFS directory.
When VxWorks is booted with this image, the ROMFS file system and the files it holds are loaded with the kernel itself. ROMFS allows you to deploy files and operating system as a unit. In addition, process-based applications can be coupled with an automated startup facility so that they run automatically at boot time. ROMFS thereby provides the ability to create fully autonomous, multi-process systems.

ROMFS can also be used to store applications that are run interactively for diagnostic purposes, or for applications that are started by other applications under specific conditions (errors, and so on).

### 7.7.1 Configuring VxWorks with ROMFS

VxWorks must be configured with the INCLUDE_ROMFS component to provide ROMFS facilities.

### 7.7.2 Building a System With ROMFS and Files

Configuring VxWorks with ROMFS and applications involves several simple steps:

1. A ROMFS directory must be created in the project directory on the host system, using the name `/romfs`.
2. Application files must be copied into the directory.
3. VxWorks must be rebuilt.

For example, adding a process-based application called `myVxApp.vxe` from the command line would look like this:

```bash
cd c:\myInstallDir\vxworks-6.1\target\proj\wrSbc8260_diab
mkdir romfs
copy c:\allMyVxApps\myVxApp.vxe romfs
make TOOL=diab
```

The contents of the `romfs` directory are automatically built into a ROMFS file system and combined with the VxWorks image.

The ROMFS directory does not need to be created in the VxWorks project directory. It can also be created in any location on (or accessible from) the host system, and the `make` utility’s `ROMFS_DIR` macro used to identify where it is in the build command. For example:

```bash
make TOOL=diab ROMFS_DIR="c:\allMyVxApps"
```
7 Local File Systems
7.8 Target Server File System: TSFS

Note that any files located in the romfs directory are included in the system image, regardless of whether or not they are application executables.

7.7.3 Accessing Files in ROMFS

At runtime, the ROMFS file system is accessed as /romfs. The content of the ROMFS directory can be browsed using the ls and cd shell commands, and accessed programmatically with standard file system routines, such as open() and read().

For example, if the directory installDir/vxworks-6.x/target/proj/wrSbc8260_diab/romfs has been created on the host, the file foo copied to it, and the system rebuilt and booted; then using cd and ls from the shell (with the command interpreter) looks like this:

```
[vxWorks *]# cd /romfs
[vxWorks *]# ls
.
.. foo
[vxWorks *]#
```

And foo can also be accessed at runtime as /romfs/foo by any applications running on the target.

7.7.4 Using ROMFS to Start Applications Automatically

ROMFS can be used with various startup mechanisms to start process-based applications automatically when VxWorks boots.

See the VxWorks Application Programmer’s Guide: Applications and Processes for more information.

7.8 Target Server File System: TSFS

The Target Server File System (TSFS) is designed for development and diagnostic purposes. It is a full-featured VxWorks file system, but the files are actually located on the host system.
The TSFS provides all of the I/O features of the network driver for remote file access (*netDrv*; see 6.7.5 Non-NFS Network Devices, p.354), without requiring any target resources—except those required for communication between the target system and the target server on the host. The TSFS uses a WDB target agent driver to transfer requests from the VxWorks I/O system to the target server. The target server reads the request and executes it using the host file system. When you open a file with TSFS, the file being opened is actually on the host. Subsequent *read()* and *write()* calls on the file descriptor obtained from the *open()* call read from and write to the opened host file.

The TSFS VIO driver is oriented toward file I/O rather than toward console operations. TSFS provides all the I/O features that *netDrv* provides, without requiring any target resource beyond what is already configured to support communication between target and target server. It is possible to access host files randomly without copying the entire file to the target, to load an object module from a virtual file source, and to supply the filename to routines such as *ld()* and *copy()*.

Each I/O request, including *open()* , is synchronous; the calling target task is blocked until the operation is complete. This provides flow control not available in the console VIO implementation. In addition, there is no need for WTX protocol requests to be issued to associate the VIO channel with a particular host file; the information is contained in the name of the file.

Consider a *read()* call. The driver transmits the ID of the file (previously established by an *open()* call), the address of the buffer to receive the file data, and the desired length of the read to the target server. The target server responds by issuing the equivalent *read()* call on the host and transfers the data read to the target program. The return value of *read()* and any *errno* that might arise are also relayed to the target, so that the file appears to be local in every way.

For detailed information, see the API reference for *wdbTsfsDrv*.

**Socket Support**

TSFS sockets are operated on in a similar way to other TSFS files, using *open()* , *close()* , *read()* , *write()* , and *ioctl()* . To open a TSFS socket, use one of the following forms of filename:

- *TCP:hostIP:port*
- *TCP:hostname:port*

The *flags* and *permissions* arguments are ignored. The following examples show how to use these filenames:
The result of this `open()` call is to open a TCP socket on the host and connect it to the target server socket at hostname or hostIP awaiting connections on port. The resultant socket is non-blocking. Use `read()` and `write()` to read and write to the TSFS socket. Because the socket is non-blocking, the `read()` call returns immediately with an error and the appropriate `errno` if there is no data available to read from the socket. The `ioctl()` usage specific to TSFS sockets is discussed in the API reference for `wdbTsfsDrv`. This socket configuration allows VxWorks to use the socket facility without requiring `sockLib` and the networking modules on the target.

**Error Handling**

Errors can arise at various points within TSFS and are reported back to the original caller on the target, along with an appropriate error code. The error code returned is the VxWorks `errno` which most closely matches the error experienced on the host. If a WDB error is encountered, a WDB error message is returned rather than a VxWorks `errno`.

**Configuring VxWorks for TSFS Use**

To use TSFS, configure VxWorks with the `INCLUDE_WDB_TSFS` component. This creates the `/tgtsvr` file system on the target.

The target server on the host system must also be configured for TSFS. This involves assigning a root directory on your host to TSFS (see the discussion of the target server `-R` option in Security Considerations, p. 460). For example, on a PC host you could set the TSFS root to `c:\myTarget\logs`.

Having done so, opening the file `/tgtsvr/logFoo` on the target causes `c:\myTarget\logs\logFoo` to be opened on the host by the target server. A new file descriptor representing that file is returned to the caller on the target.
Security Considerations

While TSFS has much in common with netDrv, the security considerations are different (also see 6.7.5 Non-NFS Network Devices, p.354). With TSFS, the host file operations are done on behalf of the user that launched the target server. The user name given to the target as a boot parameter has no effect. In fact, none of the boot parameters have any effect on the access privileges of TSFS.

In this environment, it is less clear to the user what the privilege restrictions to TSFS actually are, since the user ID and host machine that start the target server may vary from invocation to invocation. By default, any host tool that connects to a target server which is supporting TSFS has access to any file with the same authorizations as the user that started that target server. However, the target server can be locked (with the -L option) to restrict access to the TSFS.

The options which have been added to the target server startup routine to control target access to host files using TSFS include:

-R Set the root of TSFS.
   For example, specifying -R /tftpboot prepends this string to all TSFS filenames received by the target server, so that /tgtsvr/etc/passwd maps to /tftpboot/etc/passwd. If -R is not specified, TSFS is not activated and no TSFS requests from the target will succeed. Restarting the target server without specifying -R disables TSFS.

-RW Make TSFS read-write.
   The target server interprets this option to mean that modifying operations (including file create and delete or write) are authorized. If -RW is not specified, the default is read only and no file modifications are allowed.

   NOTE: For more information about the target server and the TSFS, see the tgtsvr command reference. For information about specifying target server options from the IDE, see the host development environment documentation.

Using the TSFS to Boot a Target

For information about using the TSFS to boot a targets, see Reconfiguring for Booting with the Target Server File System (TSFS), p.17.
8.1 Introduction

TrueFFS is flash management software that provides access to flash memory by emulating disk access. It provides VxWorks with block device functionality, which allows the dosFs file system to be used to access flash memory in the same manner as a disk (see 7.4 MS-DOS-Compatible File System: dosFs, p. 420). In addition, TrueFFS provides full flash media management capabilities.

TrueFFS is a VxWorks-compatible implementation of M-Systems FLite, version 2.0. This system is reentrant, thread-safe, and supported on all CPU architectures that host VxWorks. TrueFFS consists of four layers:

- The core layer, which connects the other layers and handles global facilities, such as back-grounding, garbage collection, timers, and other system resources. This layer provides the block device interface for dosFs.
The flash translation layer, which maintains the block allocation map that associates the file system’s view of the storage medium with erase blocks in flash.

The MTD layer, which implements the low-level programming of the flash medium (map, read, write, and erase functions).

The socket layer, which provides an interface between TrueFFS and the board hardware with board-specific hardware access routines.

Figure 8-1 illustrates the relationship between the TrueFFS layers, the dosFs file system, and the flash medium itself.

This chapter provides instructions for using TrueFFS with the MTDs and drivers that are included in this release. It provides quick-start material for configuring TrueFFS and formatting TrueFFS drives, and thus presents the basic steps required to use the default TrueFFS facilities with your application.

It also provides information about creating a boot image region that excludes TrueFFS, and about writing the boot image to that region.

If you need to customize or create new socket drivers or MTDs, or would simply like more detailed information about TrueFFS technology, see the VxWorks Device Driver Developer’s Guide: Flash File System Support with TrueFFS.

NOTE: This version of the TrueFFS product is a block device driver to VxWorks that, although intended to be file system neutral, is guaranteed to work only with the dosFs (MS-DOS compatible) file system offered with this product.
### 8.2 Overview of Implementation Steps

This section provides an overview of how to use TrueFFS with VxWorks BSPs that provide support for the TrueFFS component. To determine if your BSP provides TrueFFS support, see the online BSP reference documentation (or the file `installDir/vxworks-6.x/target/config/bspName/target.ref`).

You may need to write certain sub-components for your application. This most often occurs in the MTD layer. See the *VxWorks Device Driver Developer’s Guide: Flash File System Support with TrueFFS* for information in this regard.

To determine if this release provides an MTD suitable for your flash hardware, see 8.3.1 Selecting an MTD Component, p.464 and Including the MTD Component, p.467.

**Step 1: Select an MTD Component**

Choose an MTD, appropriate for your hardware, from those provided with the TrueFFS product. See 8.3.1 Selecting an MTD Component, p.464.

**Step 2: Identify the Socket Driver**

Ensure that you have a working socket driver. The socket driver is a source code component, implemented in the file `sysTffs.c`. For BSPs that already support TrueFFS, the socket driver is fully defined and located in the BSP directory. See 8.3.2 Identifying the Socket Driver, p.465.

**Step 3: Configure the System**

Configure your system for TrueFFS by adding the appropriate VxWorks components. Minimum support requires components for dosFs and the four TrueFFS layers. See 8.3.3 Configuring VxWorks and Building the System, p.465.

**NOTE:** VxWorks component names use the abbreviation TFFS for TrueFFS.

**Step 4: Build the System**

Build the system from the IDE or from the command line. See Building the System, p.469.

**Step 5: Boot the Target and Format the Drives**

Boot the target and then format the drives. See 8.3.4 Formatting the Flash, p.469.
Step 6: Create a Flash Region for Boot Code
Optionally, create a boot image region that excludes TrueFFS, and write the boot image to that region. See 8.3.5 Creating a Region in Flash for a Boot Image, p.471.

Step 7: Mount the Drive
Mount the VxWorks DOS file system on a TrueFFS flash drive. See 8.3.6 Mounting the Drive, p.474.

Step 8: Test the Drive
Test your drive(s). See 8.3.7 Testing the Drive, p.475.

8.3 Creating a System with TrueFFS
This section provides detailed instructions on configuring VxWorks with the required TrueFFS and dosFs components, building the system, formatting the flash, mounting the drive, and testing the drive. It also provides information about creating a region for a boot image.

8.3.1 Selecting an MTD Component
This section provides as complete list of these MTDs. Evaluate whether any of these drivers support the device that you intend to use for TrueFFS. Devices are usually identified by their JEDEC IDs. If you find an MTD appropriate to your flash device, you can use that MTD. These drivers are also provided in binary form; so you do not need to compile the MTD source code unless you have modified it.

The directory installDir/vxworks-6.x/target/src/drv/tffs contains the source code for the following types of MTD components:

- MTDs that work with several of the devices provided by Intel, AMD, Fujitsu, and Sharp.
- Two generic MTDs that can be used for devices complying with CFI.

To better support the out-of-box experience, these MTDs attempt to cover the widest possible range of devices (in their class) and of bus architectures.
Consequently, the drivers are bulky and slow in comparison to drivers written specifically to address the runtime environment that you want to target.

If the performance and size of the drivers provided do not match your requirements, you can modify them to better suit your needs. For more information, see the *VxWorks Device Driver Developer’s Guide: Flash File System Support with TrueFFS*.

**NOTE:** For the list of the MTD components and details about adding the MTD component to your system, see *Including the MTD Component*, p.467.

### 8.3.2 Identifying the Socket Driver

The socket driver that you include in your system must be appropriate for your BSP. Some BSPs include socket drivers, others do not. The socket driver file is `sysTffs.c` and, if provided, it is located in your BSP directory.

If your BSP does not provide this file, follow the procedure described in the *VxWorks Device Driver Developer’s Guide: Flash File System Support with TrueFFS*, which explains how to port a stub version to your hardware.

In either case, the build process requires that a working socket driver (`sysTffs.c`) be located in the BSP directory. For more information, see *Adding the Socket Driver*, p.468.

### 8.3.3 Configuring VxWorks and Building the System

To configure a VxWorks systems with TrueFFS, you must include:

- Components to fully support the dosFs file system.
- The core layer TrueFFS component, `INCLUDE_TFFS`.
- At least one software module from each of the other three TrueFFS layers: the translation, MTD, and socket layers.

You can configure and build your system either from the command line or with the IDE. For general information on configuration procedures, see the host development environment documentation.

For either configuration and build method, special consideration must be given to cases where either the socket driver or the MTD, or both, are not provided. The drivers need to be registered and MTDs need appropriate component descriptions.
For more information, see the VxWorks Device Driver Developer’s Guide: Flash File System Support with TrueFFS.

**NOTE:** Included with TrueFFS are sources for several MTDs and socket drivers. The MTDs are in `installDir/vxworks-6.x/target/src/drv/tffs`. The socket drivers are defined in the `sysTffs.c` files provided in the `installDir/vxworks-6.x/target/config/bspname` directory for each BSP that supports TrueFFS.

**Including dosFs File System Components**

A system configuration with TrueFFS is not useful without the VxWorks compatible file system, MS-DOS. Therefore, dosFs support and all components that it depends upon need to be included in your VxWorks system. For information on this file system and support components, see 7.4 MS-DOS-Compatible File System: dosFs, p.420.

There are other file system components that are not required, but which may be useful. These components add support for the basic functionality needed to use a file system, such as the commands `ls`, `cd`, `copy`, and so forth (which are provided by the `INCLUDE_DISK_UTIL` component).

**Including the Core TrueFFS Component**

VxWorks must be configured with the TrueFFS core component, `INCLUDE_TFFS`. Including this component triggers the correct sequence of events, at boot time, for initializing this product. It also ensures that the socket driver is included in your system (see Adding the Socket Driver, p.468).

**Including Utility Components**

This section describes optional TrueFFS utility components, their purpose, and their default configuration options.

**INCLUDE_TFFS_SHOW**

Including this component adds two TrueFFS configuration display utilities, `tffsShow()` and `tffsShowAll()` for use from the shell.

The `tffsShow()` routine prints device information for a specified socket interface. It is particularly useful when trying to determine the number of
Flash File System Support with TrueFFS

8.3 Creating a System with TrueFFS

erase units required to write a boot image (8.3.5 Creating a Region in Flash for a Boot Image, p.471). The \texttt{tffsShowAll()} routine provides the same information for all socket interfaces registered with VxWorks. The \texttt{tffsShowAll()} routine can be used from the shell to list the drives in the system. The drives are listed in the order in which they were registered. This component is not included by default. You can include it from the IDE facility or with the \texttt{vxprj} command-line facility.

\textbf{INCLUDE\_TFFS\_BOOT\_IMAGE}

This component is used for configuring flash-resident boot images. Defining this component automatically includes \texttt{tffsBootImagePut()} in your \texttt{sysTffs.o} file. The routine is used to write the boot image to flash memory (see Writing the Boot Image to Flash, p.473)

\textbf{NOTE: INCLUDE\_TFFS\_BOOT\_IMAGE} is normally added by default in the socket driver, \texttt{sysTffs.c}.

Including the MTD Component

Add the MTD component appropriate to your flash device (8.3.1 Selecting an MTD Component, p.464) to your system. The MTD components for flash devices from Intel, AMD, Fujitsu, and Sharp, are described in Table 8-2. For more information about support for these devices, see the VxWorks Device Driver Developer’s Guide: Flash File System Support with TrueFFS.

<table>
<thead>
<tr>
<th>Component</th>
<th>Device</th>
</tr>
</thead>
<tbody>
<tr>
<td>INCLUDE_MTD_CFISCS</td>
<td>CFI/SCS device.</td>
</tr>
<tr>
<td>INCLUDE_MTD_CFIAMD</td>
<td>CFI-compliant AMD and Fujitsu devices.</td>
</tr>
<tr>
<td>INCLUDE_MTD_I28F016</td>
<td>Intel 28f016 device.</td>
</tr>
<tr>
<td>INCLUDE_MTD_I28F008</td>
<td>Intel 28f008 device.</td>
</tr>
<tr>
<td>INCLUDE_MTD_AMD</td>
<td>AMD, Fujitsu: 29F0{40,80,16} 8-bit devices.</td>
</tr>
</tbody>
</table>

If you have written your own MTD, you need to be sure that it is correctly defined for inclusion in the system, and that it explicitly requires the transition layer. See the VxWorks Device Driver Developer’s Guide: Flash File System Support with TrueFFS for information.
Including the Translation Layer Component

Choose the translation layer appropriate to the technology used by your flash medium. The main variants of flash devices are NOR and NAND. TrueFFS provides support for:

- NOR devices
- NAND devices that conform to the SSFDC specification

The translation layer is provided in binary form only. The translation layer components are listed in Table 8-2.

Table 8-2 Components for TrueFFS Translation Layers

<table>
<thead>
<tr>
<th>Component</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>INCLUDE_TL_FTL</td>
<td>The translation layer for NOR flash devices. If you can execute code in flash, your device uses NOR logic.</td>
</tr>
<tr>
<td>INCLUDE_TL_SSFDC</td>
<td>The translation layer for devices that conform to Toshiba Solid State Floppy Disk Controller Specifications. TrueFFS supports only those NAND devices that comply with the SSFDC specification.</td>
</tr>
</tbody>
</table>

The component descriptor files specify the dependency between the translation layers and the MTDs; therefore, when configuring through the IDE or the CLI vxprj facility, you do not need to explicitly select a translation layer. The build process handles it for you.

For more information about the translation layer, see the VxWorks Device Driver Developer’s Guide: Flash File System Support with TrueFFS.

Adding the Socket Driver

Inclusion of the socket driver is relatively automatic. By including the core TrueFFS component, INCLUDE_TFFS, in VxWorks, the build process checks for a socket driver, sysTffs.c, in the BSP directory and includes that file in the system.

If your BSP does not provide a socket driver, follow the procedure described in the VxWorks Device Driver Developer’s Guide: Flash File System Support with TrueFFS for writing a socket driver. To include the socket driver in your system, a working version of the socket driver (sysTffs.c) must be located in your BSP directory.
Building the System

Build the system, from either the IDE or from the command line.

8.3.4 Formatting the Flash

If the flash array for your device provides a boot program, then space must be reserved for the program (see 8.3.5 Creating a Region in Flash for a Boot Image, p. 471). The flash must then be formatted for use with TrueFFS and then mounted as a file system.

First, boot your system. After the system boots and registers the socket driver(s), bring up the shell. From the shell, run `tffsDevFormat()` to format the flash memory for use with TrueFFS. This routine takes two arguments, a drive number and a format argument:

```
tffsDevFormat (int tffsDriveNo, int formatArg);
```

**NOTE:** You can format the flash medium even though there is not yet a block device driver associated with the flash.

**CAUTION:** Running `tffsDevFormat()` on a device that is sharing boot code with the file system, leaves the board without boot code once the routine completes. The board then becomes unusable until some alternative method is provided for re-flashing the lost image. Once re-flashed, the file system that you created by formatting is destroyed.

Specifying the Drive Number

The first argument, `tffsDriveNo`, is the drive number (socket driver number). The drive number identifies the flash medium to be formatted and is determined by the order in which the socket drivers were registered. Most common systems have a single flash drive, but TrueFFS supports up to five. Drive numbers are assigned to the flash devices on your target hardware by the order in which the socket drivers are registered in `sysTffsInit()` during boot. The first to be registered is drive 0, the second is drive 1, and so on up to 4. Therefore, the socket registration process determines the drive number. (Details of this process are described in see the VxWorks Device Driver Developer’s Guide: Flash File System Support with TrueFFS.) You use this number to specify the drive when you format it.
Formatting the Device

The second argument, `formatArg`, is a pointer to a `tffsDevFormatParams` structure (cast to an `int`). This structure describes how the volume should be formatted. The `tffsDevFormatParams` structure is defined in `installDir/vxworks-6.x/target/h/tffs/tffsDrv.h`:

```c
typedef struct {
    tffsFormatParams formatParams;
    unsigned formatFlags;
} tffsDevFormatParams;
```

**TFFS_STD_FORMAT_PARAMS**

To facilitate calling `tffsDevFormat()` from the shell, you can simply pass zero (or a NULL pointer) for the second argument, `formatArg`. Doing so will use a macro, `TFFS_STD_FORMAT_PARAMS`, which defines default values for the `tffsDevFormatParams` structure. The macro defines the default values used in formatting a flash disk device. `TFFS_STD_FORMAT_PARAMS` is defined in `tffsDrv.h` as:

```c
#define TFFS_STD_FORMAT_PARAMS {{0, 99, 1, 0x100000L, NULL, {0,0,0,0}, NULL, 2, 0, NULL}, FTL_FORMAT_IF_NEEDED}
```

If the second argument, `formatArg`, is zero, `tffsDevFormat()` uses the default values from this macro.

The macro passes values for both the first and second members of the `tffsDevFormatParams` structure. These are:

```c
formatParams = {0, 99, 1, 0x100000L, NULL, (0,0,0,0), NULL, 2, 0, NULL}
formatFlags = FTL_FORMAT_IF_NEEDED
```

The meaning of these default values, and other possible arguments for the members of this structure, are described below.

**formatParams**

The `formatParams` member is of the type `tffsFormatParams`. Both this structure, and the default values used by the `TFFS_STD_FORMAT_PARAMS` macro, are defined in `installDir/vxworks-6.x/target/h/tffs/tffsDrv.h`.

If you use the `TFFS_STD_FORMAT_PARAMS` macro, the default values will format the entire flash medium for use with TrueFFS. The most common reason for changing `formatParams` is to support a boot region. If you want to create a boot image region that excludes TrueFFS (8.3.5 Creating a Region in Flash for a Boot Image, p.471), you need to modify these default values by changing only the first member.
of the `tffsFormatParams` structure, `bootImageLen`. For details, see 8.3.5 Creating a Region in Flash for a Boot Image, p.471.

formatFlags

The second member of the `tffsDevFormatParams` structure, `formatFlags`, determines the option used to format the drive. There are several possible values for `formatFlags`, which are listed in Table 8-3.

Table 8-3 Options for formatFlags

<table>
<thead>
<tr>
<th>Macro</th>
<th>Value</th>
<th>Meaning</th>
</tr>
</thead>
<tbody>
<tr>
<td>FTL_FORMAT</td>
<td>1</td>
<td>FAT and FTL formatting</td>
</tr>
<tr>
<td>FTL_FORMAT_IF_NEEDED</td>
<td>2</td>
<td>FAT formatting, FTL formatting if needed</td>
</tr>
<tr>
<td>NO_FTL_FORMAT</td>
<td>0</td>
<td>FAT formatting only</td>
</tr>
</tbody>
</table>

The default macro `TFFS_STD_FORMAT_PARAMS` passes `FTL_FORMAT_IF_NEEDED` as the value for this argument.

8.3.5 Creating a Region in Flash for a Boot Image

Although the translation services of TrueFFS provide many advantages for managing the data associated with a file system, those same services also complicate the use of flash memory as a boot device. The only practical solution is to first create a boot image region that excludes TrueFFS, and then write the boot image to that region. This section describes, first, the technical details of the situation, then, how to create the region, and finally, how to write the boot image to it.

Write Protecting Flash

TrueFFS requires that all flash devices that interact with the file system, including boot image regions and NVRAM regions, not be write-protected by the MMU. This is because it is essential to the proper working of the product that all commands being issued to the device reach it. Write-protecting the device would impact this behavior, since it would also prevent commands being issued, that are not write-oriented, from reaching the device. For more information, see the VxWorks Device Driver Developer’s Guide: Flash File System Support with TrueFFS.
You can, however, reserve a fallow region that is not tampered with by the file system. TrueFFS supports boot code by allowing the user to specify a fallow area when formatting the device. Fallow areas are always reserved at the start of the flash. There have been a few instances where architecture ports have required the fallow area to be at the end of flash. This was accomplished by faking the size of the identification process in the MTD (that is, by telling the MTD that it has less memory than is actually available). The format call is then told that no fallow area is required. TrueFFS does not care how the fallow area is managed, nor is it affected by any faking.

Creating the Boot Image Region

To create the boot image region, format the flash memory so that the TrueFFS segment starts at an offset. This creates a fallow area within flash that is not formatted for TrueFFS. This preserves a boot image region. If you want to update the boot image, you can write a boot image into this fallow area, as described in Writing the Boot Image to Flash, p.473.

Formatting at an Offset

To format the flash at an offset, you need to initialize the tffsFormatParams structure to values that leave a space on the flash device for a boot image. You do this by specifying a value for the bootImageLen member (of the structure) that is at least as large as the boot image. The bootImageLen member specifies the offset after which to format the flash medium for use with TrueFFS. For details on bootImageLen and other members of the structure, see the comments in the header file installDir/vxworks-6.x/target/h/tffs/tffsDrv.h.

The area below the offset determined by bootImageLen is excluded from TrueFFS. This special region is necessary for boot images because the normal translation and wear-leveling services of TrueFFS are incompatible with the needs of the boot program and the boot image it relies upon. When tffsDevFormat() formats flash, it notes the offset, then erases and formats all erase units with starting addresses higher than the offset. The erase unit containing the offset address (and all previous erase units) are left completely untouched. This preserves any data stored before the offset address.

For more information on wear-leveling, see the VxWorks Device Driver Developer’s Guide: Flash File System Support with TrueFFS.
Using a BSP Helper Routine

Some BSPs provide an optional, BSP-specific, helper routine, `sysTffsFormat()`, which can be called externally to create or preserve the boot image region. This routine first sets up a pointer to a `tffsFormatParams` structure that has been initialized with a value for `bootImageLen` that formats at an offset, creating the boot image region; then it calls `tffsDevFormat()`.

Several BSPs, among them the ads860 BSP, include a `sysTffsFormat()` routine that reserves 0.5 MB for the boot image. For example:

```c
STATUS sysTffsFormat (void)
{
    STATUS status;
    tffsDevFormatParams params =
    {
        #define HALF_FORMAT
        /* lower 0.5MB for bootimage, upper 1.5MB for TFFS */
        #ifdef HALF_FORMAT
        {0x80000l, 99, 1, 0x10000l, NULL, {0,0,0,0}, NULL, 2, 0, NULL},
        #else
        {0x000000l, 99, 1, 0x10000l, NULL, {0,0,0,0}, NULL, 2, 0, NULL},
        #endif /* HALF_FORMAT */
        FTL_FORMAT_IF_NEEDED
    };
    /* assume that the drive number 0 is SIMM */
    status = tffsDevFormat (0, (int)&params);
    return (status);
}
```

For more examples of `sysTffsFormat()` usage, see the socket drivers in `installDir/vxworks-6.x/target/src/drv/tffs/sockets`. If your BSP does not provide a `sysTffsFormat()` routine, then create a similar routine, or pass the appropriate argument to `tffsDevFormat()`.

Writing the Boot Image to Flash

If you have created a boot image region, write the boot image to the flash device. To do this you use `tffsBootImagePut()`, which bypasses TrueFFS (and its translation layer) and writes directly into any location in flash memory. However, because `tffsBootImagePut()` relies on a call to `tffsRawio()`, you cannot use this routine once the TrueFFS volume is mounted.
The `tffsBootImagePut()` routine is defined in `installDir/vxworks-6.x/target/src/drv/tffs/tffsConfig.c` as:

```c
STATUS tffsBootImagePut
{
    int    driveNo,    /* TFFS drive number */
    int    offset,    /* offset in the flash chip/card */
    char * filename   /* binary format of the bootimage */
}
```

Arguments:

- **driveNo**
  - the same drive number as the one used as input to the format routine.

- **offset**
  - actual offset from the start of flash at which the image is written (most often specified as zero).

- **filename**
  - pointer to the boot image (bootApp or boot ROM image).

**WARNING:** Because `tffsBootImagePut()` lets you write directly to any area of flash, it is possible to accidentally overwrite and corrupt the TrueFFS-managed area of flash. For more information about how to carefully use this utility, see the reference entry for `tffsBootImagePut()` in the VxWorks API reference.

**NOTE:** For a detailed description of the `bootImagePut()` routine, see the comments in `installDir/vxworks-6.x/target/src/drv/tffs/tffsConfig.c`.

### 8.3.6 Mounting the Drive

Use the `usrTffsConfig()` routine to mount the VxWorks dosFs file system on a TrueFFS flash drive. This routine is defined in `installDir/vxworks-6.x/target/config/comps/src/usrTffs.c`:

```c
STATUS usrTffsConfig
{
    int    drive,    /* drive number of TFFS */
    int    removable, /* 0 for nonremovable flash media */
    char * fileName   /* mount point */
}
```
Arguments:

drive
specifies the drive number of the TFFS flash drive; valid values are 0 through
the number of socket interfaces in BSP.

removable
specifies whether the media is removable. Use 0 for non-removable, 1 for
removable.

fileName
specifies the mount point, for example, '/tffs0/).

The following example runs usrTffsConfig() to attach a drive to dosFs, and then
runs devs to list all drivers:

```
-> usrTffsConfig 0,0,"/flashDrive0/"
```

```
-> devs
drv name
0 /null
1 /tyCo/0
1 /tyCo/1
5 host:
6 /vio
2 /flashDrive0/
```

Internally, usrTffsConfig() calls other routines, passing the parameters you input.

Among these routines is tffsDevCreate(), which creates a TrueFFS block device on
top of the socket driver. This routine takes, as input, a number (0 through 4) that
identifies the socket driver on top of which to construct the TrueFFS block device.
The tffsDevCreate() call uses this number as an index into the array of FLSocket
structures. This number is visible later to dosFs as the driver number.

After the TrueFFS block device is created, dcacheDevCreate() and then
dosFsDevCreate() are called. This routine mounts dosFs onto the device. After
mounting dosFs, you can read and write from flash memory just as you would
from a standard disk drive.

### 8.3.7 Testing the Drive

One way to test your drive is by copying a text file from the host (or from another
type of storage medium) to the flash file system on the target. Then, copy the file
to the console or to a temporary file for comparison, and verify the content. The
following example is run from the shell:
8.4 Using TrueFFS Shell Commands

This section illustrates using TrueFFS shell commands (with the C interpreter) to access the flash file system. These examples assume that the flash has been configured into the system in the default configuration for the BSPs shown. For detailed information on creating a system with TrueFFS, see 8.2 Overview of Implementation Steps, p.463 and 8.3 Creating a System with TrueFFS, p.464.

Target with a Board-Resident Flash Array and a Boot Image Region

This example uses `sysTffsFormat()` to format board-resident flash, preserving the boot image region. It does not update the boot image, so no call is made to `tffsBootImagePut()` . Then, it mounts the non-removable RFA medium as drive number 0.

At the shell prompt, enter the following commands:

```
-> sysTffsFormat
-> usrTffsConfig 0,0,"/RFA/"
```

Target with a Board-Resident Flash Array and a PCMCIA Slot

This example formats RFA and PCMCIA flash for two drives.

The first lines of this example format the board-resident flash by calling the helper routine, `sysTffsFormat()` , which preserves the boot image region. This example does not update the boot image. It then mounts the drive, numbering it as 0 and

NOTE: The copy command requires the appropriate configuration of dosFs support components. For more information on dosFs, see 7.4 MS-DOS-Compatible File System: dosFs, p.420.
8 Flash File System Support with TrueFFS
8.4 Using TrueFFS Shell Commands

passing 0 as the second argument to \texttt{usrTffsConfig()). Zero is used because RFA is non-removable.

The last lines of the example format PCMCIA flash, passing default format values to \texttt{tffsDevFormat()} for formatting the entire drive. Then, it mounts that drive. Because PCMCIA is removable flash, it passes 1 as the second argument to \texttt{usrTffsConfig()). (See 8.3.6 Mounting the Drive, p.474 for details on the arguments to \texttt{usrTffsConfig()).}

Insert a flash card in the PCMCIA socket. At the shell prompt, enter the following commands:

\begin{verbatim}
  -> sysTffsFormat
  -> usrTffsConfig 0,0,"/RFA/
  -> tffsDevFormat 1,0
  -> usrTffsConfig 1,1,"/PCMCIA1/
\end{verbatim}

**Target with a Board-Resident Flash Array and No Boot Image Region Created**

This example formats board-resident flash using the default parameters to \texttt{tffsDevFormat()}, as described in 8.3.4 Formatting the Flash, p.469. Then, it mounts the drive, passing 0 as the drive number and indicating that the flash is non-removable.

At the shell prompt, enter the following commands:

\begin{verbatim}
  -> tffsDevFormat 0,0
  -> usrTffsConfig 0,0,"/RFA/
\end{verbatim}

**Target with Two PCMCIA Slots**

This example formats PCMCIA flash for two drives. Neither format call preserves a boot image region. Then, it mounts the drives, the first is numbered 0, and the second is numbered 1. PCMCIA is a removable medium.

Insert a flash card in each PCMCIA socket. At the shell prompt, enter the following commands:

\begin{verbatim}
  -> tffsDevFormat 0,0
  -> usrTffsConfig 0,1,"/PCMCIA1/
  -> tffsDevFormat 1,0
  -> usrTffsConfig 1,1,"/PCMCIA2/
\end{verbatim}
9

Error Detection and Reporting

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9.7 Sample Error Record 490
9.1 Introduction

VxWorks provides an error detection and reporting facility to help debug software faults. It does so by recording software exceptions in a specially designated area of memory that is not cleared between warm reboots. The facility also allows for selecting system responses to fatal errors, with alternate strategies for development and deployed systems.

The key features of the error detection and reporting facility are:

- A persistent memory region in RAM used to retain error records across warm reboots.
- Mechanisms for recording various types of error records.
- Error records that provide detailed information about runtime errors and the conditions under which they occur.
- The ability to display error records and clear the error log from the shell.
- Alternative error-handling options for the system’s response to fatal errors.
- Macros for implementing error reporting in user code.

The hook routines described in the edrLib API reference can be used as the basis for implementing custom functionality for non-RAM storage for error records.

For more information about error detection and reporting routines in addition to that provided in this chapter, see the API references for edrLib, edrShow, edrErrLogLib, and edrSysDbgLib.

For information about related facilities, see 5.8 Memory Error Detection, p. 289.

NOTE: This chapter provides information about facilities available in the VxWorks kernel. For information about facilities available to real-time processes, see the corresponding chapter in the VxWorks Application Programmer’s Guide.
9.2 Configuring Error Detection and Reporting Facilities

To use the error detection and reporting facilities:

- VxWorks must be configured with the appropriate components.
- A persistent RAM memory region must be configured, and it must be sufficiently large to hold the error records.
- Optionally, users can change the system’s default response to fatal errors.

9.2.1 Configuring VxWorks

To use the error detection and reporting facility, the kernel must be configured with the following components:

- INCLUDE_EDR_PM
- INCLUDE_EDR_ERRLOG
- INCLUDE_EDR_SHOW
- INCLUDE_EDR_SYSDBG_FLAG

As a convenience, the BUNDLE_EDR component bundle may be used to include all of the above components.

9.2.2 Configuring the Persistent Memory Region

The persistent-memory region is an area of RAM at the top of system memory specifically reserved for an error records. It is protected by the MMU and the VxWorks vmLib facilities. The memory is not cleared by warm reboots, provided a VxWorks 6.x boot loader is used.

A cold reboot always clears the persistent memory region. The pmInvalidate() routine can also be used to explicitly destroy the region (making it unusable) so that it is recreated during the next warm reboot.

The persistent-memory area is write-protected when the target system includes an MMU and VxWorks has been configured with MMU support.

The size of the persistent memory region is defined by the PM_RESERVED_MEM configuration parameter. By default the size is set to six pages of memory.

By default, the error detection and reporting facility uses one-half of whatever persistent memory is available. If no other applications require persistent memory, the component may be configured to use almost all of it. This can be accomplished
by defining EDR_ERRLOG_SIZE to be the size of PM_RESERVED_MEM less the size of one page of memory.

If you increase the size of the persistent memory region beyond the default, you must create a new boot loader with the same PM_RESERVED_MEM value. The memory area between RAM_HIGH_ADRS and sysMemTop() must be big enough to copy the VxWorks boot loader. If it exceeds the sysMemTop() limit, the boot loader may corrupt the area of persistent memory reserved for core dump storage when it loads VxWorks. The boot loader, must therefore be rebuilt with a lower RAM_HIGH_ADRS value.

**WARNING:** If the boot loader is not properly configured (as described above), this could lead into corruption of the persistent memory region when the system boots.

The EDR_RECORD_SIZE parameter can be used to change the default size of error records. Note that for performance reasons, all records are necessarily the same size.

The pmShow() shell command (for the C interpreter) can be used to display the amount of allocated and free persistent memory.

For more information about persistent memory, see 5.6 Reserved Memory, p.286 and the pmLib API reference.

**WARNING:** A VxWorks 6.x boot loader must be used to ensure that the persistent memory region is not cleared between warm reboots. Prior versions of the boot loader may clear this area.

### 9.2.3 Configuring Responses to Fatal Errors

The error detection and reporting facilities provide for two sets of responses to fatal errors. See 9.5 Fatal Error Handling Options, p.486 for information about these responses, and various ways to select one for a runtime system.

### 9.3 Error Records

Error records are generated automatically when the system experiences specific kinds of faults. The records are stored in the persistent memory region of RAM in
a circular buffer. Newer records overwrite older records when the persistent memory buffer is full.

The records are classified according to two basic criteria:

- event type
- severity level

The event type identifies the context in which the error occurred (during system initialization, or in a process, and so on).

The severity level indicates the seriousness of the error. In the case of fatal errors, the severity level is also associated with alternative system’s responses to the error (see 9.5 Fatal Error Handling Options, p. 486).

The event types are defined in Table 9-1, and the severity levels in Table 9-2.

**Table 9-1**

<table>
<thead>
<tr>
<th>Type</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>INIT</td>
<td>System initialization events.</td>
</tr>
<tr>
<td>BOOT</td>
<td>System boot events.</td>
</tr>
<tr>
<td>REBOOT</td>
<td>System reboot (warm boot) events.</td>
</tr>
<tr>
<td>KERNEL</td>
<td>VxWorks kernel events.</td>
</tr>
<tr>
<td>INTERRUPT</td>
<td>Interrupt handler events.</td>
</tr>
<tr>
<td>RTP</td>
<td>Process environment events.</td>
</tr>
<tr>
<td>USER</td>
<td>Custom events (user defined).</td>
</tr>
</tbody>
</table>

**Table 9-2**

<table>
<thead>
<tr>
<th>Severity Level</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>FATAL</td>
<td>Fatal event.</td>
</tr>
<tr>
<td>NONFATAL</td>
<td>Non-fatal event.</td>
</tr>
<tr>
<td>WARNING</td>
<td>Warning event.</td>
</tr>
<tr>
<td>INFO</td>
<td>Informational event.</td>
</tr>
</tbody>
</table>
The information collected depends on the type of events that occurs. In general, a complete fault record is recorded. For some events, however, portions of the record are excluded for clarity. For example, the record for boot and reboot events exclude the register portion of the record.

Error records hold detailed information about the system at the time of the event. Each record includes the following generic information:

- date and time the record was generated
- type and severity
- operating system version
- task ID
- process ID, if the failing task in a process
- task name
- process name, if the failing task is in a process
- source file and line number where the record was created
- a free form text message

It also optionally includes the following architecture-specific information:

- memory map
- exception information
- processor registers
- disassembly listing (surrounding the faulting address)
- stack trace

### 9.4 Displaying and Clearing Error Records

The $\texttt{edrShow}$ library provides a set of commands for the shell’s C interpreter that are used for displaying the error records created since the persistent memory region was last cleared. See Table 9-3.

Table 9-3  Shell Commands for Displaying Error Records

<table>
<thead>
<tr>
<th>Command</th>
<th>Action</th>
</tr>
</thead>
<tbody>
<tr>
<td>$\texttt{edrShow()}$</td>
<td>Show all records.</td>
</tr>
<tr>
<td>$\texttt{edrFatalShow()}$</td>
<td>Show only $\texttt{FATAL}$ severity level records.</td>
</tr>
<tr>
<td>$\texttt{edrInfoShow()}$</td>
<td>Show only $\texttt{INFO}$ severity level records.</td>
</tr>
</tbody>
</table>
The shell’s command interpreter provides comparable commands. See the API references for the shell, or use the `help edr` command.

In addition to displaying error records, each of the show commands also displays the following general information about the error log:

- total size of the log
- size of each record
- maximum number of records in the log
- the CPU type
- a count of records missed due to no free records
- the number of active records in the log
- the number of reboots since the log was created

See the `edrShow` API reference for more information.
9.5 Fatal Error Handling Options

In addition to generating error records, the error detection and reporting facility provides for two modes of system response to fatal errors for each event type:

- debug mode, for lab systems (development)
- deployed mode, for production systems (field)

The difference between these modes is in their response to fatal errors in processes (RTP events). In debug mode, a fatal error in a process results in the process being stopped. In deployed mode, as fatal error in a process results in the process being terminated.

The operative error handling mode is determined by the system debug flag (see 9.5.2 Setting the System Debug Flag, p.487). The default is deployed mode.

Table 9-4 describes the responses in each mode for each of the event types. It also lists the routines that are called when fatal records are created.

Developers can modify the routines in this file to implement different system responses to fatal errors. The names of the routines, however, cannot be changed.

Table 9-4  FATAL Error-Handling Options

<table>
<thead>
<tr>
<th>Event Type</th>
<th>Debug Mode</th>
<th>Deployed Mode</th>
<th>Error Handling Routine</th>
</tr>
</thead>
<tbody>
<tr>
<td>INIT</td>
<td>Reboot</td>
<td>Reboot</td>
<td>edrInitFatalPolicyHandler( )</td>
</tr>
<tr>
<td>KERNEL</td>
<td>Stop failed task</td>
<td>Stop failed task</td>
<td>edrKernelFatalPolicyHandler( )</td>
</tr>
<tr>
<td>INTERRUPT</td>
<td>Reboot</td>
<td>Reboot</td>
<td>edrInterruptFatalPolicyHandler( )</td>
</tr>
<tr>
<td>RTP</td>
<td>Stop process</td>
<td>Delete process</td>
<td>edrRtpFatalPolicyHandler( )</td>
</tr>
</tbody>
</table>

Note that when the debugger is attached to the target, it gains control of the system before the error-handling option is invoked, thus allowing the system to be debugged even if the error-handling option calls for a reboot.
9.5.1 Configuring VxWorks with Error Handling Options

In order to provide the option of debug mode error handling for fatal errors, VxWorks must be configured with the `INCLUDE_EDR_SYSDBG_FLAG` component, which it is by default. The component allows a system debug flag to be used to select debug mode, as well as reset to deployed mode (see 9.5.2 Setting the System Debug Flag, p.487). If `INCLUDE_EDR_SYSDBG_FLAG` is removed from VxWorks, the system defaults to deployed mode (see Table 9-4).

9.5.2 Setting the System Debug Flag

How the error detection and reporting facility responds to fatal errors, beyond merely recording the error, depends on the setting of the system debug flag. When the system is configured with the `INCLUDE_EDR_SYSDBG_FLAG` component, the flag can be used to set the handling of fatal errors to either debug mode or deployed mode (the default).

For systems undergoing development, it is obviously desirable to leave the system in a state that can be more easily debugged; while in deployed systems, the aim is to have them recover as best as possible from fatal errors and continue operation.

The debug flag can be set in any of the following ways:

- Statically, with boot loader configuration.
- Interactively, at boot time.
- Programmatically, using APIs in application code.

When a system boots, the banner displayed on the console displays information about the mode defined by the system debug flag. For example:

```
ED&R Policy Mode: Deployed
```

The modes are identified as **Debug**, **Deployed**, or **Permanently Deployed**. The latter indicates that the `INCLUDE_EDR_SYSDBG_FLAG` component is not included in the system, which means that the mode is deployed and that it cannot be changed to debug.

For more information, see the following sections and the API reference for `edrSysDbgLib`. 
Setting the Debug Flag Statically

The system can be set to either debug mode or deployed mode with the f boot loader parameter when a boot loader is configured and built. The value of 0x00 is used to select deployed mode. The value of 0x400 is used to select debug mode. By default, it is set to deployed mode.

To change the default behavior when configuring a new system, change the f parameter in the DEFAULT_BOOT_LINE definition in installDir/vxworks-6.x/target/config/bspName/config.h, which looks like this:

```
#define DEFAULT_BOOT_LINE BOOT_DEV_NAME \
  "(0,0)wrSbc8260:vxworks " \
  "e=192.168.100.51 " \
  "h=192.168.100.155 " \
  "g=0.0.0.0 " \
  "u=anonymous pw=user " \
  "f=0x00 tn=wrSbc8260"
```

For information about configuring and building boot loaders, see 2.4 Boot Loader, p.12.

Setting the Debug Flag Interactively

To change the system debug flag interactively, stop the system when it boots. Then use the c command at the boot-loader command prompt. Change the value of the f parameter: use 0x000 for deployed mode (the default) or to 0x400 for debug mode.

Setting the Debug Flag Programmatically

The state of the system debug flag can also be changed in user code with the edrSysDbgLib API.
9.6 Using Error Reporting APIs in Application Code

The `edrLib.h` file provides a set of convenient macros that developers can use in their source code to generate error messages (and responses by the system to fatal errors) under conditions of the developers choosing.

The macros have no effect if VxWorks has not been configured with error detection and reporting facilities. Code does not, therefore, need to be conditionally compiled to make use of these facilities.

The `edrLib.h` file is in `installDir/vxworks-6.x/target/h`.

The following macros are provided:

- **EDR_USER_INFO_INJECT (trace, msg)**
  Creates a record in the error log with an event type of USER and a severity of INFO.

- **EDR_USER_WARNING_INJECT (trace, msg)**
  Creates a record in the error log with event type of USER and a severity of WARNING.

- **EDR_USER_FATAL_INJECT (trace, msg)**
  Creates a record in the error log with event type of USER and a severity of FATAL.

All the macros use the same parameters. The `trace` parameter is a boolean value indicating whether or not a traceback should be generated for the record. The `msg` parameter is a string that is added to the record.
9.7 Sample Error Record

The following is an example of a record generated by a failed kernel task:

```
sev=fatal task=kernel
inj=excArchLib.c:2523
```

```
Severity/Facility: FATAL/KERNEL
Boot Cycle: 1
OS Version: 6.0.0
Time: THU JAN 01 05:15:07 1970 (ticks = 1134446)
Task: "kernelTask" (0x0068c6c8)
Injection Point: excArchLib.c:2523

fatal kernel task-level exception!

```

```
<<<Memory Map>>>>>

0x00100000 -> 0x002a48dc: kernel

```

```
<<<Exception Information>>>>>

data access
Exception current instruction address: 0x002110cc
Machine Status Register: 0x0000b032
Data Access Register: 0x50000000
Condition Register: 0x20000080
Data storage interrupt Register: 0x40000000

```

```
<<<Registers>>>>>

r0 = 0x00210ff8 sp = 0x006e0f50 r2 = 0x00000000
r3 = 0x00213a10 r4 = 0x00003032 r5 = 0x00000001
r6 = 0x0068c6c8 r7 = 0x0000003a r8 = 0x00000000
r9 = 0x0000000000 r10 = 0x00000002 r11 = 0x00000002
r12 = 0x00000007f r13 = 0x00000000 r14 = 0x00000000
r15 = 0x0000000000 r16 = 0x00000000 r17 = 0x00000000
r18 = 0x0000000000 r19 = 0x00000000 r20 = 0x00000000
r21 = 0x0000000000 r22 = 0x00000000 r23 = 0x00000000
r24 = 0x0000000000 r25 = 0x00000000 r26 = 0x00000000
r27 = 0x0000000000 r28 = 0x00000000 r29 = 0x006e0f74
r30 = 0x0000000000 r31 = 0x50000000 msr = 0x00000b032
lr = 0x00210ff8 ctr = 0x0024046c pc = 0x0022110cc
sr = 0x20000000 xer = 0x20000000 pgTblPtr = 0x00481000
sCrTb1Ptr = 0x0047fe4c srTb1Ptr = 0x0047fe4c

```

```
<<<Disassembly>>>>>

0x2110ac 2c0b0004 cmpi crf0,0,r11,0x4 # 4
0x2110b0 41820024 bc 0xc,2, 0x21110d # 0x0021110d4
0x2110b4 2c0b0008 cmpi crf0,0,r11,0x8 # 8
0x2110b8 41820030 bc 0xc,2, 0x21110e8 # 0x0021110e8
0x2110bc 4800004c b 0xc211108 # 0x00211108
0x2110c0 3c600021 lis r3,0x21 # 33
0x2110c4 83e1001c lwz r31,28(r1)
0x2110c8 38633a10 addi r3,r3,0x3a10 # 14864
*0x2110cc a09f0000 lhz r4,0(r31)

```
9 Error Detection and Reporting
9.7 Sample Error Record

0x2110d0  48000048  b  0x211118 # 0x00211118
0x2110d4  83e1001c  lwz  r31,28(r1)
0x2110d8  3c600021  lis  r3,0x21 # 33
0x2110dc  38633a15  addi  r3,r3,0x3a15 # 14869
0x2110e0  80f00000  lwz  r4,0(r31)
0x2110e4  48000034  b  0x211118 # 0x00211118
0x2110e8  83e1001c  lwz  r31,28(r1)

<<<<<<Traceback>>>>>

0x0011047c vxTaskEntry +0x54 : 0x002111244 ()
0x00211258 d +0x18 : memoryDump ()

<<<<<<Traceback>>>>>
10 Target Tools

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10.5 Show Routines 538
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10.7 Common Problems 556
10.1 Introduction

The Wind River host development environment provides tools that reside and execute on the host machine. This approach conserves target memory and resources. However, there are many situations in which it is desirable to make use of target-resident facilities: a target-resident shell, kernel object-module loader, debug facilities, and system symbol table. The uses for these target-resident tools include the following:

- Debugging a deployed system over a serial connection.
- Developing and debugging network protocols, where it is useful to see the target’s view of a network.
- Loading kernel modules from a target disk, from ROMFS, or over the network, and running them interactively (or programmatically).

The target based tools are partially independent of each other. For example, the kernel shell may be used without the kernel object-module loader, and vice versa. However, for any of the other individual tools to be completely functional, the system symbol table is required.

In some situations, it may be useful to use both the host-resident development tools and the target-resident tools at the same time. In this case, additional facilities are required so that both environments maintain consistent views of the system. For more information, see 10.4.5 Synchronizing Host and Kernel Modules List and Symbol Table, p.537.

For the most part, the target-resident facilities work the same as their host development environment counterparts. For more information, see the appropriate chapters of the Wind River Workbench User’s Guide and the VxWorks Command-Line Tools User’s Guide.

This chapter describes the target-resident kernel shell, kernel object-module loader, debug facilities, and system symbol table. It also provides an overview of the most commonly used VxWorks show routines, which are executed from the shell. In addition, it describes the WDB target agent. WDB is a target-resident, runtime facility required for connecting host tools with a VxWorks target system.
10.2 Kernel Shell

For the most part, the target-resident kernel shell works the same as the host shell (also known as WindSh—for Wind Shell). The kernel shell, however, supports only the C interpreter and command interpreter (see 10.2.2 Kernel and Host Shell Differences, p.496 for information about other differences).

For detailed information about the host shell and the shell interpreters, see the VxWorks Command-Line Tools User’s Guide: Debugging Applications with the Host Shell and Host Shell Commands and Options chapters, and the online Wind River Host Shell API Reference.

Multiple kernel shell sessions may be run simultaneously, which allows for simultaneous access to the target from the host console and remote connections made with telnet or rlogin.

10.2.1 C Interpreter and Command Interpreter

The kernel shell includes both a C interpreter and a command interpreter. The command interpreter provides the fullest set of facilities for starting, monitoring, and debugging real-time processes. The facilities of the C interpreter are limited in this regard, and are designed primarily for working with the kernel and kernel-based applications.

See the online Wind River Host Shell API Reference for a detailed description of the C and command interpreter commands. Also see the VxWorks Command-Line Tools User’s Guide: Debugging Applications with the Host Shell and Host Shell Commands and Options chapters for discussions and examples of interpreter use.

Developers can add new commands to the command interpreter and create their own interpreters for the kernel shell (see 10.2.18 Adding Custom Commands to the Command Interpreter, p.510 and 10.2.19 Creating a Custom Interpreter, p.515).

---

1. In versions of VxWorks prior to 6.0, the kernel shell was called the target shell. The new name reflects the fact that the target-resident shell runs in the kernel and not in a process.
10.2.2 Kernel and Host Shell Differences

The major differences between the target and host shells are:

- The host and kernel shells do not provide exactly the same set of commands. The kernel shell, for example, has commands related to network, shared data, environment variables, and some other facilities that are not provided by the host shell. However, the host and kernel shells provide a very similar set of commands for their command and C interpreters.

- Each shell has its own distinct configuration parameters, as well as those that are common to both.

- Both shells include a command and a C interpreter. The host shell also provides a Tcl interpreter and a gdb interpreter. The gdb interpreter has about 40 commands and is intended for debugging processes (RTPs); and it references host file system paths.

- For the host shell to work, VxWorks must be configured with the WDB target agent component. For the kernel shell to work, VxWorks be configured with the kernel shell component, as well as the target-resident symbol tables component.

- The host shell can perform many control and information functions entirely on the host, without consuming target resources.

- The kernel shell does not require any Wind River host tool support.

- The host shell uses host system resources for most functions, so that it remains segregated from the target. This means that the host shell can operate on the target from the outside, whereas the kernel shell is part of the VxWorks kernel. For example, because the kernel shell task is created with the \texttt{taskSpawn()} \texttt{VX\_UNBREAKABLE} option, it is not possible to set breakpoints on a function executed within the kernel shell task context. Therefore, the user must create a new task, with \texttt{sp()}, to make breakable calls. For example, from the kernel shell you must do this:

  -> b printf
  -> sp printf, "Test\n"

Whereas from the host shell you can do this:

  -> b printf
  -> printf ("Test\n")

Conflicts in task priority may also occur while using the kernel shell.
The kernel shell has its own set of terminal-control characters, unlike the host shell, which inherits its setting from the host window from which it was invoked. (See 10.2.7 Using Kernel Shell Control Characters, p.503.)

The kernel shell correctly interprets the tilde operator in pathnames for UNIX and Linux host systems (or remote file systems on a UNIX or Linux host accessed with ftp, rsh, NFS, and so on), whereas the host shell cannot. For example, the following command executed from the kernel shell (with the C interpreter) by user panloki would correctly locate the kernel module /home/panloki/foo.o on the host system and load it into the kernel:

```
- > ld < ~/foo.o
```

When the kernel shell encounters a string literal (“...”) in an expression, it allocates space for the string, including the null-byte string terminator, plus some additional overhead. The value of the literal is the address of the string in the newly allocated storage. For example, the following expression allocates 12-plus bytes from the target memory pool, enters the string in that memory (including the null terminator), and assigns the address of the string to x:

```
- > x = "hello there"
```

The following expression can be used to return the memory to the target memory pool (see the memLib reference entry for information on memory management):

```
- > free (x)
```

Furthermore, even when a string literal is not assigned to a symbol, memory is still permanently allocated for it. For example, the following expression uses memory that is never freed:

```
- > printf ("hello there")
```

This is because if strings were only temporarily allocated, and a string literal was passed to a routine being spawned as a task, by the time the task executed and attempted to access the string, the kernel shell would have already released (and possibly even reused) the temporary storage where the string was held.

---

2. The amount of memory allocated is rounded up to the minimum allocation unit for the architecture in question, plus the amount for the header for that block of memory.
If the accumulation of memory used for strings has an adverse effect on performance after extended development sessions with the kernel shell, you can use the `strFree()` routine (with the C interpreter) or the equivalent `string free` command (with the command interpreter).

The host shell also allocates memory on the target if the string is to be used there. However, it does not allocate memory on the target for commands that can be performed at the host level (such as `lkup()`, `ld()`, and so on).

### 10.2.3 Configuring VxWorks With the Kernel Shell

The functionality of the kernel shell is provided by a suite of components, some of which are required, and others of which are optional.

#### Required Components

To use the kernel shell, you must configure VxWorks with the `INCLUDE_SHELL` component. The configuration parameters for this component are described in Table 10-1.

You must also configure VxWorks with components for symbol table support, using either the `INCLUDE_STANDALONE_SYM_TBL` or `INCLUDE_NET_SYM_TBL` component. For information about configuring VxWorks with symbol tables, see 10.4.1 Configuring VxWorks with Symbol Tables, p.532.

#### Table 10-1 INCLUDE_SHELL Configuration Parameters

<table>
<thead>
<tr>
<th>Configuration Parameter</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><code>SHELL_SECURE</code></td>
<td>Access the kernel shell attached to the console through a login access.</td>
</tr>
<tr>
<td><code>SHELL_STACK_SIZE</code></td>
<td>Default stack size of kernel shell task.</td>
</tr>
<tr>
<td><code>SHELL_TASK_NAME_BASE</code></td>
<td>Default basename for the kernel shell tasks.</td>
</tr>
<tr>
<td><code>SHELL_TASK_PRIORITY</code></td>
<td>Priority of the kernel shell tasks.</td>
</tr>
<tr>
<td><code>SHELL_TASK_OPTIONS</code></td>
<td>Spawning options for the kernel shell tasks.</td>
</tr>
<tr>
<td><code>SHELL_START_AT_BOOT</code></td>
<td>The kernel shell is launched automatically at boot time on the console.</td>
</tr>
</tbody>
</table>
Optional Components

Table 10-2 describes components that provide additional shell functionality.

<table>
<thead>
<tr>
<th>Component</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>INCLUDE_DEBUG</td>
<td>Debugging facilities, such as disassembly, task stack trace, setting a breakpoint, stepping, and so on.</td>
</tr>
<tr>
<td>INCLUDE_SHELL_BANNER</td>
<td>Display the shell banner on startup.</td>
</tr>
<tr>
<td>INCLUDE_SHELL_VI_MODE</td>
<td>Editing mode similar to the vi editing mode.</td>
</tr>
<tr>
<td>INCLUDE_SHELL_EMACS_MODE</td>
<td>Editing mode similar to the emacs editing mode.</td>
</tr>
<tr>
<td>INCLUDE_SHELL_INTERP_C</td>
<td>C interpreter for the kernel shell.</td>
</tr>
<tr>
<td>INCLUDE_SHELL_INTERP_CMD</td>
<td>Command interpreter for the kernel shell.</td>
</tr>
<tr>
<td>INCLUDE_STARTUP_SCRIPT</td>
<td>Kernel shell startup script facility.</td>
</tr>
</tbody>
</table>
Table 10-3 describes components that provide additional command interpreter functionality. They must be used with the INCLUDE_SHELL_INTERP_CMD component (described above in Table 10-2).

<table>
<thead>
<tr>
<th>Component</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>INCLUDE_DISK_UTIL_SHELL_CMD</td>
<td>File system shell commands.</td>
</tr>
<tr>
<td>INCLUDE_EDR_SHELL_CMD</td>
<td>Error detection and reporting shell commands.</td>
</tr>
<tr>
<td>INCLUDE_TASK_SHELL_CMD</td>
<td>Task shell commands.</td>
</tr>
<tr>
<td>INCLUDE_DEBUG_SHELL_CMD</td>
<td>Debug shell commands.</td>
</tr>
<tr>
<td>INCLUDE_SYM_SHELL_CMD</td>
<td>Symbol shell commands.</td>
</tr>
<tr>
<td>INCLUDE_VM_SHOW_SHELL_CMD</td>
<td>Virtual memory show shell commands.</td>
</tr>
<tr>
<td>INCLUDE_ADR_SPACE_SHELL_CMD</td>
<td>Address space shell commands.</td>
</tr>
<tr>
<td>INCLUDE_SHARED_DATA_SHOW_SHELL_CMD</td>
<td>Shared data show shell commands.</td>
</tr>
<tr>
<td>INCLUDE_MEM_EDR_SHELL_CMD</td>
<td>Memory detection and reporting shell commands.</td>
</tr>
<tr>
<td>INCLUDE_MEM_EDR_RTP_SHELL_CMD</td>
<td>Memory detection and reporting shell commands for processes (RTPs).</td>
</tr>
<tr>
<td>INCLUDE_MODULE_SHELL_CMD</td>
<td>Kernel loader shell command.</td>
</tr>
<tr>
<td>INCLUDE_UNLOADER_SHELL_CMD</td>
<td>Kernel unloader shell command.</td>
</tr>
<tr>
<td>INCLUDE_SHL_SHELL_CMD</td>
<td>Shared library commands for processes.</td>
</tr>
<tr>
<td>INCLUDE_RTP_SHELL_CMD</td>
<td>Process shell commands.</td>
</tr>
<tr>
<td>INCLUDE_RTP_SHOW_SHELL_CMD</td>
<td>Process show shell commands.</td>
</tr>
</tbody>
</table>
Additional components that are useful are the following:

**INCLUDE_DISK_UTIL**
Provides file utilities, such as `ls` and `cd` (it is required by INCLUDE_DISK_UTIL_SHELL_CMD).

**INCLUDE_SYM_TBL_SHOW**
Provides symbol table show routines, such as `lkup`.

It can also be useful to include components for the kernel object-module loader and unloader (see 10.3.1 Configuring VxWorks with the Kernel Object-Module Loader, p.520). These components are required for the `usrLib` commands that load modules into, and unload modules from, the kernel (see 10.2.9 Loading and Unloading Kernel Object Modules, p.504).

Note that the BUNDLE_STANDALONE_SHELL and BUNDLE_NET_SHELL component bundles are also available to provide for a standalone kernel shell or a networked kernel shell.

### 10.2.4 Configuring the Kernel Shell

The kernel shell can be configured statically with various VxWorks component parameter options (as part of the configuration and build of the operating stem), as well as configured dynamically from the shell terminal for a shell session.

The default configuration is defined for all shell sessions of the system with the component parameter `SHELL_DEFAULT_CONFIG`. However, the configuration for the initial shell session launched at boot time can be set differently with the `SHELL_FIRST_CONFIG` parameter, and the configuration for remote sessions (telnet or rlogin) can be set with `SHELL_REMOTE_CONFIG`.

Each of these component parameters provide various sets of shell configuration variables that can be set from the command line. These include `INTERPRETER`, `LINE>Edit_MODE`, `VXE_PATH`, `AUTOLOGOUT`, and so on.

Some of the configuration variables are dependent on the inclusion of other VxWorks components in the operating system. For example, `RTP_CREATE_STOP` is only available if VxWorks is configured with process support and the command interpreter component (INCLUDE_RTP and INCLUDE_SHELL_INTERP_CMD).

With the C interpreter, `shConfig()` can be used to reconfigure the shell interactively. Similarly, using the command interpreter, the shell configuration can be displayed and changed with the `set config` command.
Some useful configuration variables are:

**INTERPRETER**
Identify the interpreter, either C or Cmd. The default is the first interpreter registered (the C interpreter).

**LINE_EDIT_MODE**
Set the line edit mode, either emacs or vi. The default is the first line edit mode style registered (vi mode).

**LINE_LENGTH**
Set the shell line length (it cannot be changed dynamically). The default is 256 characters.

### 10.2.5 Starting the Kernel Shell

The kernel shell starts automatically after VxWorks boots, by default. If a console window is open over a serial connection, the shell prompt appears after the shell banner.

For information about booting VxWorks, and starting a console window, see the *Wind River Workbench User’s Guide: Setting up Your Hardware*.

The shell component parameter `SHELL_START_AT_BOOT` controls if an initial shell session has to be started (TRUE) or not (FALSE). Default is TRUE. If set to FALSE, the shell session does not start. It is up to the user to start it either programmatically (from an application), from the host shell, from a telnet or rlogin shell session or from the *wtxConsole* (a host tool). Use `shellInit()` or `shellGenericInit()` to start a shell session.

Note that when a user calls a routine from the kernel shell, the routine is executed in the context of the shell task. So that if the routine hangs, the shell session will hang as well.

### 10.2.6 Using Kernel Shell Help

For either the C or the command interpreter, the `help` command displays the basic set of interpreter commands.

See the online *Wind River Host Shell API Reference* for a detailed description of the C and command interpreter commands. Also see the *VxWorks Command-Line Tools User’s Guide: Debugging Applications with the Host Shell and Host Shell Commands and Options* chapters for discussions and examples of interpreter use.
10.2.7 Using Kernel Shell Control Characters

The kernel shell has its own set of terminal-control characters, unlike the host shell, which inherits its setting from the host window from which it was invoked. Table 10-4 lists the kernel shell’s terminal-control characters. The first four of these are defaults that can be mapped to different keys using routines in *tyLib* (see also *tty Special Characters*, p. 349).

<table>
<thead>
<tr>
<th>Command</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>CTRL+C</td>
<td>Aborts and restarts the shell. However, if a process is launched with the command interpreter (using <em>rtp exec</em>), the function of CTRL+C changes. It is used to interrupt the process.</td>
</tr>
<tr>
<td>CTRL+D</td>
<td>Logs out when the terminal cursor is at the beginning of a line.</td>
</tr>
<tr>
<td>CTRL+H</td>
<td>Deletes a character (backspace).</td>
</tr>
<tr>
<td>CTRL+Q</td>
<td>Resumes output.</td>
</tr>
<tr>
<td>CTRL+S</td>
<td>Temporarily suspends output.</td>
</tr>
<tr>
<td>CTRL+U</td>
<td>Deletes an entire line.</td>
</tr>
<tr>
<td>CTRL+W</td>
<td>If a process is launched with the command interpreter (using <em>rtp exec</em>), this key sequence suspends the process running in the foreground.</td>
</tr>
<tr>
<td>CTRL+X</td>
<td>Reboots (trap to the ROM monitor).</td>
</tr>
<tr>
<td>ESC</td>
<td>Toggles between input mode and edit mode (<em>vi</em> mode only).</td>
</tr>
</tbody>
</table>

The shell line-editing commands are the same as they are for the host shell. See the *ledLib* API references.

10.2.8 Defining Kernel Shell Command Aliases

Aliases can be created for shell commands, as with a UNIX shell. They can be defined programatically using the *shellCmdAliasAdd()* and
shellCmdAliasArrayAdd() routines (see Sample Custom Commands, p.515 for examples).

For information about creating command aliases interactively, see the VxWorks Command-Line Tools User’s Guide.

10.2.9 **Loading and Unloading Kernel Object Modules**

Kernel object modules can be dynamically loaded into a running VxWorks kernel with the target-resident loader. For information about configuring VxWorks with the loader, and about its use, see 10.3 Kernel Object-Module Loader, p.519.

**NOTE:** For information about working with real-time processes from the shell, see the VxWorks Command-Line Tools User’s Guide: Debugging Applications with the Host Shell and Host Shell Commands and Options chapters, the VxWorks Application Programmer’s Guide: Applications and Processes, and the online Wind River Host Shell API Reference.

The following is a typical load command from the shell, in which the user downloads appl.o using the C interpreter:

```
  -> ld < /home/panloki/appl.o
```

The ld() command loads an object module from a file, or from standard input into the kernel. External references in the module are resolved during loading.

Once an application module is loaded into target memory, subroutines in the module can be invoked directly from the shell, spawned as tasks, connected to an interrupt, and so on. What can be done with a routine depends on the flags used to download the object module (visibility of global symbols or visibility of all symbols).

Modules can be reloaded with reld(), which unloads the previously loaded module of the same name before loading the new version. Modules can be unloaded with unld().

For more information about ld, see the VxWorks API reference for usrLib. For more information about reld() and unld(), see the VxWorks API reference for unldLib. Note that these routines are meant for use from the shell only; they cannot be used programmatically.

Undefined symbols can be avoided by loading modules in the appropriate order. Linking independent files before download can be used to avoid unresolved references if there are circular references between them, or if the number of
modules is unwieldy. The static linker ldarch can be used to link interdependent files, so that they can only be loaded and unloaded as a unit. (See *Statically Linking Kernel-Based Application Modules*, p. 64)

Unloading a code module releases all of the resources used when loading the module, as far as that is possible. This includes removing symbols from the target’s symbol table, removing the module from the list of modules loaded in the kernel, removing the text, data, and bss segments from the kernel memory they were stored in, and freeing that memory. It does not include freeing memory or other resources (such as semaphores) that were allocated or created by the module itself while it was loaded.

### 10.2.10 Debugging with the Kernel Shell

The kernel shell includes the same task-level debugging utilities for kernel space as the host shell if VxWorks has been configured with the INCLUDE_DEBUG component. For details on the debugging commands available, see the *VxWorks Command-Line Tools User’s Guide: Debugging Applications with the Host Shell* and *Host Shell Commands and Options* chapters, and the online *Wind River Host Shell API Reference*.

You cannot use system mode debug utilities with the kernel shell.

### 10.2.11 Aborting Routines Executing from the Kernel Shell

Occasionally it is desirable to abort the shell’s evaluation of a statement. For example, an invoked routine can loop excessively, suspend, or wait on a semaphore. This can happen because of errors in the arguments specified in the invocation, errors in the implementation of the routine, or oversight regarding the consequences of calling the routine. In such cases it is usually possible to abort and restart the kernel shell task. This is done by pressing the special target-shell abort character on the keyboard, CTRL+C by default. This causes the kernel shell task to restart execution at its original entry point. Note that the abort key can be changed to a character other than CTRL+C by calling `tyAbortSet()`.

When restarted, the kernel shell automatically reassigns its system standard input and output streams to the original assignments they had when the kernel shell was first spawned. Thus any kernel shell redirections are canceled, and any executing shell scripts are aborted.
The abort facility works only if the following are true:

- `excTask()` is running.
- The driver for the particular keyboard device supports it (all VxWorks-supplied drivers do).

Also, you may occasionally enter an expression that causes the kernel shell to incur a fatal error such as a bus/address error or a privilege violation. Such errors normally result in the suspension of the offending task, which allows further debugging.

However, when such an error is incurred by the kernel shell task, VxWorks automatically restarts the kernel shell, because further debugging is impossible without it. Note that for this reason, as well as to allow the use of breakpoints and single-stepping, it is often useful when debugging to spawn a routine as a task instead of just calling it directly from the kernel shell.

When the kernel shell is aborted for any reason, either because of a fatal error or because it is aborted from the terminal, a task trace is displayed automatically. This trace shows where the kernel shell was executing when it died.

Note that an offending routine can leave portions of the system in a state that may not be cleared when the kernel shell is aborted. For instance, the kernel shell might have taken a semaphore, which cannot be given automatically as part of the abort.

### 10.2.12 Console Login Security

Console login security can be provided for the kernel shell by adding the `INCLUDE_SECURITY` component to the VxWorks configuration. In addition, the shell’s `SHELL_SECURE` component parameter must be set to `TRUE` (it is set to `FALSE` by default).

With this configuration, the shell task is not launched at startup. Instead, a login task runs on the console, waiting for the user to enter a valid login ID and password. After validation of the login, the shell task is launched for the console.

When the user logs out from the console, the shell session is terminated, and a new login task is launched.

Also see *Remote Login Security*, p.508.
10.2.13 Using a Remote Login to the Kernel Shell

Users can log into a VxWorks system with telnet and rlogin and use the kernel shell, provided that VxWorks has been configured with the appropriate components. VxWorks can also be configured with a remote-login security feature that imposes user ID and password constraints on access to the system.

Note that VxWorks does not support rlogin access from the VxWorks system to the host.

Remote Login With telnet and rlogin

When VxWorks is first booted, the shell’s terminal is normally the system console. You can, however, use telnet to access the kernel shell from a host over the network if VxWorks is built with the INCLUDE_TELNET component (which can be configured with the TELNETD_MAX_CLIENTS parameter). This component creates the tTelnetd task when the system boots. It is possible to start several shells for different network connections. (Remote login is also available with the wtxConsole tool.)

To access the kernel shell over the network, use the telnet command with the name of the target VxWorks system. For example:

```
% telnet myVxBox
```

UNIX host systems can also use rlogin to access to the kernel shell from the host. VxWorks must be configured with the INCLUDE_RLOGIN component to create the tRlogind task.

To end an rlogin connection to the shell, you can do any of the following:

- Use the CTRL+D key combination.
- Use the logout() command with the shell’s C interpreter, or the logout command with the command interpreter.
- Type the tilde and period characters at the shell prompt:

```
-> ~.
```
Remote Login Security

VxWorks can be configured with a remote-login security feature that imposes user ID and password constraints on access to the system. The INCLUDE_SECURITY component provides this facility.

A user is then prompted for a login user name and password when accessing the VxWorks target remotely. The default login user name and password provided with the supplied system image is target and password.

The default user name and password can be changed with the loginUserAdd() routine, as follows:

```c
loginUserAdd "fred", "encrypted_password"
```

The default user name and password can be changed with loginUserAdd(), which requires an encrypted password. To create an encrypted password, use the vxencrypt tool on the host system. The tool prompts you to enter a password, and then displays the encrypted version. The user name and password can then be changed with the loginUserAdd() command with the shell’s C interpreter. For example, mysecret is encrypted as bee9QdRzs, and can be used with the user name fred as follows to change the default settings:

```c
loginUserAdd "fred", "bee9QdRzs"
```

To define a group of login names, include a list of loginUserAdd() calls in a startup script and run the script after the system has been booted. Or include the loginUserAdd() calls in usrAppInit(); for information on this regard, see 2.7.9 Configuring VxWorks to Run Applications Automatically, p.68.

NOTE: The values for the user name and password apply only to remote login into the VxWorks system. They do not affect network access from VxWorks to a remote system; See Wind River Network Stack for VxWorks 6 Programmer’s Guide.

The remote-login security feature can be disabled at boot time by specifying the flag bit 0x20 (SYSFLAG_NO_SECURITY) in the flags boot parameter.

Also see 10.2.12 Console Login Security, p.506.
10.2.14 **Launching a Shell Script Programmatically**

A simple way to have a script executed programmatically by an interpreter (the command interpreter for example) is as follows:

```c
fdScript = open("myScript", O_RDONLY);
shellGenericInit("INTERPRETER=Cmd", 0, NULL, &shellTaskName, FALSE, FALSE,
    fdScript, STD_OUT, STD_ERR); do
    taskDelay(sysClkRateGet());
    while (taskIdToId(shellTaskName) != ERROR); close(fdScript);
```

The do/while loop is necessary for waiting for the shell script to terminate.

10.2.15 **Executing Shell Commands Programmatically**

There is no `system()` API as there is for a UNIX operating system. In order to be able to execute shell commands from an application, the same technique as described above can be used. It is not a file handle that is passed to the `shellGenericInit()` API, but a pseudo-device slave file descriptor (see the API reference for the `ptyDrv` library for information about pseudo-devices).

The application writes the commands it wants to be executed into the master side of the pseudo-device. A pseudo-code representation of this might be as follows:

```c
fdSlave = open("system.S", O_RDWR);
fdMaster = open("system.M", O_RDWR);
shellGenericInit("INTERPRETER=Cmd", 0, NULL, &shellTaskName, FALSE, FALSE,
    fdSlave, STD_OUT, STD_ERR);
    taskDelay(sysClkRateGet());
    write(fdMaster, "pwd\n", 4);
    close(fdMaster);
    close(fdSlave);
```

10.2.16 **Accessing Kernel Shell Data Programmatically**

Shell data is available with the `shellDataLib` library. This allows the user to associate data values with a shell session (uniquely per shell session), and to access them at any time. This is useful to maintain default values, such as the memory dump width, the disassemble length, and so on. These data values are not accessible interactively from the shell, only programmatically.
10.2.17 Using Kernel Shell Configuration Variables

Shell configuration variables are available using the `shellConfigLib` library. This allows the user to define default configurations for commands or for the shell itself. Such variables already exist for the shell (see the configuration variables `RTP_CREATE_STOP` or `LINE_EDIT_MODE`). They behave similarly to environment strings in a UNIX shell. These variables can be common to all shell sessions, or local to a shell session. They can be modified and displayed interactively by the shell user with the command `set config` or the shell routine `shConfig()`.

10.2.18 Adding Custom Commands to the Command Interpreter

The kernel shell’s command interpreter consists of a line parser and of a set of commands. It can be extended with the addition of custom commands written in C. (The host shell’s command interpreter can likewise be extended, but with commands written in Tcl.)

The syntax of a command statement is standard shell command-line syntax, similar to that used with the UNIX `sh` shell or the Windows `cmd` shell. The syntax is:

```
command [options] [arguments]
```

Blank characters (such as a space or tab) are valid word separators within a statement. A blank character can be used within an argument string if it is escaped (that is, prefixed with the back-slash character) or if the argument is quoted with double quote characters. The semicolon character is used as a command separator, used for entering multiple commands in a single input line. To be used as part of an argument string, a semicolon must escaped or quoted.

The command parser splits the statement string into a command name string and a string that consists of the options and the arguments. These options and arguments are then passed to the command routine.

The command name may be a simple name (one word, such as `reboot`) or a composite name (several words, such as `task info`). Composite names are useful for creating classes of commands (commands for tasks, commands for processes, and so on).

Options do not need to follow any strict format, but the standard UNIX option format is recommended because it is handled automatically by the command parser. If the options do not follow the standard UNIX format, the command routine must parse the command strings to extract options and arguments. See Defining a Command, p.512 for more information.
The standard UNIX option format string is:

```
-character [extra option argument]
```

The special double-dash option (--) is used in the same way as in UNIX. That is, all elements that follow it are treated as an argument string, and not options. For example, if the command `test` that accepts option `-a`, `-b` and `-f` (the latter with an extra option argument), then the following command sets the three options, and passes the `arg` string as an argument:

```
test -a -b -f arg
```

However, the next command only sets the `-a` option. Because they follow the double-dash, the `-b`, `-f` and `arg` elements of the command are passed to the C routine of the `test` command as strings:

```
test -a -- -b -f file arg
```

The command interpreter only handles strings. As a consequence, the arguments of a command routine are strings as well. It is up to the command routine to transform the strings into numerical values if necessary.

For information about symbol access syntax, see the material on the command interpreter in the *VxWorks Command-Line Tools User's Guide*.

### Creating A New Command

The command interpreter is designed to allow customers to add their own commands to the kernel shell.

Commands are stored in an internal database of the command interpreter. The information describing a command are defined by a C structure that contains:

- The command name.
- A pointer to the C routine for the command.
- The command options string (if needed).
- A short description of the command.
- A full description of the command.
- A usage synopsis.

The command descriptions and the synopsis are used by the `help` command.

A command is registered with the command interpreter database along with a topic name. The topic is used by the `help` command to display related commands. For example, to display all commands related to the memory, you would use the command `help memory`. 
This section describes the conventions used for creating commands and provides information about examples of commands that can serve as models. Also see the shellInterpCmdLib API reference.

It may also be useful to review the other shell documentation in order to facilitate the task of writing new commands. See the shellLib, shellDataLib, and shellConfigLib API references, as well as the material on the command interpreter in the VxWorks Command-Line Tools User’s Guide.

**Defining a Command**

A command is defined for the interpreter by a C structure composed of various strings and a function pointer, with the following elements:

```c
nameStruct = { "cmdFullname",
                "func",
                "opt",
                "shortDesc",
                "fullDesc",
                "%s [ synopsis ]" };
```

The string `cmdFullname` is the name of the command. It may be a composite name, such as `foo bar`. In this case, `foo` is the top-level command name, and `bar` is a sub-command of `foo`. The command name must be unique.

The `func` element is the name of the routine to call for that command name.

The string `opt` can be used in several different ways to define how option input is passed to the command routine:

- If `opt` is not NULL, it describes the possible options that the command accepts. Each option is represented as a single character (note that the parser is case sensitive). If an option takes an argument, a colon character (:) must be added after the option character. For example, the following means that the command accepts the `-a`, `-v`, and `-f arg` options:

  ```c
  avf:
  ```

- If `opt` not NULL, and consists only of a semicolon, the option input is passed to the command routine as a single string. It is up to the routine to extract options and arguments.

- If `opt` is NULL, the parser splits the input line into tokens and passes them as traditional argc/argv parameters to the command routine.

Note that the command routine must be coded in a manner appropriate to how the `opt` string is used in the command-definition structure (see *Writing the Command Routine*, p.513).
The string \textit{shortDesc} is a short description of the command. A sequence of string conversion characters (\texttt{\%s}) within that string is replaced by the command name when the description is displayed. The string should not be ended by a new-line character (\texttt{\n}).

The string \textit{fullDesc} is the full description of the command. A sequence of string conversion characters (\texttt{\%s}) within that string is replaced by the command name when the description is displayed. This description should contain the explanation of the command options. The string should not be ended by newline (\texttt{\n}) character.

The string \textit{synopsis} is the synopsis of the command. A sequence of string conversion characters (\texttt{\%s}) within that string is replaced by the command name when the synopsis is displayed. The string should not be ended by a newline (\texttt{\n}) character.

The description and synopsis strings are used by the command interpreter’s \texttt{help} command.

The rules for the C language command routines associated with the command structures are described in \textit{Writing the Command Routine}, p.513. See \textit{Sample Custom Commands}, p.515 for examples.

\textbf{Writing the Command Routine}

This section describes how to write the C routine for a command, including how the routine should handle command options.

The command definition structure and the command routine must be coordinated, most obviously with regard to the command routine name, but also with regard to the \texttt{opt} element of the structure defining the command:

- If the \texttt{opt} element is not equal to \texttt{NULL}, the declaration of the routine must include the \texttt{options} array:

  ```c
  int func
  {
    SHELL_OPTION options[]    /* options array */
    ...
  }
  ```

  In this declaration, \texttt{options} is a pointer on the argument array of the command.

- However, if the \texttt{opt} element is \texttt{NULL}, the declaration of the routine must include \texttt{argc/argv} elements:
int func
  (int argc, /* number of argument */
   char ** argv /* pointer on the array of arguments */
   ...)  

In this declaration, argc is the number of arguments of the command, and argv is an array that contains the argument strings.

In the first case the parser populates the options[] array.

In the second case it splits and passes the arguments as strings using argc/argv to the routine.

When the opt element is used to define options, the order in which they are listed is significant, because that is the order in which they populate the options[] array. For example, if opt is avf:

- Option a is described by the first cell of the options array: options[0].
- Option v is described by the second cell of the options array: options[1].
- Option f is described by the third cell of the options array: options[2].
- The argument of option f is options[2].string.

Each cell of the options array passed by the parser to func is composed of a boolean value (TRUE if the option is set, FALSE if not) and a pointer to a string (pointer to an argument), if so defined. For example, if -a has been defined, the value of options[0].isSet value is TRUE. Otherwise it is FALSE.

A boolean value indicates if it is the last cell of the array. If the option string opt is only a colon, the argument string of the command is passed to func without any processing, into the string field of the first element of the options array.

The return value of the command routine is an integer. By convention, a return value of zero means that the command has run without error. Any other value indicates an error value.

See Defining a Command, p.512 for information about the command-definition structure. See Sample Custom Commands, p.515 for examples of command structures and routines.

Registering a New Command

The shell commands have to be registered against the shell interpreter. This can be done at anytime; the shell component does not need to be initialized before commands can be registered.
A command is registered in a topic section. The topic name and the topic description must also be registered in the command interpreter database. The routine used to do so is `shellCmdTopicAdd()`. This routine accepts two parameters: a unique topic name and a topic description. The topic name and description are displayed by the help command.

Two routines are used to register commands: `shellCmdAdd()` adds a single command, and `shellCmdArrayAdd()` adds an array of commands.

See *Sample Custom Commands*, p.515 for information about code that illustrates command registration.

**Sample Custom Commands**

For an example of custom command code, see `installDir/vxworks-6.x/target/src/demo/shell/tutorialShellCmd.c`. In addition to illustrating how to implement custom commands, it shows how to create command aliases (also see *10.2.8 Defining Kernel Shell Command Aliases*, p.503).

The code can be used with Wind River Workbench as a downloadable kernel module project, or included in a kernel project. For information about using the IDE, see the *Wind River Workbench User’s Guide*.

It can also be built from the command line with the command `make CPU=cpuType`. For example:

```
make CPU=PENTIUM2
```

This resulting module can then be loaded into the kernel, using the IDE, host shell, kernel shell.

The `tutorialShellCmdInit()` routine must be called to register the commands before they can be executed, regardless of how the code is implemented.

**10.2.19 Creating a Custom Interpreter**

The kernel shell is designed to allow customers to add their own interpreter. Two interpreters are provided by Wind River: the C interpreter and the command interpreter. An interpreter receives the user input line from the shell, validates the input against its syntax and grammar, and performs the action specified by the input line (such as redirection, calling a VxWorks function, reading or writing memory, or any other function that an interpreter might perform).
Within the shell, an interpreter is defined by a set of static information and an
interpreter context.

The static information about the interpreter is:

- an interpreter context initialization routine
- a parsing routine
- an evaluation routine
- a completion routine
- a restart routine
- an interpreter context finalization routine (to release any resources)
- an interpreter name (which must be unique)
- a default interpreter prompt

This information is set when the interpreter is registered with the shell using the
shellInterpRegister() routine.

The interpreter context is created the first time the interpreter is used. It is unique
to each interpreter and to each shell session. When it has been created, the shell
calls the interpreter context initialization routine so that the interpreter can add
any private data that it requires to that context. The pInterpParam field of the
interpreter’s context structure SHELL_INTERP_CTX can be used for that purpose.

Each time a line is entered from terminal or read from a script, the shell calls the
parsing routine of the current interpreter so that it can evaluate the line.

The arguments to the parsing routine are the interpreter context, a pointer to the
input line string, and a boolean value indicating whether or not the shell session is
interactive.

The job of the parsing routine is to split the line into meaningful words, according
to the interpreter’s syntax, and to perform the appropriate actions.

The evaluation routine is called by the shell whenever an evaluation for that
interpreter is required, using the shellInterpEvaluate() routine. Usually, the
evaluation routine and the parsing routine can share most of their code. The
evaluation routine returns an evaluation value.

The completion routine is called by the shell whenever a completion key is hit
(completion keys are defined by the line editing mode of the shell; see the ledLib
API reference). It is up to the interpreter to perform word completion according to
its syntax, and to the position of the cursor in the line.
It is up to the interpreter to release its resource whenever a shell session terminates or is restarted. The finalize and restart routines specified at registration time are called by the shell for this purpose.

The restart routine is called by the shell whenever the shell task is restarted, either because it has taken an exception or it was restarted by the user. This routine is useful for releasing any resources reserved by the interpreter (memory blocks, semaphores, and so on).

The context-finalize routine is called when the shell session is terminated. It is used to free any resources allocated by the interpreter.

Note that the stream redirection characters must be handled by the interpreter itself (for example, the command interpreter’s <, > and >> redirection characters).

The interpreter name is a string that uniquely identifies the interpreter among any others registered with the shell.

The default interpreter prompt is the string that identifies the interpreter visually. For example, the C interpreter prompt is the string:

```
->
```

The interpreter prompt may contain format conversion characters that are dynamically replaced when printed by another string. For example %l is replaced by the current shell session path, %n is replaced by the user name, and so on (see the shellPromptLib API reference for more information). Moreover, it is possible to add new format strings with shellPromptFmtStrAdd( ). For example the command-related process adds the format string %c to display the name of the current working memory context.

The current interpreter is defined by the shell configuration variable named INTERPRETER (the C macro SHELL_CFG_INTERP is defined for it in shellLib.h).

A shell user can switch to its own interpreter by setting the value of the configuration variable INTERPRETER to its interpreter name. This can be done either interactively or programmatically. For illustrative purposes, the following commands change the interpreter from C to command and back again at the shell command line:

```
-> shConfig "INTERPRETER=Cmd"
[wxWorks]# set config INTERPRETER=C
->
```

It is also possible to create custom commands to allow for switching to and from a custom interpreter, similar to those used to switch between the command and C interpreters (C is used with the command interpreter, and cmd with the C interpreter to switch between the two).
The following code fragment sets the command interpreter for the current session:

```c
shellConfigValueSet (CURRENT_SHELL_SESSION, SHELL_CFG_INTERP, "Cmd");
```

For more information, see the `set config` command and the `shellConfigValueSet()` routine in the `shellConfigLib` API reference.

**Sample Custom Interpreter**

For an example of an interpreter code, see: `installDir/vxworks-6.x/target/src/demo/shell/shellInterpDemo.c`. The sample interpreter is called DEMO, and it has only six commands:

- `task` to create a task
- `list` to list the tasks
- `kill` to destroy a task
- `dump` to dump the memory contents
- `sym` to access symbol
- `C` to switch back to the C interpreter

The DEMO code illustrates how to create a small interpreter with a few commands, and how to make use of some of the routines exported by the shell (from the `shellDataLib` and `shellConfigLib` libraries).

To use this interpreter in the shell, you can build and download it as a downloadable kernel module. To register the commands, the initialization function `shellInterpDemoInit()` has to be called first. You can load it, register it, and set it for the current session as follows:

```bash
-> ld < shellInterpDemo.o
-> shellInterpRegister (shellInterpDemoInit)
-> shConfig "INTERPRETER=DEMO"
  DEMO #
```

If you choose to link the module with VxWorks instead of downloading it, you have to make the `shellInterpDemoInit()` call in the user startup code (see 2.7.7 Linking Kernel-Based Application Object Modules with VxWorks, p.65 and 2.7.9 Configuring VxWorks to Run Applications Automatically, p.68).

The code can be used with Wind River Workbench as a downloadable kernel module project, or included in a kernel project. For information about using the IDE, see the Wind River Workbench User's Guide.
10.3 Kernel Object-Module Loader

The target-resident, VxWorks kernel object-module loader lets you add object modules to the kernel at run-time. This operation, called loading, or downloading, allows you to install kernel-space applications or to extend the operating system itself. (For the sake of brevity, the kernel object-module loader is also referred to as simply as the kernel loader, or loader, in this section).

The downloaded code can be a set of routines, meant to be used by some other code (the equivalent of a library in other operating systems), or it can be an application, meant to be executed by a task or a set of tasks. The units of code that can be downloaded are referred to as object modules.

The ability to load individual object modules brings significant flexibility to the development process, in several different ways. The primary use of this facility during development is to unload, recompile, and reload object modules under development. The alternative is to link the developed code into the VxWorks image, to rebuild this image, and to reboot the target, every time the development code must be recompiled.

The kernel loader also enables you to dynamically extend the operating system, since once code is loaded, there is no distinction between that code and the code that was compiled into the image that booted.

Finally, you can configure the kernel loader to optionally handle memory allocation, on a per-load basis, for modules that are downloaded. This allows flexible use of the target's memory. The loader can either dynamically allocate memory for downloaded code, and free that memory when the module is unloaded; or, the caller can specify the addresses of memory that has already been allocated. This allows the user more control over the layout of code in memory. For more information, see 10.3.5 Specifying Memory Locations for Loading Objects, p.525.

The functionality of the kernel loader is provided by two components: the loader proper, which installs the contents of object modules in the target system’s memory; and the unloader, which uninstalls object modules. In addition, the loader relies on information provided by the system symbol table.

**NOTE:** The target-resident kernel object-module loader is sometimes confused with the boot loader, which is used to load the kernel image into memory. Although these two tools perform similar functions, and share some support code, they are separate entities. The boot loader loads only complete system images, and does not perform relocations. See 2.4 Boot Loader, p.12.
10.3.1 **Configuring VxWorks with the Kernel Object-Module Loader**

By default, the kernel object-module loader is not included in VxWorks. To use the loader, you must configure VxWorks with the `INCLUDE_LOADER` component.

Adding the `INCLUDE_LOADER` component automatically includes several other components that together provide complete loader functionality. These components are:

- **INCLUDE_UNLOADER**
  Provides facilities for unloading object modules.

- **INCLUDE_MODULE_MANAGER**
  Provides facilities for managing loaded modules and obtaining information about them. For more information, see the VxWorks API reference for `moduleLib`.

- **INCLUDE_SYM_TBL**
  Provides facilities for storing and retrieving symbols. For more information, see 10.4 Kernel Symbol Tables, p.531 and the VxWorks API reference for `symLib`.

- **INCLUDE_SYM_TBL_INIT**
  Specifies a method for initializing the system symbol table.

⚠️ **CAUTION:** If you want to use the target-resident symbol tables and kernel object-module loader in addition to the host tools, you must configure VxWorks with the `INCLUDE_WDB_MDL_SYM_SYNC` component to provide host-target symbol table and module synchronization. This component is included by default when both the kernel loader and WDB agent are included in VxWorks. For more information, see 10.4.4 Using the VxWorks System Symbol Table, p.535.

The kernel loader and unloader are discussed further in subsequent sections, and in the VxWorks API references for `loadLib` and `unldLib`. 
10.3.2 Kernel Object-Module Loader API

The API routines, shell C interpreter commands, and shell command interpreter commands available for loading and unloading kernel modules are described in Table 10-5 and Table 10-6.

Note that the kernel loader routines can be called directly from the C interpreter or from code. The shell commands, however, should only from the shell and not from within programs. In general, shell commands handle auxiliary operations, such as opening and closing a file; they also print their results and any error messages to the console.

### Table 10-5 Routines for Loading and Unloading Object Modules

<table>
<thead>
<tr>
<th>Routine</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>loadModule()</td>
<td>Loads an object module.</td>
</tr>
<tr>
<td>loadModuleAt()</td>
<td>Loads an object module into a specific memory location.</td>
</tr>
<tr>
<td>unldByModuleId()</td>
<td>Unloads an object module by specifying a module ID.</td>
</tr>
<tr>
<td>unldByNameAndPath()</td>
<td>Unloads an object module by specifying name and path.</td>
</tr>
<tr>
<td>unldByGroup()</td>
<td>Unloads an object module by specifying its group.</td>
</tr>
</tbody>
</table>

### Table 10-6 Shell C Interpreter Commands for Object Modules

<table>
<thead>
<tr>
<th>Command</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>ld()</td>
<td>Loads an object module into kernel memory.</td>
</tr>
<tr>
<td>reld()</td>
<td>Unloads and reloads an object module (specified by filename or module ID).</td>
</tr>
<tr>
<td>unld()</td>
<td>Unloads an object module (specified by filename or module ID) from kernel memory.</td>
</tr>
</tbody>
</table>

The use of some of these routines and commands is discussed in the following sections.

3. In future releases, calling shell commands programmatically may not be supported.
For detailed information, see the loadLib, unldLib, and usrLib API references, the shell command reference, as well as 10.3.3 Summary List of Kernel Object-Module Loader Options, p.522.

### 10.3.3 Summary List of Kernel Object-Module Loader Options

The kernel loader’s behavior can be controlled using load flags passed to loadLib and unldLib routines. Many of these flags can be combined (using a logical OR operation); some are mutually exclusive. The tables in this section group these options by category.

#### Table 10-7 Kernel Loader and Unloader Options for C++

<table>
<thead>
<tr>
<th>Option</th>
<th>Hex Value</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>LOAD_CPLUS_XTOR_AUTO</td>
<td>0x1000</td>
<td>Call C++ constructors on loading.</td>
</tr>
<tr>
<td>LOAD_CPLUS_XTOR_MANUAL</td>
<td>0x2000</td>
<td>Do not call C++ constructors on loading.</td>
</tr>
<tr>
<td>UNLD_CPLUS_XTOR_AUTO</td>
<td>0x20</td>
<td>Call C++ destructors on unloading.</td>
</tr>
<tr>
<td>UNLD_CPLUS_XTOR_MANUAL</td>
<td>0x40</td>
<td>Do not call C++ destructors on unloading.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>If this option is used, be sure that the destructor routines are not used to release resources back to the system (such as memory or semaphores); or the caller may first run any static destructors by calling cplusDtors().</td>
</tr>
</tbody>
</table>

#### Table 10-8 Kernel Loader Options for Symbol Registration

<table>
<thead>
<tr>
<th>Option</th>
<th>Hex Value</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>LOAD_NO_SYMBOLS</td>
<td>0x2</td>
<td>No symbols from the module are registered in the system’s symbol table.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Consequently, linkage against the code module is not possible. This option is useful for deployed systems, when the module is not supposed to be used in subsequent link operations.</td>
</tr>
</tbody>
</table>
**Table 10-8**  **Kernel Loader Options for Symbol Registration** (cont’d)

<table>
<thead>
<tr>
<th>Option</th>
<th>Hex Value</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>LOAD_LOCAL_SYMBOLS</td>
<td>0x4</td>
<td>Only local (private) symbols from the module are registered in the system’s symbol table. No linkage is possible against this code module’s public symbols. This option is not very useful by itself, but is one of the base options for LOAD_ALL_SYMBOLS.</td>
</tr>
<tr>
<td>LOAD_GLOBAL_SYMBOLS</td>
<td>0x8</td>
<td>Only global (public) symbols from the module are registered in the system’s symbol table. No linkage is possible against this code module’s private symbols. This is the kernel loader’s default when the loadFlags parameter is left as NULL.</td>
</tr>
<tr>
<td>LOAD_ALL_SYMBOLS</td>
<td>0xC</td>
<td>Local and global symbols from the module are registered in the system’s symbol table. This option is useful for debugging.</td>
</tr>
</tbody>
</table>

**Table 10-9**  **Kernel Loader Options for Code Module Visibility**

<table>
<thead>
<tr>
<th>Option</th>
<th>Hex Value</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>HIDDEN_MODULE</td>
<td>0x10</td>
<td>The code module is not visible from the moduleShow() routine or the host tools. This is useful on deployed systems when an automatically loaded module should not be detectable by the user. It only affects user visibility, and does not affect linking with other modules.</td>
</tr>
</tbody>
</table>

**Table 10-10**  **Kernel Unloader Options for Breakpoints and Hooks**

<table>
<thead>
<tr>
<th>Option</th>
<th>Hex Value</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>UNLD_KEEP_BREAKPOINTS</td>
<td>0x1</td>
<td>The breakpoints are left in place when the code module is unloaded. This is useful for debugging, as all breakpoints are otherwise removed from the system when a module is unloaded.</td>
</tr>
</tbody>
</table>
If several matching symbols exist for the options \texttt{LOAD\_COMMON\_MATCH\_USER} and \texttt{LOAD\_COMMON\_MATCH\_ALL}, the order of precedence is as follows: symbols in the \texttt{data} segment, then symbols in the \texttt{bss} segment, then symbols in the \texttt{common} segment. If several matching symbols exist within a single segment type, the symbol most recently added to the symbol table is used.
10.3.4 Loading C++ Modules into the Kernel

For information about loading C++ modules from the shell, see 11.4 Downloadable Kernel C++ Modules, p.563. Also see 10.3.3 Summary List of Kernel Object-Module Loader Options, p.522 for C++ kernel loader and unloader options.

10.3.5 Specifying Memory Locations for Loading Objects

By default, the kernel object-module loader allocates the memory necessary to hold a code module. It is also possible to specify where in memory any or all of the text, data, and bss segments of an object module should be installed using the loadModuleAt() command. If an address is specified for a segment, then the caller must allocate sufficient space for the segment at that address before calling the load routine. If no addresses are specified, the kernel loader allocates one contiguous area of memory for all three of the segments.

For any segment that does not have an address specified, the loader allocates the memory (using memPartAlloc() or, for aligned memory, using memalign()). The base address can also be set to the value LOAD_NO_ADDRESS, in which case the loader replaces the LOAD_NO_ADDRESS value with the actual base address of the segment once the segment is installed in memory.

The basic unit of information in a relocatable ELF object file is a section. In order to minimize memory fragmentation, the loader gathers sections so that they form the logical equivalent of an ELF segment. For simplicity, these groups of sections are also referred to as segments. For more information, see ELF Object Module Format, p.527).

The kernel loader creates three segments: text, data, and bss. When gathering sections together to form segments, the sections are placed into the segments in the same order in which they occur in the ELF file. It is sometimes necessary to add extra space between sections to satisfy the alignment requirements of all of the sections. When allocating space for one or more segments, care must be taken to ensure that there is enough space to permit all of the sections to be aligned properly. (The alignment requirement of a section is given as part of the section description in the ELF format. The binary utilities readelfarch and objdumparch can be used to obtain the alignment information.)

In addition, the amount of padding required between sections depends on the alignment of the base address. To ensure that there will be enough space without knowing the base address in advance, allocate the block of memory so that it is aligned to the maximum alignment requirement of any section in the segment. So,
for instance, if the data segment contains sections requiring 128 and 264 byte alignment, in that order, allocate memory aligned on 264 bytes.

The kernel unloader can remove the segments from wherever they were installed, so no special instructions are required to unload modules that were initially loaded at specific addresses. However, if the base address was specified in the call to the loader, then, as part of the unload, unloader does not free the memory area used to hold the segment. This allocation was performed by the caller, and the de-allocation must be as well.

10.3.6 Guidelines and Caveats for Kernel Object-Module Loader Use

The following sections describe the criteria used to load modules and issues with loading that may need to be taken into account.

Relocatable Object Files

Relocatable object files are used for modules that can be dynamically loaded into the VxWorks kernel and run. In contrast to an executable file, which is fully linked and ready to run at a specified address, a relocatable file is an object file for which text and data sections are in a transitory form, meaning that some addresses are not yet known. Relocatable object modules are generated by the compiler with .o extension (similar to the ones produced as an intermediate step between the application source files—.c, .s, .cpp—and the corresponding executable files that run in VxWorks processes).

Relocatable files are used for downloadable modules because the layout of the VxWorks image and downloaded code in memory are not available to a compiler running on a host machine. Therefore, the code handled by the target-resident kernel loader must be in relocatable form, rather than an executable. The loader itself performs some of the same tasks as a traditional linker in that it prepares the code and data of an object module for the execution environment. This includes the linkage of the module’s code and data to other code and data.

Once installed in the system’s memory, the entity composed of the object module’s code, data, and symbols is called a code module. For information about installed code modules, see the VxWorks API reference for moduleLib.
ELF Object Module Format

An relocatable ELF object file is essentially composed of two categories of elements: the headers and the sections. The headers describe the sections, and the sections contain the actual text and data to be installed.

An executable ELF file is a collection of segments, which are aggregations of sections. The kernel object-module loader performs an aggregation step on the relocatable object files that is similar to the process carried out by toolchains when producing an executable ELF file. The resulting image consists of one text segment, one data segment, and one bss segment. (A general ELF executable file may have more than one segment of each type, but the loader uses the simpler model of at most one segment of each type.) The loader installs the following categories of sections in the system’s memory:

- **text** sections that hold the application’s instructions
- **data** sections that hold the application’s initialized data
- **bss** sections that hold the application’s un-initialized data
- read-only data sections that hold the application’s constant data

Read-only data sections are placed in the text segment by the loader.

Linking and Reference Resolution

The kernel object-module loader performs some of the same tasks as a traditional linker in that it prepares the code and data of an object module for the execution environment. This includes the linkage of the module’s code and data to other code and data.

The loader is unlike a traditional linker in that it does this work directly in the target system’s memory, and not in producing an output file.

In addition, the loader uses routines and variables that already exist in the VxWorks system, rather than library files, to relocate the object module that it loads. The system symbol table (see 10.4.4 Using the VxWorks System Symbol Table, p.535) is used to store the names and addresses of functions and variables already installed in the system. This has the side effect that once symbols are installed in the system symbol table, they are available for future linking by any module that is loaded. Moreover, when attempting to resolve undefined symbols in a module, the loader uses all global symbols compiled into the target image, as well as all global symbols of previously loaded modules. As part of the normal load process, all of the global symbols provided by a module are registered in the system symbol
table. You can override this behavior by using the LOAD_NO_SYMBOLS load flag (see Table 10-8).

The system symbol table allows name clashes to occur. For example, suppose a symbol named `func` exists in the system. A second symbol named `func` is added to the system symbol table as part of a load. From this point on, all links to `func` are to the most recently loaded symbol. See also, 10.4.1 Configuring VxWorks with Symbol Tables, p.532.

Load Sequence Requirements and Caveats

The kernel object-module loader loads code modules in a sequential manner. That is, a separate load is required for each separate code module. The user must, therefore, consider dependencies between modules and the order in which they must be loaded to link properly.

Suppose a user has two code modules named A_module and B_module, and A_module references symbols that are contained in B_module. The user may either use the host-resident linker to combine A_module and B_module into a single module, or they should load B_module first, and then load A_module.

When code modules are loaded, they are irreversibly linked to the existing environment; meaning that, once a link from a module to an external symbol is created, that link cannot be changed without unloading and reloading the module.

Therefore dependencies between modules must be taken into account when modules are loaded to ensure that references can be resolved for each new module, using either code compiled into the VxWorks image or modules that have already been loaded into the system.

Failure to do so results in incompletely resolved code, which retains references to undefined symbols at the end of the load process. For diagnostic purposes, the loader prints a list of missing symbols to the console. This code should not be executed, since the behavior when attempting to execute an improperly relocated instruction is not predictable.

Normally, if a load fails, the partially installed code is removed. However, if the only failure is that some symbols are unresolved, the code is not automatically unloaded (but the API returns NULL to indicate failure programmatically). This allows the user to examine the result of the failed load, and even to execute portions of the code that are known to be completely resolved. Therefore, code modules that have unresolved symbols must be removed by a separate unload command (unld() with the C interpreter, or module unload with the command interpreter).
Note that the sequential nature of the loader means that unloading a code module which has been used to resolve another code module may leave references to code or data which are no longer available. Execution of code holding such dangling references may have unexpected results.

See *Statically Linking Kernel-Based Application Modules*, p.64.

### Resolving Common Symbols

Common symbols provide a challenge for the kernel object-module loader that is not confronted by a traditional linker. Consider the following example:

```c
#include <stdio.h>
int willBeCommon;
void main (void) {}
{
...
}
```

The symbol `willBeCommon` is uninitialized, so it is technically an undefined symbol. Many compilers will generate a common symbol in this case.

ANSI C allows multiple object modules to define uninitialized global symbols of the same name. The linker is expected to consistently resolve the various modules references to these symbols by linking them against a unique instance of the symbol. If the different references specify different sizes, the linker should define a single symbol with the size of the largest one and link all references against it. This is not a difficult task when all of the modules are linked in the same operation, such as when executing the host-resident linker, ldarch.

However, when VxWorks modules are loaded sequentially, the loader can only resolve the references of the module that it is currently loading with the those of the modules that it has already loaded, regardless of what the final, full set of modules may be. The `loadLib` API functions provide three options for controlling how common symbols are linked:

- The default behavior is to treat common symbols as if there were no previous matching reference (`LOAD_COMMON_MATCH_NONE`). The result is that every loaded module has its own copy of the symbol. For example, for three loads using this option with the same common symbol, three new global symbols are created.

- Common symbols are identified with any matching symbol in the symbol table (`LOAD_COMMON_MATCH_ALL`).
Common symbols are identified with any matching symbol that was not in the original boot image (LOAD_COMMON_MATCH_USER).

Note that these options only control the loader’s behavior with regard to the operation in which they are used—they only affect what happens with the symbols of the module being loaded. For example, consider the case in which module A has common symbols, and module B has undefined symbols that are resolved by module A. If module A is loaded with the LOAD_COMMON_MATCH_NONE option, this does not prevent module B from being linked against A’s symbols when B is loaded next. That is, the load flag used with module A does not prevent the loader from resolving undefined references in module B against module A.

The option to specify matching of common symbols may be set in each call using the loadLib API. Extreme care should be used when mixing the different possible common matching behaviors for the loader. It is much safer to pick a single matching behavior and to use it for all loads. For detailed descriptions of the matching behavior under each option, see Table 10-11.

**NOTE:** Note that the shell load command, ld, has a different mechanism for controlling how common symbols are handled and different default behavior. For details, see the reference entry for usrLib.

### Function Calls, Relative Branches, and Load Failures

For some architectures, function calls are performed using relative branches by default. This causes problems if the routine that is called resides further in memory than a relative branch instruction can access (which may occur if the board has a large amount of memory).

In this case, a module load fails; the kernel module loader prints an error message about relocation overflow and sets the S_loadElfLib_RELOCATION_OFFSET_TOO_LARGE errno (kernel shell).

To deal with this problem, compilers (both GNU and Wind River) have options to prevent the use of relative branches for function calls. See the *VxWorks Architecture Supplement* for more information.
10.4 Kernel Symbol Tables

A symbol table is a data structure that stores information that describes the routines, variables, and constants in all modules, and any variables created from the shell. There is a symbol table library, which can be used to manipulate the two different types of kernel symbol tables: a user-created symbol table and a system symbol table, which is the most commonly used. Note that both types of symbol tables used in the kernel are entirely independent of the symbol tables used by applications running in user-space processes (RTPs).

A system symbol table is required for the kernel object-module loader.

Symbol Entries

Each symbol in the table comprises these items:

- **name**
  The name is a character string derived from the name in the source code.

- **value**
  The value is usually the address of the element that the symbol refers to: either the address of a routine, or the address of a variable (that is, the address of the contents of the variable). The value is represented by a pointer.

- **group**
  The group number of the module that the symbol comes from.

- **symRef**
  The `symRef` is usually the module ID of the module that the symbol comes from.

- **type**
  The type is provides additional information about the symbol. For symbols in the system symbol table, it is one of the types defined in `installDir/vxworks-6.x/target/h/symbol.h`. For example, `SYM_UNDF`, `SYM_TEXT`, and so on. For user symbol tables, this field can be user-defined.

Symbol Updates

The symbol table is updated whenever modules are loaded into, or unloaded from, the target. You can control the precise information stored in the symbol table by using the kernel object-module loader options listed in Table 10-8.
Searching the Symbol Library

You can easily search all symbol tables for specific symbols. To search from the shell with the C interpreter, use `lkup()`. You can also use `symShow()` for general symbol information. For details, see the API references for these commands.

To search programmatically, use the symbol library API’s, which can be used to search the symbol table by address, by name, and by type, and a function that may be used to apply a user-supplied function to every symbol in the symbol table. For details, see the `symLib` reference entry.

10.4.1 Configuring VxWorks with Symbol Tables

The basic configuration is required for all symbol tables. This configuration provides the symbol table library, and is sufficient for a user symbol table. However, it is not sufficient for creating a system symbol table. This section describes both the basic configuration, and the additional configuration necessary to create a system symbol table.

For information about user symbol tables, see 10.4.6 Creating and Using User Symbol Tables, p.537. For information about the system symbol table, see 10.4.4 Using the VxWorks System Symbol Table, p.535.

Basic Configuration

The most basic configuration for a symbol table is to include the component, `INCLUDE_SYM_TBL`. This provides the basic symbol table library, `symLib`, (which is not equivalent to the system symbol table) and sufficient configuration for creating user symbol tables. The symbol table library component includes configuration options, which allow you to modify the symbol table width and control whether name clashes are permitted.

- Hash Table Width

  The `INCLUDE_SYM_TBL` component includes a configuration parameter that allows the user to change the default symbol table width. The parameter `SYM_TBL_HASH_SIZE_LOG2` defines the width of the symbol table’s hash table. It takes a positive value that is interpreted as a power of two. The default value for `SYM_TBL_HASH_SIZE_LOG2` is 8; thus, the default width of the symbol table is 256. Using smaller values requires less memory, but degrades lookup performance, so the search takes longer on average.
Name Clash Handling

The system symbol table, `sysSymTbl`, can be configured to allow or disallow name clashes. The flags used to call `symTblCreate()` determine whether or not duplicate names are permitted in a symbol table. If set to `FALSE`, only one occurrence of a given symbol name is permitted. When name clashes are permitted, the most recently added symbol of several with the same name is the one that is returned when searching the symbol table by name.

System Symbol Table Configuration

To include information about the symbols present in the kernel—and therefore to enable the shell, kernel object-module loader, and debugging facilities to function properly—a system symbol table must be created and initialized.

To create a system table, VxWorks must include a component for either a symbol table that is part of the system image or a component for a symbol table that is downloaded separately from the host system:

- **INCLUDE_STANDALONE_SYM_TBL**
  Creates a built-in system symbol table, in which both the system symbol table and the VxWorks image are contained in the same module in which the system symbol table is contained, in the VxWorks image. This type of symbol table is described in 10.4.2 Creating a Built-In System Symbol Table, p.533.

- **INCLUDE_NET_SYM_TBL**
  Creates an separate system symbol table as a .sym file that is downloaded to the VxWorks system. This type of symbol table is described in 10.4.3 Creating a Loadable System Symbol Table, p.535.

When the system symbol table is first created at system initialization time, it contains no symbols. Symbols must be added to the table at run-time. Each of these components handles the process of adding symbols differently.

10.4.2 Creating a Built-In System Symbol Table

A built-in system symbol table copies information into wrapper code, which is then compiled and linked into the kernel when the system is built.

Although using a built-in symbol table can produce a larger VxWorks image file than might otherwise be the case, it has several advantages, particularly for production systems:
It requires less memory than using a loadable symbol table—as long as you are not otherwise using the kernel object-module loader and associated components that are required for a loadable symbol table.

- It does not require that the target have access to a host (unlike the downloadable symbol table).
- It is faster to load the single image file than loading separate files for the VxWorks image and the loadable symbol table .sym file because some remote operations on a file take longer than the data transfer to memory.
- It is useful in deployed ROM-based systems that have no network connectivity, but require the shell as user interface.

Generating the Symbol Information

A built-in system symbol table relies on the `makeSymTbl` utility to obtain the symbol information. This utility uses the GNU utility `nm` to generate information about the symbols contained in the image. Then it processes this information into the file `symTbl.c` that contains an array, `standTbl`, of type `SYMBOL` described in Symbol Entries, p.531. Each entry in the array has the symbol name and type fields set. The address (value) field is not filled in by `makeSymTbl`.

Compiling and Linking the Symbol File

The `symTbl.c` file is treated as a normal `.c` file, and is compiled and linked with the rest of the VxWorks image. As part of the normal linking process, the toolchain linker fills in the correct address for each global symbol in the array. When the build completes, the symbol information is available in the image as a global array of VxWorks symbols. After the kernel image is loaded into target memory at system initialization, the information from the global `SYMBOL` array is used to construct the system symbol table.

The definition of the `standTbl` array can be found in the following files after the VxWorks image is built:

- `installDir/vxworks-6.x/target/config/bspName/symTbl.c` for images built directly from a BSP directory.
- `installDir/vxworks-6.x/target/proj/projDir/buildDir/symTbl.c` for images using the project facility.

4. That use `open()`, `seek()`, `read()`, and `close()`. 
10.4.3 Creating a Loadable System Symbol Table

A loadable symbol table is built into a separate object module file (vxWorks.sym file). This file is downloaded to the system separately from the system image, at which time the information is copied into the symbol table.

Creating the .sym File

The loadable system symbol table uses a vxWorks.sym file, rather than the symTbl.c file. The vxWorks.sym file is created by using the objcopy utility to strip all sections, except the symbol information, from the final VxWorks image. The resulting symbol information is placed in and ELF file named vxWorks.sym.

Loading the .sym File

During boot and initialization, the vxWorks.sym file is downloaded using the kernel object-module loader, which directly calls loadModuleAt(). To download the vxWorks.sym file, the loader uses the current default device, which is described in 6.2.1 Filenames and the Default Device, p.320.

To download the VxWorks image, the loader also uses the default device, as is current at the time of that download. Therefore, the default device used to download the vxWorks.sym file may, or may not, be the same device. This is because the default device can be set, or reset, by other initialization code that runs. This modification can happen after the VxWorks image is downloaded, but before the symbol table is downloaded.

Nevertheless, in standard VxWorks configurations, that do not include customized system initialization code, the default device at the time of the download of the vxWorks.sym, is usually set to one of the network devices, and using either rsh or ftp as the protocol.

10.4.4 Using the VxWorks System Symbol Table

Once it is initialized, the VxWorks system symbol table includes a complete list of the names and addresses of all global symbols in the compiled image that is booted. This information is needed on the target to enable the full functionality of the target tools libraries.
The target tools maintain the system symbol table with up-to-date name and address information for all of the code statically compiled into the system or dynamically downloaded. (The `LOAD_NO_SYMBOLS` option can be used to hide loaded modules, so that their symbols do not appear in the system symbol table; see Table 10-9).

Symbols are dynamically added to, and removed from, the system symbol table when:

- modules are loaded and unloaded
- variables are dynamically created from the shell
- the `wdb` agent synchronizes symbol information with the host (see 10.4.5 Synchronizing Host and Kernel Modules List and Symbol Table, p.537)

The exact dependencies between the system symbol table and the other target tools are as follows:

- **Kernel Object-Module Loader**: The kernel loader requires the system symbol table. The system symbol table does not require the presence of the loader.

- **Debugging Facilities**: The target-based symbolic debugging facilities and user commands such as `i` and `tt`, rely on the system symbol table to provide information about entry points of tasks, symbolic contents of call stacks, and so on.

- **Kernel Shell**: The kernel shell does not strictly require the system symbol table, but its functionality is greatly limited without it. The kernel shell requires the system symbol table to provide the ability to run functions using their symbolic names. The kernel shell uses the system symbol table to execute shell commands, to call system routines, and to edit global variables. The kernel shell also includes the library `usrLib`, which contains the commands `i`, `ti`, `sp`, `period`, and `bootChange`.

- **WDB Target Agent**: The WDB target agent adds symbols to the system symbol table as part of the symbol synchronization with the host.

If the facilities provided by the symbol table library are needed for user (non-operating system) code, another symbol table should be created and manipulated using the symbol library. See 10.4.6 Creating and Using User Symbol Tables, p.537.

**NOTE**: If you choose to use both the host-resident and target-resident tools at the same time, use the synchronization method to ensure that both the host and target resident tools share the same list of symbols.
10.4.5 Synchronizing Host and Kernel Modules List and Symbol Table

If both host tools and target tools are going to be used with a target system, the modules list and symbol table maintained on the host system needs to be synchronized with the modules list and symbol table maintained on the target. This ensures that the host and target tools share the same list of symbols.

The host tools maintain their own modules list and symbol table—the target server modules list and symbol table—on the host. In this chapter it is referred to as the host modules list and symbol table.

Module list and symbol table synchronization is provided automatically when VxWorks is configured with the WDB target agent and the kernel object-module loader (INCLUDE_WDB and INCLUDE_LOADER). To remove this feature, you need only remove the INCLUDE_WDB_MDL_SYM_SYNC component.

Note that the modules and symbols synchronization will only work if the WDB agent is in task mode. If the WDB agent is in system mode, the modules and symbols added from both the host and the target will not be synchronized.

For information about WDB, see 10.6 WDB Target Agent, p.540.

10.4.6 Creating and Using User Symbol Tables

Although it is possible for user code in the kernel to manipulate symbols in the system’s symbol table, this is not a recommended practice. Addition and removal of symbols to and from the symbol table should only be carried out by operating system libraries. Any other use of the system symbol table may interfere with the proper operation of the operating system; and even simply introducing additional symbols could have an adverse and unpredictable effect on linking any modules that are subsequently downloaded.

Therefore, user-defined symbols should not be added programmatically to the system symbol table. Instead, when user code in kernel space requires a symbol table for its own purposes, a user symbol table should be created. For more information, see the VxWorks API reference for symLib.
10.5 Show Routines

VxWorks includes system information routines that can be invoked from the shell’s C interpreter. They should not be used programmatically.

The show routines print pertinent system status on the specified object or service; however, they show only a snapshot of the system service at the time of the call and may not reflect the current state of the system. To use these routines, you must include the appropriate component when you configure VxWorks. When you invoke them, their output is sent to the standard output device. Table 10-12 lists common system show routines:

<table>
<thead>
<tr>
<th>Call</th>
<th>Description</th>
<th>Component</th>
</tr>
</thead>
<tbody>
<tr>
<td>envShow()</td>
<td>Displays the environment for a given task on stdout.</td>
<td>INCLUDE_TASK_SHOW</td>
</tr>
<tr>
<td>memPartShow()</td>
<td>Shows the partition blocks and statistics.</td>
<td>INCLUDE_MEM_SHOW</td>
</tr>
<tr>
<td>memShow()</td>
<td>System memory show routine.</td>
<td>INCLUDE_MEM_SHOW</td>
</tr>
<tr>
<td>moduleShow()</td>
<td>Prints information for all loaded modules, or an individual module.</td>
<td>INCLUDE_MODULE_MANAGER</td>
</tr>
<tr>
<td>msgQShow()</td>
<td>Message queue show utility (both POSIX and wind).</td>
<td>INCLUDE_POSIX_MQ_SHOW, INCLUDE_MSG_Q_SHOW</td>
</tr>
<tr>
<td>semShow()</td>
<td>Semaphore show utility (both POSIX and wind).</td>
<td>INCLUDE_SEM_SHOW, INCLUDE_POSIX_SEM_SHOW</td>
</tr>
<tr>
<td>show()</td>
<td>Generic object show utility. The show() routine does not work with modules or symbol tables; see moduleShow() and symshow().</td>
<td>INCLUDE_SHOW_ROUTINES</td>
</tr>
<tr>
<td>stdioShow()</td>
<td>Standard I/O file pointer show utility.</td>
<td>INCLUDE_STDIO_SHOW</td>
</tr>
<tr>
<td>symShow()</td>
<td>Prints symbol table information.</td>
<td>INCLUDE_SYM_TBL_SHOW</td>
</tr>
<tr>
<td>taskSwitchHookShow()</td>
<td>Shows the list of task switch routines.</td>
<td>INCLUDE_TASK_HOOKS_SHOW</td>
</tr>
<tr>
<td>taskCreateHookShow()</td>
<td>Shows the list of task create routines.</td>
<td>INCLUDE_TASK_HOOKS_SHOW</td>
</tr>
</tbody>
</table>
VxWorks also includes network information show routines, which are described in the *Wind River Network Stack for VxWorks 6 Programmer’s Guide*. These routines are initialized by configuring VxWorks with the `INCLUDE_NET_SHOW` component. Table 10-13 lists commonly called network show routines:

<table>
<thead>
<tr>
<th>Call</th>
<th>Description</th>
<th>Component</th>
</tr>
</thead>
<tbody>
<tr>
<td>ifShow( )</td>
<td>Display the attached network interfaces.</td>
<td></td>
</tr>
<tr>
<td>inetstatShow( )</td>
<td>Display all active connections for IP sockets.</td>
<td></td>
</tr>
<tr>
<td>ipstatShow( )</td>
<td>Display IP statistics.</td>
<td></td>
</tr>
<tr>
<td>netPoolShow( )</td>
<td>Show pool statistics.</td>
<td></td>
</tr>
<tr>
<td>netStackDataPoolShow( )</td>
<td>Show network stack data pool statistics.</td>
<td></td>
</tr>
<tr>
<td>netStackSysPoolShow( )</td>
<td>Show network stack system pool statistics.</td>
<td></td>
</tr>
<tr>
<td>mbufShow( )</td>
<td>Report mbuf statistics.</td>
<td></td>
</tr>
<tr>
<td>netShowInit( )</td>
<td>Initialize network show routines.</td>
<td></td>
</tr>
<tr>
<td>arpShow( )</td>
<td>Display entries in the system ARP table.</td>
<td></td>
</tr>
<tr>
<td>arptabShow( )</td>
<td>Display the known ARP entries.</td>
<td></td>
</tr>
<tr>
<td>routestatShow( )</td>
<td>Display routing statistics.</td>
<td></td>
</tr>
<tr>
<td>routeShow( )</td>
<td>Display host and network routing tables.</td>
<td></td>
</tr>
<tr>
<td>hostShow( )</td>
<td>Display the host table.</td>
<td></td>
</tr>
<tr>
<td>mRouteShow( )</td>
<td>Print the entries of the routing table.</td>
<td></td>
</tr>
</tbody>
</table>
10.6 WDB Target Agent

The VxWorks WDB target agent is a target-resident, runtime facility that is required for connecting host tools to a VxWorks target system. It is not required (or generally useful) for deployed systems, nor is it required for development using the kernel shell (see 10.2 Kernel Shell, p.495). The facility is also referred to as the target agent, the WDB agent, or simply as WDB. The acronym stands for Wind DeBug.

The WDB agent carries out requests transmitted from host-based debugging tools and replies with the results. The WDB agent contains a compact implementation of UDP/IP, and a proprietary RPC messaging protocol called WDB. The WDB (Wind DeBug) protocol specifies how the target server (on the host) communicates with the target agent (on the target). The protocol includes a compact programming language called Gopher, which permits on-the-fly extension by supporting programmable investigation of the target system.

The WDB protocol provides a core minimum of the services necessary to respond to requests from the host tools. These protocol requests include memory transactions, breakpoint/event notification services, virtual I/O support, tasking control, and real-time process control. The WDB protocol uses the Sun Microsystems specification for External Data Representation (XDR) for data transfer.

WDB can be configured for system mode debugging, task mode debugging, or both (switching between the two modes under the control of host tools). In task mode, WDB runs as a kernel task. In system mode, WDB operates independently of the kernel, and the kernel is under WDB’s control. With system mode, WDB can be started before VxWorks is running, which can be particularly useful in the early stages of porting a BSP to a new board. (See Debugging Mode Options, p.545 and 10.6.5 Starting the WDB Target Agent Before the VxWorks Kernel, p.553).

The WDB agent’s interface to communications drivers avoids the run-time I/O system, so that the WDB agent remains independent of the run-time OS. Drivers for the WDB agent are low-level drivers that provide both interrupt-driven and polling-mode operation. Polling mode is required to support system-level control of the target.

The WDB agent synthesizes the target-control strategies of task-level and system-wide debugging. The agent can execute in either mode and switch dynamically between them, provided the appropriate drivers are present in the Board Support Package (BSP). This permits debugging of any aspect of an embedded application whether it is a task, an interrupt service routine, or the kernel itself.
10.6.1 Configuring VxWorks with the WDB Target Agent

WDB target agent functionality is provided by a suite of components, some of which are optional, and others of which provide support for alternate modes of connection. By default VxWorks is configured with a pipe connection for the VxWorks simulator, and an Enhanced Network Driver (END) connection for all hardware targets.

The INCLUDE_WDB component provides the basic target agent facilities. It allows you to connect a target server, get basic information about the target, and load modules.

Basic WDB Configuration

The configuration parameters for the basic INCLUDE_WDB component are:

**WDB_COMM_PORT**

The UDP port used by the WDB agent to connect to the host (the default is 0x4321). This is also the default port used by the target server. If you need to change this port, be sure to update the port information in Wind River Workbench.

**WDB_STACK_SIZE**

The stack size of the WDB agent.

**WDB_POOL_SIZE**

The size of the memory pool used by the WDB agent.

To configure the WDB agent, you must also choose the appropriate connection-type component, one or more debugging modes, one initialization component, and any other options you may need. For information about reducing the size of the agent, see 10.6.3 Scaling the WDB Target Agent, p.552.
Host-Target Communication Options

The WDB components required for different types of host-target connections are described in Table 10-14. VxWorks should be configured with only one WDB communication component.

<table>
<thead>
<tr>
<th>Component</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>INCLUDE_WDB_COMM_END</td>
<td>The WDB enhanced network driver (END) connection component. The END driver supports both system and task mode debugging. This component is the default.</td>
</tr>
<tr>
<td>INCLUDE_WDB_COMM_NETWORK</td>
<td>The WDB UDP/IP network connection component. This communication type only supports task mode debugging.</td>
</tr>
<tr>
<td>INCLUDE_WDB_PROXY INCLUDE_WDB_PROXY_TIPC</td>
<td>The WDB TIPC components required for the gateway system on a TIPC network.</td>
</tr>
<tr>
<td>INCLUDE_WDB_COMM_TIPC</td>
<td>The WDB TIPC component for other targets (non-gateway) on a TIPC network.</td>
</tr>
<tr>
<td>INCLUDE_WDB_COMM_SERIAL</td>
<td>The WDB serial connection component. Useful when no network connections are available.</td>
</tr>
<tr>
<td>INCLUDE_WDB_COMM_VTMD</td>
<td>The WDB visionICE II or visionProbe II emulator connection component. This communication link is useful when debugging hardware bring up.</td>
</tr>
<tr>
<td>INCLUDE_WDB_COMM_PIPE</td>
<td>The WDB simulator pipe connection component—used only for the VxWorks simulator.</td>
</tr>
<tr>
<td>INCLUDE_WDB_COMM_CUSTOM</td>
<td>A WDB custom connection component, created by the user (see 10.6.6 Creating a Custom WDB Communication Component, p.554).</td>
</tr>
</tbody>
</table>
Enhanced Network Driver Connection Configuration

The configuration parameters for the INCLUDE_WDB_COMM_END component are:

- **WDB_MTU**
  The maximum transfer unit (MTU). The default MTU is 512 bytes.

- **WDB_END_DEVICE_NAME**
  By default, this parameter is set to NULL and the END driver used by the WDB agent is the one specified with a VxWorks boot loader device parameter. If you want to use a different device, set this parameter to the name of the device (for example, \texttt{dc}).

- **WDB_END_DEVICE_UNIT**
  If \texttt{WDB_END_DEVICE_NAME} is specified, set this parameter to the unit number of the END device you want to use.

Network Connection Configuration

The sole configuration parameter for the INCLUDE_WDB_COMM_NETWORK component is **WDB_MTU**, which defines the MTU for a UDP/IP network connection.

TIPC Network Connection Configuration

The configuration parameters for the INCLUDE_WDB_COMM_TIPC component are:

- **WDB_TIPC_PORT_TYPE**
  The TIPC port type. The default is 70.

- **WDB_TIPC_PORT_INSTANCE**
  The TIPC port instance. The default is 71.

Note that the INCLUDE_WDB_COMM_TIPC component is used for the targets on a TIPC network that you want to connect to with host tools, but not for the target that serves as a gateway between the host system’s TCP/IP network and the...
targets’ TIPC network. See 10.6.2 Using the WDB Target Agent with a TIPC Network, p.550 for more information.

For more information about TIPC itself, see the Wind River TIPC for VxWorks 6 Programmer’s Guide.

Serial Connection Configuration

The configuration parameters for the INCLUDE_WDB_COMM_SERIAL component are:

**WDB_MTU**
The MTU for a serial connection.

**WDB_TTY_BAUD**
The bps rate of the serial channel. The default is 9600 bps. For better performance, use the highest line speed available, which is often 38400 bps. Values higher than 34800 may not provide satisfactory performance. Try a slower speed if you suspect data loss.

**WDB_TTY_CHANNEL**
The channel number. Use 0 if you have only one serial port on the target. Use 1 (the default) if you want to keep the VxWorks console on the first serial port. If your target has a single serial channel, you can use the target server virtual console to share the channel between the console and the target agent. You must configure your system with the CONSOLE_TTY parameter set to NONE and the WDB_TTY_CHANNEL parameter set to 0.

When multiplexing the virtual console with WDB communications, excessive output to the console may lead to target server connection failures. The following may help resolve this problem:

- Decrease the amount of data being transmitted to the virtual console from your application.
- Increase the time-out period for the target server.
- Increase the baud rate of the target agent and the target server connection.

**INCLUDE_WDB_TTY_TEST**
When set to TRUE, this parameter causes words WDB READY to be displayed on the WDB serial port on startup. By default, this parameter is set to TRUE.

5. VxWorks serial channels are numbered starting at 0. Thus Channel 1 corresponds to the second serial port if the board’s ports are labeled starting at 1. If your board has only one serial port, you must change WDB_TTY_CHANNEL to 0 (zero).
WDB_TTY_ECHO
When set to TRUE, all characters received by the WDB agent are echoed on the serial port. As a side effect, echoing stops the boot process until a target server is attached. By default, this parameter is set to FALSE.

visionICE or visionProbe Connection Configuration
The sole configuration parameter for the INCLUDE_WDB_COMM_VTMD component is TMD_DEFAULT_POLL_DELAY, which specifies the clock tick interval for polling data on the target.

Pipe Connection Configuration
The sole configuration parameter for the INCLUDE_WDB_COMM_PIPE component (for the VxWorks simulator only) is WDB_MTU, which defines the MTU for a pipe connection.

Debugging Mode Options
WDB provides two debugging mode options by default: system mode and task mode. With system mode, the entire system is stopped when a breakpoint is hit. This allows you to set breakpoints anywhere, including ISRs. With task mode, a task or group of tasks is stopped when a breakpoint is set, but an exception or an interrupt does not stop if it hits a breakpoint. When the WDB agent is configured for task mode, the tWdbTask task is used to handle all WDB requests from the host.

You can include support for both modes, which allows tools such as the host shell or the debugger to dynamically switch from one mode to the other.

System Mode Debugging Configuration
The INCLUDE_WDB_SYS component provides support for system mode debugging. Note that this mode is only supported when the communication type has a polling mode for reading the device, which is not the case with the network component INCLUDE_WDB_COMM_NETWORK.

The configuration parameters for the INCLUDE_WDB_SYS component are:

WDB_SPAWN_OPTS
The task options flag used by tasks spawned in system mode.

WDB_SPAWN_PRI
The task priority used by tasks spawned in system mode.
**WDB_SPAWN_STACK_SIZE**

The stack size used by tasks spawned by the WDB target agent.

**Task Mode Debugging Configuration**

The `INCLUDE_WDB_TASK` component provides support for task mode. The configuration parameters are:

- **WDB_MAX_RESTARTS**
  The maximum number of times an agent can be restarted when it gets an error (the default is 5).

- **WDB_RESTART_TIME**
  The delay (in seconds) before restarting the WDB agent task when it gets an error (the default is 10 seconds).

- **WDB_TASK_OPTIONS**
  The options parameter of the WDB task.

- **WDB_TASK_PRIORITY**
  The priority of the WDB task. The default priority is 3.

- **WDB_SPAWN_STACK_SIZE**
  The stack size used by tasks spawned by the WDB target agent.

**Process Management Options**

The `INCLUDE_WDB_RTP` component provides support for real time process (RTP) operations (creation, deletion) and notifications (creation, deletion). This component is automatically included if the system supports real time processes (`INCLUDE_RTP`) and task debugging mode (`INCLUDE_WDB_TASK`).

The `INCLUDE_WDB_RTP_BP` component provides support for real time process debugging. It allows use of process-wide breakpoints. This component is automatically included when the system supports real time processes (`INCLUDE_RTP`) and task breakpoints (`INCLUDE_WDB_TASK_BP`).

The `INCLUDE_WDB_RTP_CONTROL` component allows the debugger to configure a process or kernel task such that its child processes are stopped at creation; that is, they do not start automatically when they are spawned. By default, processes can be spawned with an option that causes them to stop before they run, but child processes to not inherit this characteristic.
Initialization Options

WDB can be configured to start either before or after kernel initialization. By default, WDB is started after the kernel has been initialized.

The INCLUDE_WDB_POSTKERNEL_INIT component causes WDB to be started once kernel has fully been initialized. The INCLUDE_WDB_PREKERNEL_INIT component causes WDB to be started before kernel initialization has completed.

If WDB starts before kernel initialization, it is possible to perform early system debugging. However, because the kernel has not been initialized when WDB starts, task debugging is not supported in this mode. In addition, the END connection cannot be used with this mode because the network has not been initialized when WDB starts. Also see 10.6.5 Starting the WDB Target Agent Before the VxWorks Kernel, p.553.

When WDB starts after kernel initialization, all WDB features are fully supported. It is, of course, not possible to debug kernel initialization activity.

Additional Options

The following components provide additional optional functions. You can include or exclude them based on your requirements.

The INCLUDE_WDB_BANNER component displays the WDB banner on the console.

The INCLUDE_WDB_BP component provides support for breakpoints in the WDB agent itself. This component is needed if you want to debug the target from a host tool. The configuration parameter for this component is WDB_BP_MAX, which specifies the maximum number of breakpoints allocated on the target at startup. When this number of breakpoints is reached, it is still possible to allocate space for new breakpoints in task mode. In system mode, however, it is not possible to set additional breakpoints once the limit has been reached.

The INCLUDE_WDB_BP_SYNC component provides a breakpoint synchronization mechanism between host tools and target system. If this component is included in the VxWorks configuration, host tools are notified of any breakpoint creations and deletions that are made from the kernel shell. The component is automatically included when debug is provided for both the kernel shell (with INCLUDE_DEBUG) and the host tools (with INCLUDE_WDB_TASK_BP).
The INCLUDE_WDB_CTX component provides support for context operations: creation, deletion, suspension, resumption. A context can be a task, a real time process, or the system itself.

The INCLUDE_WDB_DIRECT_CALL component allows you to call functions in the WDB agent context directly.

The INCLUDE_WDB_EVENTPOINTS component adds support for eventpoints. An eventpoint can be a breakpoint, an eventpoint on context creation, or an eventpoint on context deletion. This component is the core component for all eventpoint types. Each time an eventpoint is hit, the corresponding event is sent to the target server.

The INCLUDE_WDB_EVENTS component adds support for asynchronous events. Asynchronous events are sent from the target to target server, to notify host tools about event activity on the target; for example, if a breakpoint has been hit, an exception occurred, or a context (task or process) has started or exited. The component is required (and is automatically included) when using breakpoints, exception notification, context start/exit notification, and so on.

The INCLUDE_WDB_EXC_NOTIFY component adds support for exception notification. When an exception occurs on the target, the appropriate event is sent to the target server.

The INCLUDE_WDB_EXIT_NOTIFY component adds support for context deletion notification. To be notified of a context exit, an eventpoint of type WDB_CTX_EXIT must be set. Tools set this eventpoint when they need to be notified. This component supports notification for task and real time process contexts.

The INCLUDE_WDB_FUNC_CALL component handles function calls by spawning tasks to run the functions. This service is only available in task mode.

The INCLUDE_WDB_GOPHER component provides support for the Gopher information gathering language. It is used by many host tools and cannot be removed from a system that uses other WDB options. The configuration parameters for this component are:

- **WDB_GOPHER_TAPE_LEN**, which defines the length of one gopher tape. Gopher tapes are used to record and upload data processed by the gopher. The default tape length is 1400 words, each of which are 32 bits wide.

- **WDB_GOPHER_TAPE_NB**, which defines the maximal number of gopher tapes that can be dynamically allocated. At startup, only one gopher tape is available. As needed, more tapes can be allocated. Dynamic allocation of tapes is only available in task mode. The default number of tapes is 10.
The **INCLUDE_WDB_MEM** component provides support for reading from, and writing to, target memory.

The **INCLUDE_WDB_REG** component provides support for reading from, and writing to, registers. The **WDB_REGS_SIZE** configuration parameter defines the size of an internal memory buffer used by the WDB agent to store coprocessor registers (to allow access to the registers in system mode).

The **INCLUDE_WDB_START_NOTIFY** component provides support for context creation notification. To be notified of a context exit, an eventpoint of type **WDB_CTX_START** must be set. Tools set this eventpoint when they need to be notified. This component supports task and real time process contexts.

The **INCLUDE_WDB_TASK_BP** component provides support for breakpoints in task debugging mode. This component is automatically included when WDB breakpoints (**INCLUDE_WDB_BP**) and task debugging mode (**INCLUDE_WDB_TASK**) are included in the system.

The **INCLUDE_WDB_TASK_HOOKS** component initializes task hooks needed to support task debugging mode. It is automatically included when task debugging mode (**INCLUDE_WDB_TASK**) is included, and should never be removed manually.

The **INCLUDE_WDB_TASK_REG** component provides support for task register operations (read and write). It is automatically included when WDB supports register operations (**INCLUDE_WDB_REG**) and task debugging mode (**INCLUDE_WDB_TASK**).

The **INCLUDE_WDB_TSFS** component adds support for a virtual file system enabled by the WDB protocol, the Target Server File System (see 7.8 Target Server File System: TSFS, p.457). This component is automatically included when the **INCLUDE_WVUPLOAD_TSFSSOCK** component is included for System Viewer upload support.

The **INCLUDE_WDB_USER_EVENT** component handles user defined events. For more information about user events, see the VxWorks **wdbUserEvtLib** API reference.

The **INCLUDE_WDB_VIO** component provides a driver for a virtual I/O (VIO) access.

The **INCLUDE_WDB_VIO_LIB** component handles VIO access (**read()** and **write()**). It requires the VIO driver component and the events component.

The **INCLUDE_WDB_MDL_SYM_SYNC** component handles module and symbol synchronization between the target and the target server. It is required only if both the WDB agent (**INCLUDE_WDB**) and the kernel object-module loader
(INCLUDE_LOADER) are available on the target and you are using the host-based loader (through the debugger, for example). The component synchronizes the records of modules and symbols that are kept by the host and kernel loaders. For more information, see the VxWorks wdbMdlSymSyncLib API reference and 10.4.5 Synchronizing Host and Kernel Modules List and Symbol Table, p.537.

10.6.2 Using the WDB Target Agent with a TIPC Network

Wind River host tools can be used to debug VxWorks targets on a TIPC network that do not have direct access to the host by way of TCP/IP or a serial line. In order to do so, however, one of the VxWorks targets on the TIPC network must serve as a gateway system.

A gateway must have access to both the host’s TCP/IP network and the targets’ TIPC network, and it must run a target agent proxy server. The proxy server supports both networking protocols and provides the link between the host target server and the WDB agent on the target system, thus allowing for remote debugging of the other VxWorks targets on the TIPC network. The proxy server can support multiple connections between the host system and different VxWorks targets.

Note that with TIPC, WDB system mode debugging is not supported over TIPC (see Debugging Mode Options, p.545).

For information about TIPC, see the Wind River TIPC for VxWorks 6 Programmer’s Guide.

Target System Configuration

The VxWorks gateway target and the other VxWorks targets on the TIPC network (to which the host tools attach) must each be configured with different WDB components:

- The gateway target must be configured with the INCLUDE_WDB_PROXY and INCLUDE_WDB_PROXY_TIPC components (as well as with both TIPC and TCP/IP support).
- Any other targets to which host tools will attach must be configured with the basic INCLUDE_WDB component and the INCLUDE_WDB_COMM_TIPC component (as well as with TIPC support).
When the INCLUDE_WDB_COMM_TIPC component is included, WDB system mode is excluded from the configuration, as it is not supported with TIPC communication.

For information about the configuration parameters for these components, see Basic WDB Configuration, p.541 and TIPC Network Connection Configuration, p.543.

Establishing a Host-Target Connection

To establish a connection between the host and the targets on the TIPC network, first boot the gateway and other targets.

Wind River Workbench provides options for connecting with a target running a WDB agent proxy. See the Wind River Workbench User’s Guide for more information.

From the command line, the syntax for starting a target server connection with a target running the WDB agent proxy is as follows:

```
tgtsrv -V -B wdbproxy -tipc tgt targetTipcAddress -tipcpt tipcPortType -tipcpi tipcPortInstance wdbProxyIpAddress/name
```

In this command:
- `targetTipcAddress` is the TIPC address of the target to which you want to connect.
- `tipcPortType` is the TIPC port type used for the WDB connection (the default is 70).
- `tipcPortInstance` is the TIPC port instance used for the WDB connection (the default is 71).
- `wdbProxyIpAddress/name` is the IP address or target name of the gateway target that is running the WDB proxy agent.
10.6.3 Scaling the WDB Target Agent

In a memory-constrained system, you may wish to create a smaller target agent. To reduce its size, you can remove the optional facilities listed in Table 10-15 (they are otherwise included by default).

Table 10-15 Optional WDB Agent Components

<table>
<thead>
<tr>
<th>Component</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>INCLUDE_WDB_BANNER</td>
<td>Prints a banner to console after the agent is initialized.</td>
</tr>
<tr>
<td>INCLUDE_WDB_VIO</td>
<td>Provides the VxWorks driver for accessing virtual I/O.</td>
</tr>
<tr>
<td>INCLUDE_WDB_USER_EVENT</td>
<td>Provides the ability to send user events to the host.</td>
</tr>
</tbody>
</table>

You can also reduce the maximum number of WDB breakpoints with the WDB_BP_MAX parameter of the INCLUDE_WDB_BP component. If you are using a serial connection, you can also set the INCLUDE_WDB_TTY_TEST parameter to FALSE.

If you are using a communication path that supports both system and task mode agents, then by default both agents are started. Since each agent consumes target memory (for example, each agent has a separate execution stack), you may wish to exclude one of the agents from the target system. You can configure the target to use only a task-mode or only a system-mode agent with the INCLUDE_WDB_TASK or INCLUDE_WDB_SYS options.

10.6.4 WDB Target Agent and Exceptions

If an application or BSP uses excHookAdd() or signal handlers to handle exceptions, WDB does not notify the host tools of the exceptions handled by those facilities. Host tool notification can be suppressed for all other exceptions by removing the INCLUDE_WDB_EXC_NOTIFY component from the VxWorks configuration.

If the WDB task (tWdbTask) takes an exception, it is restarted after a (configurable) delay. The connection between the target server and the target agent is down during the delay period. The length of the delay can be set with the
**WDB_RESTART_TIME** parameter. Note that the WDB task is started in the kernel only if WDB is set to run in task mode.

### 10.6.5 Starting the WDB Target Agent Before the VxWorks Kernel

By default, the WDB target agent is initialized near the end of the VxWorks initialization sequence. This is because the default configuration calls for the agent to run in task mode and to use the network for communication; thus, WDB is initialized after the kernel and the network.

In some cases—such as during BSP development—you may want to start the agent before the kernel, and initialize the kernel under the control of the host tools.

#### VxWorks Configuration

To be able to start WDB before the kernel, reconfigure VxWorks as follows:

1. Choose a communication path that can support a system-mode agent; a raw serial connection. (The END communication path cannot be used as it requires that the system be started before it is initialized.)

2. Select only the **INCLUDE_WDB_SYS** component—and not the task mode component.
   
   By default, the task mode starts two agents: a system-mode agent and a task-mode agent. Both agents begin executing at the same time, but the task-mode agent requires the kernel to be running.

3. Remove the **INCLUDE_WDB_BANNER** component. For some architectures, calling this component before kernel is initialized may hang the target.

4. Add the **INCLUDE_WDB_PRE_KERNEL_INIT** component and remove the **INCLUDE_WDB_POST_KERNEL_INIT** component. (See *Initialization Options*, p.547.)
   
   This causes the project code generator to make the **usrWdbInit()** call earlier in the initialization sequence. It will be called from **usrInit()** just before the kernel is started.\(^6\)

---

\(^6\) The code generator for **prjConfig.c** is based on the component descriptor language, which specifies when components are initialized. The component descriptor files are searched in a specified order, with the project directory being last, and overriding the default definitions in the generic descriptor files. For more information, see *CDF Precedence*, p.85.
Runtime Operation

When the host target server has connected to the system-mode WDB target agent, you can resume the system to start the kernel under the agent’s control.

After connecting to the target agent, set a breakpoint in `usrRoot()` and then continue the system. The routine `kernelInit()` starts the multi-tasking kernel with `usrRoot()` as the entry point for the first task. Before `kernelInit()` is called, interrupts are still locked. By the time `usrRoot()` is called, interrupts are unlocked.

Errors before reaching the breakpoint in `usrRoot()` are most often caused by a stray interrupt: check that you have initialized the hardware properly in the BSP `sysHwInit()` routine. Once `sysHwInit()` is working properly, you no longer need to start the agent before the kernel.

NOTE: If you use a serial connection when you start WDB before the kernel, you need to modify the SIO driver so that it can properly deal with interrupts and the order of system initialization in this context. See the VxWorks Device Driver Developer’s Guide: Additional Drivers for detailed information.

CAUTION: When the agent is started before the kernel, there is no way for the host to get the agent’s attention until a breakpoint occurs. This is because only system mode is supported and the WDB communication channel is set to work in polled mode only. On the other hand, the host does not really need to get the agent’s attention: you can set breakpoints in `usrRoot()` to verify that VxWorks can get through this routine. Once `usrRoot()` is working, you can start the agent after the kernel (that is, within `usrRoot()`), after which the polling task is spawned normally.

10.6.6 Creating a Custom WDB Communication Component

To create a custom communication component:

1. Write a WDB packet driver. The template file `installDir/vxworks-6.x/target/src/drv/wdb/wdbTemplatePktDrv.c` can be used as a starting point.

2. Create a configlette file in `installDir/vxworks-6.x/target/config/comps/src` that contains the routine `wdbCommDevInit()` to initialize the driver. You can base it on one of the WDB communication path configlettes in this directory (`wdbEnd.c`, `wdbSerial.c`, and so on).
3. Create a component descriptor file (CDF) for the custom component called 01wdbCommCustom.cdf in the directory installDir/vxworks-6.x/target/config/comps/vxWorks. The file must identify the driver module, the configlette, and any special parameters.

For information about creating custom components, see 2.9 Custom Kernel Components, p.69, including Defining a Component, p.91.

The custom communication component can then be used by configuring VxWorks with WDB_COMM_CUSTOM.
10.7 Common Problems

This section lists frequently encountered problems that can occur when using the target tools.

Kernel Shell Debugging Never Hits a Breakpoint

I set a breakpoint on a function I called from the kernel shell, but the breakpoint is not being hit. Why not?

Explanation

The kernel shell task runs with the VX_UNBREAKABLE option. Functions that are called directly from the kernel shell command prompt, are executed within the context of the kernel shell task. Therefore, breakpoints set within the directly called function will not be hit.

Solution

Instead of running the function directly, use taskSpawn( ) with the function as the entry point, or the shell’s C interpreter sp( ) command.

Insufficient Memory

The kernel object-module loader reports insufficient memory to load a module; however, checking available memory indicates the amount of available memory to be sufficient. What is happening and how do I fix it?

Explanation

The kernel loader calls the device drivers through a VxWorks’ transparent mechanism for file management, which makes calls to open, close, and ioctl. If you use the kernel loader to load a module over the network (as opposed to loading from a target-system disk), the amount of memory required to load an object module depends on what kind of access is available to the remote file system over the network. This is because, depending on the device that is actually being used for the load, the calls initiate very different operations.

For some devices, the I/O library makes a copy of the file in target memory. Loading a file that is mounted over a device using such a driver requires enough memory to hold two copies of the file simultaneously. First, the entire file is copied...
to a buffer in local memory when opened. Second, the file resides in memory when it is linked to VxWorks. This copy is then used to carry out various seek and read operations. Therefore, using these drivers requires sufficient memory available to hold two copies of the file to be downloaded, as well as a small amount of memory for the overhead required or the load operation.

Also consider that loading a module sometimes requires additional space, as the sections have to be aligned in memory (whereas the toolchain may compact them all in the object file to save space). See 10.3.5 Specifying Memory Locations for Loading Objects, p.525.

Solution

Download the file using a different device. Loading an object module from a host file system mounted through NFS only requires enough memory for one copy of the file (plus a small amount of overhead).

"Relocation Does Not Fit" Error Message

When downloading, the following types of error messages occur:

"Relocation does not fit in 26 bits."

The actual number received in the error message varies (26, or 23, or ...) depending on the architecture. What does this error mean and what should I do?

Explanation

Some architectures have instructions that use less than 32 bits to reference a nearby position in memory. Using these instructions can be more efficient than always using 32 bits to refer to nearby places in memory.

The problem arises when the compiler has produced such a reference to something that lies farther away in memory than the range that can be accessed with the reduced number of bits. For instance, if a call to printf() is encoded with one of these instructions, the load may succeed if the object code is loaded near the kernel code, but fail if the object code is loaded farther away from the kernel image.

For additional information, see Function Calls, Relative Branches, and Load Failures, p.530.
Solution

Recompile the object file using `-Xcode-absolute-far` for the Wind River compilers, and for GNU compilers, the appropriate `long call` option, `-mlongcall` (for PPC architecture). See the VxWorks Architecture Supplement for the appropriate options.

Missing Symbols

Symbols in code modules downloaded from the host do not appear from the kernel shell, and vice versa. Symbols created from the host shell are not visible from the kernel shell, or symbols created from the kernel shell are not visible from the host shell. Why is this happening, and how can I get them to appear?

Explanation

The symbol synchronization mechanism must be enabled separately on the host and target.

Solution

Check to see if the module and symbol synchronization is enabled for the target server as well as compiled into the image. For more information, see 10.4.5 Synchronizing Host and Kernel Modules List and Symbol Table, p.537.

Kernel Object-Module Loader is Using Too Much Memory

Including the kernel loader causes the amount of available memory to be much smaller. How can I get more memory?

Explanation

Including the kernel loader causes the system symbol table to be included. This symbol table contains information about every global symbol in the compiled VxWorks image.

Using the kernel loader takes additional memory away from your application—most significantly for the target-resident symbol table required by the kernel loader.

Solution

Use the host tools rather than the target tools and remove all target tools from your VxWorks image.
Symbol Table Unavailable

The system symbol table failed to download onto my target. How can I use the kernel shell to debug the problem, since I cannot call functions by name?

Solution

Use addresses of functions and data, rather than using the symbolic names. The addresses can be obtained from the VxWorks image on the host, using the `nm` utility.

The following is an example from a UNIX host:

```
> nm vxWorks | grep memShow
0018b1e8 T memShow
0018b1ac T memShowInit
```

Use this information to call the function by address from the kernel shell. (The parentheses are mandatory when calling by address.)

```
-> 0x0018b1e8 ()
```

```
status bytes blocks avg block max block
------ --------- -------- ---------- ----------
current
free 14973336 20    748666 12658120
alloc 14201864 16163 878 -
cumulative
alloc 21197888 142523 148 -
value = 0 = 0x0
```

For modules that are relocated, use `nm` on the module to get the function address (which is the offset within the module’s text segment) then add to that value the starting address of the text segment of the module when it was loaded in memory.
11.1 Introduction

This chapter provides information about C++ development for VxWorks using the Wind River and GNU toolchains.

⚠️ **WARNING:** Wind River Compiler C++ and GNU C++ binary files are not compatible.

» **NOTE:** This chapter provides information about facilities available in the VxWorks kernel. For information about facilities available to real-time processes, see the corresponding chapter in the *VxWorks Application Programmer’s Guide.*
11.2 Configuring VxWorks for C++

By default, VxWorks includes only minimal C++ support. You can add C++ functionality by including any or all of the following components:

**INCLUDE_CTOR_DTORS**
(included in default kernels)
Ensures that compiler-generated initialization functions, including initializers for C++ static objects, are called at kernel start up time.

**INCLUDE_CPLUS**
Includes basic support for C++ applications. Typically this is used in conjunction with INCLUDE_CPLUS_LANG.

**INCLUDE_CPLUS_LANG**
Includes support for C++ language features such as *new*, *delete*, and exception handling.

**INCLUDE_CPLUS_IOSTREAMS**
Includes all library functionality.

**INCLUDE_CPLUS_DEMangler**
Includes the C++ demangler, which is useful if you are using the kernel shell loader because it provides for returning demangled symbol names to kernel shell symbol table queries. This component is added by default if both the INCLUDE_CPLUS and INCLUDE_SYM_TBL components are included in VxWorks.

11.3 C++ Code Requirements

Any VxWorks task that uses C++ must be spawned with the VX_FP_TASK option. By default, tasks spawned from host tools (such as the Wind Shell) automatically have VX_FP_TASK enabled.

**WARNING:** Failure to use the VX_FP_TASK option when spawning a task that uses C++ can result in hard-to-debug, unpredictable floating-point register corruption at run-time.

If you reference a (non-overloaded, global) C++ symbol from your C code you must give it C linkage by prototyping it using **extern "C"**:
11 C++ Development

11.4 Downloadable Kernel C++ Modules

C++ objects that will be downloaded to kernel space in a target system (downloadable modules) must be munched. There are also several strategies available for calling static constructors and destructors in downloadable modules.

11.4.1 Munching C++ Application Modules

Before a C++ module can be downloaded to the VxWorks kernel, it must undergo an additional host processing step, which for historical reasons, is called munching. Munching performs the following tasks:

- Initializes support for static objects.
- Ensures that the C++ run-time support calls the correct constructors and destructors in the correct order for all static objects.
- For the Wind River Compiler, collapses any COMDAT sections automatically; for the GNU compiler, collapses any linkonce automatically.

Munching must be performed after compilation and before download.
Munching Examples

For each toolchain, the following examples compile a C++ application source file, `hello.cpp`, run munch on the `.o`, compile the generated `ctdt.c` file, and link the application with `ctdt.o` to generate a downloadable module, `hello.out`.

Using the Wind River Toolchain

1. Compile the source code:
   
   ```
   $ dcc -tPPC604FH:vxworks61 -Xlocal-data-area-static-only -XO \ 
   -InstallDir/vxworks-6.1/target/h -DCPU=PPC32 -DTOOL_FAMILY=diab \ 
   -DTOOL=diab \ -D_WRS_KERNEL -c hello.cpp
   ```

2. Munch the object file:
   
   ```
   $ ddump -Ng hello.o | tclsh \ 
   installDir/vxworks-6.1/host/resource/hutils/tcl/munch.tcl -c ppc > ctdt.c
   ```

3. Compile the munch output:
   
   ```
   $ dcc -tPPC604FH:vxworks61 -Xlocal-data-area-static-only -XO \ 
   -InstallDir/vxworks-6.1/target/h -DCPU=PPC32 -DTOOL_FAMILY=diab \ 
   -DTOOL=diab \ -D_WRS_KERNEL -c ctdt.c
   ```

4. Link the original object file with the munched object file to create a downloadable module:
   
   ```
   $ dld -tPPC604FH:vxworks61 -X -r4 -o hello.out hello.o ctdt.o
   ```

**NOTE:** The `-r4` option collapses any COMDAT sections contained in the input files.

Using the GNU Toolchain

1. Compile the source code:
   
   ```
   ccppc -mcpu=604 -mstrict-align -O2 -fno-builtin \ 
   -InstallDir/vxworks-6.1/target/h \ 
   -DCPU=PPC604 -DTOOL_FAMILY=gnu -DTOOL=gnu -c hello.cpp
   ```

2. Munch the object file:
   
   ```
   nmppc hello.o | wtxtcl installDir/vxworks-6.1/host/src/hutils/munch.tcl \ 
   -c ppc > ctdt.c
   ```

3. Compile the munch output:
   
   ```
   ccppc -mcpu=604 -mstrict-align -f$\$0s-in-identifiers -O2 \ 
   -fno-builtin -InstallDir/vxworks-6.1/target/h \ 
   -DCPU=PPC604 -DTOOL_FAMILY=gnu -DTOOL=gnu -c ctdt.c
   ```
4. Link the original object file with the munched object file to create a downloadable module:

```bash
ccpcp -r -nostdlib -Wl,-X -T installDir/vxworks-6.1/target/h/tool/gnu/ldscripts/link.OUT -o hello.out hello.o ctdt.o
```

**NOTE:** The -T option collapses any linkonce sections contained in the input files.

**Using a Generic Makefile Rule**

If you use the VxWorks makefile definitions, you can write a simple munching rule which (with appropriate definitions of CPU and TOOL) works across all architectures for both GNU and Wind River Compiler toolchains.

```bash
CPU     = PPC604
TOOL    = gnu
TGT_DIR = $(WIND_BASE)/target
include $(TGT_DIR)/h/make/defs.bsp

default : hello.out
%.o : %.cpp
 $(CXX) $(C++FLAGS) -c $<
%.out : %.o
 $(NM) $*.o | $(MUNCH) > ctdt.c
 $(CC) $(CFLAGS) $(OPTION_DOLLAR_SYMBOLS) -c ctdt.c
 $(LD_PARTIAL) $(LD_PARTIAL_LAST_FLAGS) -o $@ $*.o ctdt.o
```

After munching, downloading, and linking, the static constructors and destructors are called. This step is described next.

**11.4.2 Calling Static Constructors and Destructors Interactively**

The kernel loader provides both manual and automatic options for calling static constructors and destructors.

Automatic invocation is the default strategy. Static constructors are executed just after the module is downloaded to the target and before the module loader returns to its caller. Static destructors are executed just prior to unloading the module.

Manual invocation means that the user must call static constructors explicitly, after downloading the module, but before running the application. It also requires the user to call static destructors explicitly, after the task finishes running, but before unloading the module.
Static constructors are called by invoking `cplusCtors()`. Static destructors are called by invoking `cplusDtors()`. These routines take an individual module name as an argument. However, you can also invoke all of the static constructors or destructors that are currently loaded into a system by calling these routines without an argument.

⚠️ **CAUTION:** When using the manual invocation method, constructors for each module must only be run once.

You can change the strategy for calling static constructors and destructors at runtime with the `cplusXtorSet()` routine. To report on the current strategy, call `cplusStratShow()`.

For more information on the routines mentioned in this section, see the API entries in the online reference manuals.

Also see **10.3.3 Summary List of Kernel Object-Module Loader Options**, p.522 for information about the C++ loader and unloader options.

### 11.5 C++ Compiler Differences

The Wind River C++ Compiler uses the Edison Design Group (EDG) C++ front end. It fully complies with the ANSI C++ Standard. For complete documentation on the Wind River Compiler and associated tools, see the *Wind River C/C++ Compiler User’s Guide*.

The GNU compilers provided with the host tools and IDE support most of the language features described in the *ANSI C++ Standard*. In particular, they provide support for template instantiation, exception handling, run-time type information, and namespaces. For complete documentation on the GNU compiler and on the associated tools, see the *GNU ToolKit User’s Guide*.

The following sections briefly describe the differences in compiler support for template instantiation, exception handling, and run-time type information.

#### 11.5.1 Template Instantiation

In C, every function and variable used by a program must be defined in exactly one place (more precisely one *translation unit*). However, in C++ there are entities
which have no clear point of definition but for which a definition is nevertheless required. These include template specializations (specific instances of a generic template; for example, `std::vector<int>`), out-of-line bodies for inline functions, and virtual function tables for classes without a non-inline virtual function. For such entities a source code definition typically appears in a header file and is included in multiple translation units.

To handle this situation, both the Wind River Compiler and the GNU compiler generate a definition in every file that needs it and put each such definition in its own section. The Wind River compiler uses `COMDAT` sections for this purpose, while the GNU compiler uses `linkonce` sections. In each case the linker removes duplicate sections, with the effect that the final executable contains exactly one copy of each needed entity.

**NOTE:** Only the WRC linker can be used to process files containing COMDAT sections. Similarly only the GNU linker can be used on files containing linkonce sections. Furthermore the VxWorks target and host loaders are not able to process COMDAT and linkonce sections. A fully linked VxWorks image will not contain any COMDAT or linkonce sections. However intermediate object files compiled from C++ code may contain such sections. To build a downloadable C++ module, or a file that can be processed by any linker, you must perform an intermediate link step using the `-r5` option (WRC) or specifying the `link.OUT` linker script (GCC). See 10.4.1 Munching C++ Application Modules for full details. (Note that while the `-r5` and `-r4` options—the latter referred to elsewhere in this chapter—both collapse COMDAT files, their overall purpose is different, and their use is mutually exclusive in a single linker command.)

It is highly recommended that you use the default settings for template instantiation, since these combine ease-of-use with minimal code size. However it is possible to change the template instantiation algorithm; see the compiler documentation for details.

**Wind River Compiler**

The Wind River Compiler C++ options controlling multiple instantiation of templates are:

-Xcomdat

This option is the default. When templates are instantiated implicitly, the generated code or data section are marked as `comdat`. The linker then collapses identical instances marked as such, into a single instance in memory.
-Xcomdat-off
Generate template instantiations and inline functions as static entities in the resulting object file. Can result in multiple instances of static member-function or class variables.

For greater control of template instantiation, the -Ximplicit-templates-off option tells the compiler to instantiate templates only where explicitly called for in source code; for example:

```cpp
template class A<int>; // Instantiate A<int> and all member functions.
template int f1(int);  // Instantiate function int f1(int).
```

GNU Compiler

The GNU C++ compiler options controlling multiple instantiation of templates are:

-implicit-templates
This option is the default. Template instantiations and out-of-line copies of inline functions are put into special linkonce sections. Duplicate sections are merged by the linker, so that each instantiated template appears only once in the output file.

-implicit-templates
This is the option for explicit instantiation. Using this strategy explicitly instantiates any templates that you require.

11.5.2 Exception Handling

Both compilers support thread-safe exception handling by default.

Wind River Compiler

To turn off support for exception handling, use the -Xexceptions-off compiler flag.
The Wind River Compiler exception handling model is table driven and requires little run-time overhead if a given exception is not thrown. Exception handling does, however, involve a size increase.

GNU Compiler

To turn off support for exception handling, use the `-fno-exceptions` compiler flag.

Unhandled Exceptions

As required by the ANSI C++ Standard, an unhandled exception ultimately calls `terminate()`. The default behavior of this routine is to suspend the offending task and to send a warning message to the console. You can install your own termination handler by calling `set_terminate()`, which is defined in the header file `exception`.

11.5.3 Run-Time Type Information

Both compilers support Run-time Type Information (RTTI), and the feature is enabled by default. This feature adds a small overhead to any C++ program containing classes with virtual functions.

For the Wind River Compiler, the RTTI language feature can be disabled with the `-Xrtti-off` flag.

For the GNU compiler, the RTTI language feature can be disabled with the `-fno-rtti` flag.

11.6 Namespaces

Both the Wind River and GNU C++ compilers support namespaces. You can use namespaces for your own code, according to the C++ standard.

The C++ standard also defines names from system header files in a namespace called `std`. The standard requires that you specify which names in a standard header file you will be using.

The following code is technically invalid under the latest standard, and will not work with this release. It compiled with a previous release of the GNU compiler,
but will not compile under the current releases of either the Wind River or GNU C++ compilers:

```cpp
#include <iostream.h>
int main()
{
    cout << "Hello, world!" << endl;
}
```

The following examples provide three correct alternatives illustrating how the C++ standard would now represent this code. The examples compile with either the Wind River or the GNU C++ compiler:

```
// Example 1
#include <iostream>
int main()
{
    std::cout << "Hello, world!" << std::endl;
}

// Example 2
#include <iostream>
using std::cout;
using std::endl;
int main()
{
    cout << "Hello, world!" << endl;
}

// Example 3
#include <iostream>
using namespace std;
int main()
{
    cout << "Hello, world!" << endl;
}
```

### 11.7 C++ Demo Example

The **factory** demo example demonstrates various C++ features in the kernel, including the Standard Template Library, user-defined templates, run-time type information, and exception handling. This demo is located in `installDir/vxworks-6.x/target/usr/apps/samples/cplusplus/factory`.

To create, compile, build, and run this demo program you can use either the IDE or the command-line, as shown below.
For the factory demo, your kernel must include the following components in VxWorks:

- INCLUDE_CPLUS
- INCLUDE_CPLUS_LANG
- INCLUDE_CPLUS_IOSTREAMS

In addition, for GNU only, include the following components:

- INCLUDE_CPLUS_STRING
- INCLUDE_CPLUS_STRING_IO

To build factory from the command line, simply copy the factory sources to the BSP directory, as shown below:

```bash
cd installDir/vxworks-6.1/target/config/bspDir
cp installDir/vxworks-6.1/target/src/demo/cplusplus.factory/factory.* .
```

Then, to build a bootable image containing the factory example, run make as shown below:

```bash
make ADDED_MODULES=factory.o
```

and boot the target.

To build a downloadable image containing the factory example, run make as shown below:

```bash
make factory.out
```

Then, from the WindSh, load the factory module, as shown below:

```bash
ld < factory.out
```

Finally, to run the factory demo example, type at the shell:

```bash
-> testFactory
```

Full documentation on what you should expect to see is provided in the source code comments for the demo program.
12.1 Introduction

VxMP is a VxWorks component that provides shared-memory objects dedicated to high-speed synchronization and communication between tasks running on separate CPUs. It is available only in the VxWorks kernel.

Shared-memory objects are a class of system objects that can be accessed by tasks running on different processors. They are called shared-memory objects because the object’s data structures must reside in memory accessible by all processors. Shared-memory objects are an extension of local VxWorks objects. Local objects are only available to tasks on a single processor.
VxMP supplies three kinds of shared-memory objects:
- shared semaphores (binary and counting)
- shared message queues
- shared-memory partitions (system- and user-created partitions)

Shared-memory objects provide the following advantages:
- A transparent interface that allows shared-memory objects to be manipulated with the same routines that are used for manipulating local objects.
- High-speed inter-processor communication—no unnecessary packet passing is required.
- The shared memory can reside either in dual-ported RAM or on a separate memory board.

The components of VxMP consist of the following: a name database (smNameLib), shared semaphores (semSmLib), shared message queues (msgQSmLib), and a shared-memory allocator (smMemLib).

This chapter presents a detailed description of each shared-memory object and internal considerations. It then describes configuration and troubleshooting.

**NOTE:** VxMP can only be used in kernel space. It cannot be used in user space (processes).

### 12.2 Using Shared-Memory Objects

VxMP provides a transparent interface that makes it easy to execute code using shared-memory objects on both a multiprocessor system and a single-processor system. After an object is created, kernel tasks can operate on shared objects with the same routines used to operate on their corresponding local objects. For example, shared semaphores, shared message queues, and shared-memory partitions have the same syntax and interface as their local counterparts. Routines such as `semGive()`, `semTake()`, `msgQSend()`, `msgQReceive()`, `memPartAlloc()` , and `memPartFree()` operate on both local and shared objects. Only the create routines are different. This allows an application to run in either a single-processor or a multiprocessor environment with only minor changes to system configuration, initialization, and object creation.
All shared-memory objects can be used on a single-processor system. This is useful for testing an application before porting it to a multiprocessor configuration. However, for objects that are used only locally, local objects always provide the best performance.

After the shared-memory facilities are initialized (see 12.4 Configuration, p. 597 for initialization differences), all processors are treated alike. Kernel tasks on any CPU can create and use shared-memory objects. No processor has priority over another from a shared-memory object’s point of view.¹

Systems making use of shared memory can include a combination of supported architectures. This enables applications to take advantage of different processor types and still have them communicate. However, on systems where the processors have different byte ordering, you must call the macros `ntohl` and `htonl` to byte-swap the application’s shared data (see Wind River Network Stack for VxWorks 6 Programmer’s Guide).

When an object is created, an object ID is returned to identify it. For kernel tasks on different CPUs to access shared-memory objects, they must be able to obtain this ID. An object’s ID is the same regardless of the CPU. This allows IDs to be passed using shared message queues, data structures in shared memory, or the name database.

Throughout the remainder of this chapter, system objects under discussion refer to shared objects unless otherwise indicated.

### 12.2.1 Name Database

The name database allows the association of any value to any name, such as a shared-memory object’s ID with a unique name. It can communicate or advertise a shared-memory block’s address and object type. The name database provides name-to-value and value-to-name translation, allowing objects in the database to be accessed either by name or by value. While other methods exist for advertising an object’s ID, the name database is a convenient method for doing this.

Typically the kernel task that creates an object also advertises the object’s ID by means of the name database. By adding the new object to the database, the task associates the object’s ID with a name. Tasks on other processors can look up the name in the database to get the object’s ID. After the task has the ID, it can use it to access the object. For example, task \texttt{t1} on CPU 1 creates an object. The object ID is returned by the creation routine and entered in the name database with the name

1. Do not confuse this type of priority with the CPU priorities associated with VMEbus access.
myObj. For task t2 on CPU 0 to operate on this object, it first finds the ID by looking up the name myObj in the name database.

This same technique can be used to advertise a shared-memory address. For example, task t1 on CPU 0 allocates a chunk of memory and adds the address to the database with the name mySharedMem. Task t2 on CPU 1 can find the address of this shared memory by looking up the address in the name database using mySharedMem.

Tasks on different processors can use an agreed-upon name to get a newly created object’s value. See Table 12-1 for a list of name service routines. Note that retrieving an ID from the name database need occur only one time for each task, and usually occurs during application initialization.

The name database service routines automatically convert to or from network-byte order; do not call htonl() or ntohl() explicitly for values from the name database.

The object types listed in Table 12-2 are defined in smNameLib.h.

### Table 12-1 Name Service Routines

<table>
<thead>
<tr>
<th>Routine</th>
<th>Functionality</th>
</tr>
</thead>
<tbody>
<tr>
<td>smNameAdd()</td>
<td>Adds a name to the name database.</td>
</tr>
<tr>
<td>smNameRemove()</td>
<td>Removes a name from the name database.</td>
</tr>
<tr>
<td>smNameFind()</td>
<td>Finds a shared symbol by name.</td>
</tr>
<tr>
<td>smNameFindByValue()</td>
<td>Finds a shared symbol by value.</td>
</tr>
<tr>
<td>smNameShow()</td>
<td>Displays the name database to the standard output device.a</td>
</tr>
</tbody>
</table>

a. Automatically included if INCLUDE_SM_OBJ is selected.

### Table 12-2 Shared-Memory Object Types

<table>
<thead>
<tr>
<th>Constant</th>
<th>Hex Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>T_SM_SEM_B</td>
<td>0</td>
</tr>
<tr>
<td>T_SM_SEM_C</td>
<td>1</td>
</tr>
<tr>
<td>T_SM_MSG_Q</td>
<td>2</td>
</tr>
</tbody>
</table>
12.2 Using Shared-Memory Objects

The following example shows the name database as displayed by `smNameShow()`, which is automatically included if VxWorks is configured with the `INCLUDE_SM_OBJ` component. The parameter to `smNameShow()` specifies the level of information displayed; in this case, 1 indicates that all information is shown. For additional information, see the `smNameShow()` API reference.

```plaintext
smNameShow 1
value = 0 = 0x0
```

The output is sent to the standard output device, and looks like the following:

```
Name in Database Max : 100 Current : 5 Free : 95
Name Value Type
----------------- ------------- -------------
myMemory 0x3835a0 SM_BLOCK
myMemPart 0x3659f9 SM_PART_ID
myBuff 0x383564 SM_BLOCK
mySmSemaphore 0x36431d SM_SEM_B
myMsgQ  0x365899 SM_MSG_Q
```

12.2.2 Shared Semaphores

Like local semaphores, shared semaphores provide synchronization by means of atomic updates of semaphore state information. See 3. Multitasking and the API reference for `semLib` for a complete discussion of semaphores. Shared semaphores can be given and taken by tasks executing in the kernel on any CPU with access to the shared memory. They can be used for either synchronization of tasks running on different CPUs or mutual exclusion for shared resources.

To use a shared semaphore, a task creates the semaphore and advertises its ID. This can be done by adding it to the name database. A task on any CPU in the system can use the semaphore by first getting the semaphore ID (for example, from the name database). When it has the ID, it can then take or give the semaphore.

In the case of employing shared semaphores for mutual exclusion, typically there is a system resource that is shared between tasks on different CPUs and the semaphore is used to prevent concurrent access. Any time a task requires exclusive
access to the resource, it takes the semaphore. When the task is finished with the resource, it gives the semaphore.

For example, there are two tasks, \( t_1 \) on CPU 0 and \( t_2 \) on CPU 1. Task \( t_1 \) creates the semaphore and advertises the semaphore’s ID by adding it to the database and assigning the name \texttt{myMutexSem}. Task \( t_2 \) looks up the name \texttt{myMutexSem} in the database to get the semaphore’s ID. Whenever a task wants to access the resource, it first takes the semaphore by using the semaphore ID. When a task is done using the resource, it gives the semaphore.

In the case of employing shared semaphores for synchronization, assume a task on one CPU must notify a task on another CPU that some event has occurred. The task being synchronized pends on the semaphore waiting for the event to occur. When the event occurs, the task doing the synchronizing gives the semaphore.

For example, there are two tasks, \( t_1 \) on CPU 0 and \( t_2 \) on CPU 1. Both \( t_1 \) and \( t_2 \) are monitoring robotic arms. The robotic arm that is controlled by \( t_1 \) is passing a physical object to the robotic arm controlled by \( t_2 \). Task \( t_2 \) moves the arm into position but must then wait until \( t_1 \) indicates that it is ready for \( t_2 \) to take the object. Task \( t_1 \) creates the shared semaphore and advertises the semaphore’s ID by adding it to the database and assigning the name \texttt{objReadySem}. Task \( t_2 \) looks up the name \texttt{objReadySem} in the database to get the semaphore’s ID. It then takes the semaphore by using the semaphore ID. If the semaphore is unavailable, \( t_2 \) pends, waiting for \( t_1 \) to indicate that the object is ready for \( t_2 \). When \( t_1 \) is ready to transfer control of the object to \( t_2 \), it gives the semaphore, readying \( t_2 \) on CPU1.

<table>
<thead>
<tr>
<th>Create Routine</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>\texttt{semBSmCreate()}</td>
<td>Creates a shared binary semaphore.</td>
</tr>
<tr>
<td>\texttt{semCSmCreate()}</td>
<td>Creates a shared counting semaphore.</td>
</tr>
</tbody>
</table>

There are two types of shared semaphores, binary and counting. Shared semaphores have their own create routines and return a \texttt{SEM_ID}. Table 12-3 lists the create routines. All other semaphore routines, except \texttt{semDelete()}, operate transparently on the created shared semaphore.
The use of shared semaphores and local semaphores differs in several ways:

- The shared semaphore queuing order specified when the semaphore is created must be FIFO. Figure 12-1 shows two tasks executing on different CPUs, both trying to take the same semaphore. Task 1 executes first, and is put at the front of the queue because the semaphore is unavailable (empty). Task 2 (executing on a different CPU) tries to take the semaphore after task 1’s attempt and is put on the queue behind task 1.

- Shared semaphores cannot be given from interrupt level.

- Shared semaphores cannot be deleted. Attempts to delete a shared semaphore return ERROR and set errno to S_smObjLib_NO_OBJECT_DESTROY.

Use semInfo() to get the shared task control block of tasks pended on a shared semaphore. Use semShow() to display the status of the shared semaphore and a list of pended tasks. (VxWorks must be configured with the INCLUDE_SEM_SHOW component.)
The following example displays detailed information on the shared semaphore `mySmSemaphoreId` as indicated by the second argument (0 = summary, 1 = details):

```
-> semShow mySmSemaphoreId, 1
value = 0 = 0x0
```

The output is sent to the standard output device, and looks like the following:

```
Semaphore Id : 0x36431d
Semaphore Type : SHARED BINARY
Task Queuing : FIFO
Pended Tasks : 2
State : EMPTY
TID    CPU Number     Shared TCB
----------------- -----------------
0x0d0618 1           0x364204
0x3be924 0           0x36421c
```

Example 12-1 **Shared Semaphores**

The following code example depicts two tasks executing on different CPUs and using shared semaphores. The routine `semTask1()` creates the shared semaphore, initializing the state to full. It adds the semaphore to the name database (to enable the task on the other CPU to access it), takes the semaphore, does some processing, and gives the semaphore. The routine `semTask2()` gets the semaphore ID from the database, takes the semaphore, does some processing, and gives the semaphore.

```c
/* semExample.h - shared semaphore example header file */
#define SEM_NAME "mySmSemaphore"

/* semTask1.c - shared semaphore example */
/* This code is executed by a task on CPU #1 */
#include <vxWorks.h>
#include <semLib.h>
#include <semSmLib.h>
#include <smNameLib.h>
#include <stdio.h>
#include <taskLib.h>
#include "semExample.h"

/*
 * semTask1 - shared semaphore user
 */

STATUS semTask1 (void)
{
    SEM_ID semSmId;
```
/* create shared semaphore */
if ((semSmId = semBSmCreate (SEM_Q_FIFO, SEM_FULL)) == NULL)
    return (ERROR);
/* add object to name database */
if (smNameAdd (SEM_NAME, semSmId, T_SM_SEM_B) == ERROR)
    return (ERROR);
/* grab shared semaphore and hold it for awhile */
semTake (semSmId, WAIT_FOREVER);
/* normally do something useful */
printf ("Task1 has the shared semaphore\n");
taskDelay (sysClkRateGet () * 5);
printf ("Task1 is releasing the shared semaphore\n");
/* release shared semaphore */
semGive (semSmId);
return (OK);
} /* semTask2.c - shared semaphore example */

/* This code is executed by a task on CPU #2. */
#include <vxWorks.h>
#include <semLib.h>
#include <semSmLib.h>
#include <smNameLib.h>
#include <stdio.h>
#include "semExample.h"

/* semTask2 - shared semaphore user */

STATUS semTask2 (void)
{
    SEM_ID semSmId;
    int objType;
    /* find object in name database */
    if (smNameFind (SEM_NAME, (void **) &semSmId, &objType, WAIT_FOREVER)
        == ERROR)
        return (ERROR);
/* take the shared semaphore */

    printf("semTask2 is now going to take the shared semaphore\n");
    semTake(semSmId, WAIT_FOREVER);

/* normally do something useful */

    printf("Task2 got the shared semaphore!!\n");

/* release shared semaphore */

    semGive(semSmId);

    printf("Task2 has released the shared semaphore\n");

    return (OK);
}

12.2.3 Shared Message Queues

Shared message queues are FIFO queues used by kernel tasks to send and receive variable-length messages on any of the CPUs that have access to the shared memory. They can be used either to synchronize tasks or to exchange data between kernel tasks running on different CPUs. See 3. Multitasking and the API reference for msgQLib for a complete discussion of message queues.

To use a shared message queue, a task creates the message queue and advertises its ID. A task that wants to send or receive a message with this message queue first gets the message queue’s ID. It then uses this ID to access the message queue.

For example, consider a typical server/client scenario where a server task t1 (on CPU 1) reads requests from one message queue and replies to these requests with a different message queue. Task t1 creates the request queue and advertises its ID by adding it to the name database assigning the name requestQue. If task t2 (on CPU 0) wants to send a request to t1, it first gets the message queue ID by looking up the name requestQue in the name database. Before sending its first request, task t2 creates a reply message queue. Instead of adding its ID to the database, it advertises the ID by sending it as part of the request message. When t1 receives the request from the client, it finds in the message the ID of the queue to use when replying to that client. Task t1 then sends the reply to the client by using this ID.

To pass messages between kernel tasks on different CPUs, first create the message queue by calling msgQSmCreate(). This routine returns a MSG_Q_ID. This ID is used for sending and receiving messages on the shared message queue.

Like their local counterparts, shared message queues can send both urgent or normal priority messages.
The use of shared message queues and local message queues differs in several ways:

- The shared message queue task queueing order specified when a message queue is created must be FIFO. Figure 12-2 shows two tasks executing on different CPUs, both trying to receive a message from the same shared message queue. Task 1 executes first, and is put at the front of the queue because there are no messages in the message queue. Task 2 (executing on a different CPU) tries to receive a message from the message queue after task 1’s attempt and is put on the queue behind task 1.

- Messages cannot be sent on a shared message queue at interrupt level. (This is true even in NO_WAIT mode.)

- Shared message queues cannot be deleted. Attempts to delete a shared message queue return ERROR and sets errno to S_smObjLib_NO_OBJECT_DESTROY.

To achieve optimum performance with shared message queues, align send and receive buffers on 4-byte boundaries.

To display the status of the shared message queue as well as a list of tasks pended on the queue, call msgQShow() (VxWorks must be configured with the
INCLUDE_MSG_Q_SHOW component.) The following example displays detailed information on the shared message queue 0x7f8c21 as indicated by the second argument (0 = summary display, 1 = detailed display).

    -> msgQShow 0x7f8c21, 1
    value = 0 = 0x0

The output is sent to the standard output device, and looks like the following:

    Message Queue Id : 0x7f8c21
    Task Queuing : FIFO
    Message Byte Len : 128
    Messages Max : 10
    Messages Queued : 0
    Receivers Blocked : 1
    Send timeouts : 0
    Receive timeouts : 0
    Receivers blocked :
    TID CPU Number Shared TCB
    ---------- -------------------- --------------
    0xd0618 1 0x1364204

Example 12-2  Shared Message Queues

In the following code example, two tasks executing on different CPUs use shared message queues to pass data to each other. The server task creates the request message queue, adds it to the name database, and reads a message from the queue. The client task gets the \texttt{smRequestQId} from the name database, creates a reply message queue, bundles the ID of the reply queue as part of the message, and sends the message to the server. The server gets the ID of the reply queue and uses it to send a message back to the client. This technique requires the use of the network byte-order conversion macros \texttt{htonl()} and \texttt{ntohl()}, because the numeric queue ID is passed over the network in a data field.

/* msgExample.h - shared message queue example header file */

#define MAX_MSG (10)
#define MAX_MSG_LEN (100)
#define REQUEST_Q "requestQue"

typedef struct message
{
    MSG_Q_ID replyQId;
    char clientRequest[MAX_MSG_LEN];
} REQUEST_MSG;

/* server.c - shared message queue example server */

/* This file contains the code for the message queue server task. */

#include <vxWorks.h>
#include <msgQLib.h>


```c
#include <msgQSmLib.h>
#include <stdio.h>
#include <smNameLib.h>
#include "msgExample.h"
#include "netinet/in.h"

#define REPLY_TEXT "Server received your request"

/* serverTask - receive and process a request from a shared message queue */

STATUS serverTask (void)
{
    MSG_Q_ID smRequestQId; /* request shared message queue */
    REQUEST_MSG request; /* request text */
    /* create a shared message queue to handle requests */
    if ((smRequestQId = msgQSmCreate (MAX_MSG, sizeof (REQUEST_MSG),
        MSG_Q_FIFO)) == NULL)
        return (ERROR);
    /* add newly created request message queue to name database */
    if (smNameAdd (REQUEST_Q, smRequestQId, T_SM_MSG_Q) == ERROR)
        return (ERROR);
    /* read messages from request queue */
    FOREVER
    {
        if (msgQReceive (smRequestQId, (char *) &request, sizeof (REQUEST_MSG),
            WAIT_FOREVER) == ERROR)
            return (ERROR);
        /* process request - in this case simply print it */
        printf ("Server received the following message:
            Server received your request\n", request.clientRequest);
        /* send a reply using ID specified in client’s request message */
        if (msgQSend ((MSG_Q_ID) ntohl ((int) request.replyQId),
            REPLY_TEXT, sizeof (REPLY_TEXT),
            WAIT_FOREVER, MSG_PRI_NORMAL) == ERROR)
            return (ERROR);
    }
}

/* client.c - shared message queue example client */

/* This file contains the code for the message queue client task. */

#include <vxWorks.h>
#include <msgQLib.h>
```
#include <msgQSmLib.h>
#include <smNameLib.h>
#include <stdio.h>
#include "msgExample.h"
#include "netinet/in.h"

/*
 * clientTask - sends request to server and reads reply
 */

STATUS clientTask
{
    char * pRequestToServer /* request to send to the server */
    /* limited to 100 chars */

    MSG_Q_ID smRequestQId; /* request message queue */
    MSG_Q_ID  smReplyQId; /* reply message queue */
    REQUEST_MSG request; /* request text */
    int objType; /* dummy variable for smNameFind */
    char serverReply[MAX_MSG_LEN]; /*buffer for server's reply */

    /* get request queue ID using its name */
    if (smNameFind (REQUEST_Q, (void **) &smRequestQId, &objType,
                    WAIT_FOREVER) == ERROR)
        return (ERROR);

    /* create reply queue, build request and send it to server */
    if ((smReplyQId = msgQSmCreate (MAX_MSG, MAX_MSG_LEN,
                                     MSG_Q_FIFO)) == NULL)
        return (ERROR);

    request.replyQId = (MSG_Q_ID) htonl ((int) smReplyQId);
    strcpy (request.clientRequest, pRequestToServer);

    if (msgQSend (smRequestQId, (char *) &request, sizeof (REQUEST_MSG),
                  WAIT_FOREVER, MSG_PRI_NORMAL) == ERROR)
        return (ERROR);

    /* read reply and print it */
    if (msgQReceive (request.replyQId, serverReply, MAX_MSG_LEN,
                     WAIT_FOREVER) == ERROR)
        return (ERROR);

    printf ("Client received the following message:
            \n\n", serverReply);
    return (OK);
}
12.2.4 Shared-Memory Allocator

The shared-memory allocator allows kernel tasks on different CPUs to allocate and release variable size chunks of memory that are accessible from all CPUs with access to the shared-memory system. Two sets of routines are provided: low-level routines for manipulating user-created shared-memory partitions, and high-level routines for manipulating a shared-memory partition dedicated to the shared-memory system pool. (This organization is similar to that used by the local-memory manager, memPartLib.)

Shared-memory blocks can be allocated from different partitions. Both a shared-memory system partition and user-created partitions are available. User-created partitions can be created and used for allocating data blocks of a particular size. Memory fragmentation is avoided when fixed-sized blocks are allocated from user-created partitions dedicated to a particular block size.

Shared-Memory System Partition

To use the shared-memory system partition, a task allocates a shared-memory block and advertises its address. One way of advertising the ID is to add the address to the name database. The routine used to allocate a block from the shared-memory system partition returns a local address. Before the address is advertised to tasks on other CPUs, this local address must be converted to a global address. Any task that must use the shared memory must first get the address of the memory block and convert the global address to a local address. When the task has the address, it can use the memory.

However, to address issues of mutual exclusion, typically a shared semaphore is used to protect the data in the shared memory. Thus in a more common scenario, the task that creates the shared memory (and adds it to the database) also creates a shared semaphore. The shared semaphore ID is typically advertised by storing it in a field in the shared data structure residing in the shared-memory block. The first time a task must access the shared data structure, it looks up the address of the memory in the database and gets the semaphore ID from a field in the shared data structure. Whenever a task must access the shared data, it must first take the semaphore. Whenever a task is finished with the shared data, it must give the semaphore.

For example, assume two tasks executing on two different CPUs must share data. Task t1 executing on CPU 1 allocates a memory block from the shared-memory system partition and converts the local address to a global address. It then adds the global address of the shared data to the name database with the name
mySharedData. Task \texttt{t1} also creates a shared semaphore and stores the ID in the first field of the data structure residing in the shared memory. Task \texttt{t2} executing on CPU 2 looks up the name \texttt{mySharedData} in the name database to get the address of the shared memory. It then converts this address to a local address. Before accessing the data in the shared memory, \texttt{t2} gets the shared semaphore ID from the first field of the data structure residing in the shared-memory block. It then takes the semaphore before using the data and gives the semaphore when it is done using the data.

User-Created Partitions

To make use of user-created shared-memory partitions, a task creates a shared-memory partition and adds it to the name database. Before a task can use the shared-memory partition, it must first look in the name database to get the partition ID. When the task has the partition ID, it can access the memory in the shared-memory partition.

For example, task \texttt{t1} creates a shared-memory partition and adds it to the name database using the name \texttt{myMemPartition}. Task \texttt{t2} executing on another CPU wants to allocate memory from the new partition. Task \texttt{t2} first looks up \texttt{myMemPartition} in the name database to get the partition ID. It can then allocate memory from it, using the ID.

Using the Shared-Memory System Partition

The shared-memory system partition is analogous to the system partition for local memory. Table 12-4 lists routines for manipulating the shared-memory system partition.

<table>
<thead>
<tr>
<th>Call</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>\texttt{smMemMalloc()}</td>
<td>Allocates a block of shared system memory.</td>
</tr>
<tr>
<td>\texttt{smMemCalloc()}</td>
<td>Allocates a block of shared system memory for an array.</td>
</tr>
<tr>
<td>\texttt{smMemRealloc()}</td>
<td>Resizes a block of shared system memory.</td>
</tr>
<tr>
<td>\texttt{smMemFree()}</td>
<td>Frees a block of shared system memory.</td>
</tr>
</tbody>
</table>
Routines that return a pointer to allocated memory return a local address (that is, an address suitable for use from the local CPU). To share this memory across processors, this address must be converted to a global address before it is advertised to tasks on other CPUs. Before a task on another CPU uses the memory, it must convert the global address to a local address. Macros and routines are provided to convert between local addresses and global addresses; see the header file `smObjLib.h` and the API reference for `smObjLib`.

Example 12.3 Shared-Memory System Partition

The following code example uses memory from the shared-memory system partition to share data between kernel tasks on different CPUs. The first member of the data structure is a shared semaphore that is used for mutual exclusion. The send task creates and initializes the structure, then the receive task accesses the data and displays it.

```c
/* buffProtocol.h - simple buffer exchange protocol header file */

#define BUFFER_SIZE 200 /* shared data buffer size */
#define BUFF_NAME "myMemory" /* name of data buffer in database */

typedef struct shared_buff
{
  SEM_ID semSmId;
  char  buff [BUFFER_SIZE];
} SHARED_BUFF;
```
/** buffSend.c - simple buffer exchange protocol send side */

/* This file writes to the shared memory. */

#include <vxWorks.h>
#include <semLib.h>
#include <semSmLib.h>
#include <smMemLib.h>
#include <smNameLib.h>
#include <smObjLib.h>
#include <stdio.h>
#include "buffProtocol.h"

/* buffSend - write to shared semaphore protected buffer */

STATUS buffSend (void)
{
   SHARED_BUFF * pSharedBuff;
   SEM_ID mySemSmId;

   /* grab shared system memory */
   pSharedBuff = (SHARED_BUFF *) smMemMalloc (sizeof (SHARED_BUFF));

   /* Initialize shared buffer structure before adding to database. The 
   * protection semaphore is initially unavailable and the receiver blocks. 
   */
   if ((mySemSmId = semBSmCreate (SEM_Q_FIFO, SEM_EMPTY)) == NULL)
      return (ERROR);
   pSharedBuff->semSmId = (SEM_ID) htonl ((int) mySemSmId);

   /* Convert address of shared buffer to a global address and add to 
   * database. 
   */
   if (smNameAdd (BUFF_NAME, (void *) smObjLocalToGlobal (pSharedBuff), 
                   T_SM_BLOCK) == ERROR)
      return (ERROR);

   /* put data into shared buffer */
   sprintf (pSharedBuff->buff,"Hello from sender\n");

   /* allow receiver to read data by giving protection semaphore */
   if (semGive (mySemSmId) != OK)
      return (ERROR);

   return (OK);
}
/* buffReceive.c - simple buffer exchange protocol receive side */
/* This file reads the shared memory. */

#include <vxWorks.h>
#include <semLib.h>
#include <semSmLib.h>
#include <smNameLib.h>
#include <smObjLib.h>
#include <stdio.h>
#include "buffProtocol.h"

/* buffReceive - receive shared semaphore protected buffer */

STATUS buffReceive (void)
{
    SHARED_BUFF * pSharedBuff;
    SEM_ID mySemSmId;
    int objType;

    /* get shared buffer address from name database */
    if (smNameFind (BUFF_NAME, (void **) &pSharedBuff,
                    &objType, WAIT_FOREVER) == ERROR)
        return (ERROR);

    /* convert global address of buff to its local value */
    pSharedBuff = (SHARED_BUFF *) smObjGlobalToLocal (pSharedBuff);

    /* convert shared semaphore ID to host (local) byte order */
    mySemSmId = (SEM_ID) ntohl ((int) pSharedBuff->semSmId);

    /* take shared semaphore before reading the data buffer */
    if (semTake (mySemSmId, WAIT_FOREVER) != OK)
        return (ERROR);

    /* read data buffer and print it */
    printf ("Receiver reading from shared memory: %s\n", pSharedBuff->buff);

    /* give back the data buffer semaphore */
    if (semGive (mySemSmId) != OK)
        return (ERROR);

    return (OK);
}
Using User-Created Partitions

Shared-memory partitions have a separate create routine, `memPartSmCreate()`, that returns a MEM_PART_ID. After a user-defined shared-memory partition is created, routines in `memPartLib` operate on it transparently. Note that the address of the shared-memory area passed to `memPartSmCreate()` (or `memPartAddToPool()`) must be the global address.

Example 12-4 User-Created Partition

This example is similar to Example 12-3, which uses the shared-memory system partition. This example creates a user-defined partition and stores the shared data in this new partition. A shared semaphore is used to protect the data.

```c
/* memPartExample.h - shared memory partition example header file */
#define CHUNK_SIZE (2400)
#define MEM_PART_NAME "myMemPart"
#define PART_BUFF_NAME "myBuff"
#define BUFFER_SIZE (40)

typedef struct shared_buff
{
    SEM_ID semSmId;
    char buff [BUFFER_SIZE];
} SHARED_BUFF;

/* memPartSend.c - shared memory partition example send side */
// This file writes to the user-defined shared memory partition. */

#include <vxWorks.h>
#include <memLib.h>
#include <semLib.h>
#include <semSmLib.h>
#include <smNameLib.h>
#include <smObjLib.h>
#include <smMemLib.h>
#include <stdio.h>
#include "memPartExample.h"

/* * memPartSend - send shared memory partition buffer */

STATUS memPartSend (void)
{
    char * pMem;
    PART_ID mmMemPartId;
    SEM_ID mySemSmId;
    SHARED_BUFF * pSharedBuff;
```
/* allocate shared system memory to use for partition */
pMem = smMemMalloc(CHUNK_SIZE);

/* Create user defined partition using the previously allocated block of memory.
 * WARNING: memPartSmCreate uses the global address of a memory pool as first parameter. */
if ((smMemPartId = memPartSmCreate(smObjLocalToGlobal(pMem), CHUNK_SIZE)) == NULL)
    return (ERROR);

/* allocate memory from partition */
pSharedBuff = (SHARED_BUFF *) memPartAlloc(smMemPartId, sizeof(SHARED_BUFF));
if (pSharedBuff == 0)
    return (ERROR);

/* initialize structure before adding to database */
if ((mySemSmId = semBSmCreate(SEM_Q_FIFO, SEM_EMPTY)) == NULL)
    return (ERROR);
pSharedBuff->semSmId = (SEM_ID) htonl((int)mySemSmId);

/* enter shared partition ID in name database */
if (smNameAdd(MEM_PART_NAME, (void *)smMemPartId, T_SM_PART_ID) == ERROR)
    return (ERROR);

/* convert shared buffer address to a global address and add to database */
if (smNameAdd(PART_BUFF_NAME, (void *)smObjLocalToGlobal(pSharedBuff), T_SM_BLOCK) == ERROR)
    return (ERROR);

/* send data using shared buffer */
sprintf(pSharedBuff->buff,"Hello from sender\n");
if (semGive(mySemSmId) != OK)
    return (ERROR);

return (OK);
/* memPartReceive.c - shared memory partition example receive side */
/* This file reads from the user-defined shared memory partition. */

#include <vxWorks.h>
#include <memLib.h>
#include <stdio.h>
#include <semLib.h>
#include <semSmLib.h>
#include <stdio.h>
#include "memPartExample.h"

>Status memPartReceive (void)
{
     SHARED_BUFF * pBuff;
     SEM_ID mySemSmId;
     int objType;

     /* get shared buffer address from name database */
     if (smNameFind (PART_BUFF_NAME, (void **) &pBuff, &objType,
                      WAIT_FOREVER) == ERROR)
         return (ERROR);

     /* convert global address of buffer to its local value */
     pBuff = (SHARED_BUFF *) smObjGlobalToLocal (pBuff);

     /* Grab shared semaphore before using the shared memory */
     mySemSmId = (SEM_ID) ntohl ((int) pBuff->semSmId);
     semTake (mySemSmId, WAIT_FOREVER);
     printf ("Receiver reading from shared memory: %s\n", pBuff->buff);
     semGive (mySemSmId);
     return (OK);
}

Side Effects of Shared-Memory Partition Options

Like their local counterparts, shared-memory partitions (both system- and user-created) can have different options set for error handling; see the API references for memPartOptionsSet() and smMemOptionsSet().

If the MEM_BLOCK_CHECK option is used in the following situation, the system can get into a state where the memory partition is no longer available. If a task
attempts to free a bad block and a bus error occurs, the task is suspended. Because shared semaphores are used internally for mutual exclusion, the suspended task still has the semaphore, and no other task has access to the memory partition. By default, shared-memory partitions are created without the MEM_BLOCK_CHECK option.

12.3 Internal Considerations

This section describes system requirements, and issues related to the spin-lock mechanism, interrupt latency, restrictions on shared-memory object use, and cache-coherency.

12.3.1 System Requirements

The shared-memory region used by shared-memory objects must be visible to all CPUs in the system. Either dual-ported memory on the master CPU (CPU 0) or a separate memory board can be used. The shared-memory objects’ anchor must be in the same address space as the shared-memory region. Note that the memory does not have to appear at the same local address for all CPUs.

⚠️ CAUTION: Boards that make use of VxMP must support hardware test-and-set (indivisible read-modify-write cycle). PowerPC is an exception; see the VxWorks Architecture Supplement.

All CPUs in the system must support indivisible read-modify-write cycle across the (VME) bus. The indivisible RMW is used by the spin-lock mechanism to gain exclusive access to internal shared data structures; see 12.3.2 Spin-lock Mechanism, p.596 for details. Because all the boards must support a hardware test-and-set, the parameter SM_TAS_TYPE must be set to SM_TAS_HARD.

CPUs must be notified of any event that affects them. The preferred method is for the CPU initiating the event to interrupt the affected CPU. The use of interrupts is dependent on the capabilities of the hardware. If interrupts cannot be used, a polling scheme can be employed, although this generally results in a significant performance penalty.
The maximum number of CPUs that can use shared-memory objects is 20 (CPUs numbered 0 through 19). The practical maximum is usually a smaller number that depends on the CPU, bus bandwidth, and application.

12.3.2 Spin-lock Mechanism

Internal shared-memory object data structures are protected against concurrent access by a spin-lock mechanism. The spin-lock mechanism is a loop where an attempt is made to gain exclusive access to a resource (in this case an internal data structure). An indivisible hardware read-modify-write cycle (hardware test-and-set) is used for this mutual exclusion. If the first attempt to take the lock fails, multiple attempts are made, each with a decreasing random delay between one attempt and the next.

Operating time for the spin-lock cycle varies greatly because it is affected by the processor cache, access time to shared memory, and bus traffic. If the lock is not obtained after the maximum number of tries specified by the SM_OBJ_MAX_TRIES parameter), errno is set to S_smObjLib_LOCK_TIMEOUT. If this error occurs, set the maximum number of tries to a higher value. Note that any failure to take a spin-lock prevents proper functioning of shared-memory objects. In most cases, this is due to problems with the shared-memory configuration; see 12.5.2 Troubleshooting Techniques, p.606.

12.3.3 Interrupt Latency

For the duration of the spin-lock, interrupts are disabled to avoid the possibility of a task being preempted while holding the spin-lock. As a result, the interrupt latency of each processor in the system is increased. However, the interrupt latency added by shared-memory objects is constant for a particular CPU.

12.3.4 Restrictions

Shared-memory objects are only available to kernel tasks. Unlike local semaphores and message queues, shared-memory objects cannot be used at interrupt level. No routines that use shared-memory objects can be called from ISRs. An ISR is dedicated to handle time-critical processing associated with an external event; therefore, using shared-memory objects at interrupt time is not appropriate. On a multiprocessor system, run event-related time-critical processing on the CPU where the time-related interrupt occurred.
Note that shared-memory objects are allocated from dedicated shared-memory pools, and cannot be deleted.

When using shared-memory objects, the maximum number of each object type must be specified; see 12.4.3 Initializing the Shared-Memory Objects Package, p. 599. If applications are creating more than the specified maximum number of objects, it is possible to run out of memory. If this happens, the shared object creation routine returns an error and `errno` is set to `S_memLib_NOT_ENOUGH_MEM`. To solve this problem, first increase the maximum number of shared-memory objects of corresponding type; see Table 12-5 for a list of the applicable configuration constants. This decreases the size of the shared-memory system pool because the shared-memory pool uses the remainder of the shared memory. If this is undesirable, increase both the number of the corresponding shared-memory objects and the size of the overall shared-memory region, `SM_OBJ_MEM_SIZE`. See 12.4 Configuration, p. 597 for a discussion of the constants used for configuration.

### 12.3.5 Cache Coherency

When dual-ported memory is used on some boards without MMU or bus snooping mechanisms, the data cache must be disabled for the shared-memory region on the master CPU. If you see the following error message, make sure that the `INCLUDE_CACHE_ENABLE` component is not included in the VxWorks configuration:

```
usrSmObjInit - cache coherent buffer not available. Giving up.
```

### 12.4 Configuration

To include VxMP shared-memory objects in VxWorks, configure the operating system with the `INCLUDE_SM_OBJ` component. Most of the configuration is already done automatically from `usrSmObjInit()` in the configlette `usrSmObj.c`. However, you may also need to modify some parameter values in to reflect your configuration; these are described in this section.
12.4.1 Shared-Memory Objects and Shared-Memory Network Driver

Shared-memory objects and the shared-memory network\(^2\) use the same memory region, anchor address, and interrupt mechanism. Configuring the system to use shared-memory objects is similar to configuring the shared-memory network driver. For a more detailed description of configuring and using the shared-memory network, see *Wind River Network Stack for VxWorks 6 Programmer’s Guide*. If the default value for the shared-memory anchor address is modified, the anchor must be on a 256-byte boundary.

One of the most important aspects of configuring shared-memory objects is computing the address of the shared-memory anchor. The shared-memory anchor is a location accessible to all CPUs on the system, and is used by both VxMP and the shared-memory network driver. The anchor stores a pointer to the shared-memory header, a pointer to the shared-memory packet header (used by the shared-memory network driver), and a pointer to the shared-memory object header.

The address of the anchor is defined with the `SM_ANCHOR_ADRS` parameter. If the processor is booted with the shared-memory network driver, the anchor address is the same value as the boot device (`sm=anchorAddress`). The shared-memory object initialization code uses the value from the boot line instead of the constant. If the shared-memory network driver is not used, modify the definition of `SM_ANCHOR_ADRS` as appropriate to reflect your system.

Two types of interrupts are supported and defined by `SM_INT_TYPE`: mailbox interrupts and bus interrupts (see *Wind River Network Stack for VxWorks 6 Programmer’s Guide*). Mailbox interrupts (`SM_INT_MAILBOX`) are the preferred method, and bus interrupts (`SM_INT_BUS`) are the second choice. If interrupts cannot be used, a polling scheme can be employed (`SM_INT_NONE`), but this is much less efficient.

When a CPU initializes its shared-memory objects, it defines the interrupt type as well as three interrupt arguments. These describe how the CPU is notified of events. These values can be obtained for any attached CPU by calling `smCpuInfoGet()`.

The default interrupt method for a target is defined with the `SM_INT_TYPE`, `SM_INT_ARG1`, `SM_INT_ARG2`, and `SM_INT_ARG3` parameters.

\(^2\) Also known as the *backplane network*. 
12.4.2 Shared-Memory Region

Shared-memory objects rely on a shared-memory region that is visible to all processors. This region is used to store internal shared-memory object data structures and the shared-memory system partition.

The shared-memory region is usually in dual-ported RAM on the master, but it can also be located on a separate memory card. The shared-memory region address is defined when configuring the system as an offset from the shared-memory anchor address, `SM_ANCHOR_ADRS`, as shown in Figure 12-3.

![Shared-Memory Layout](image)

**Figure 12-3** Shared-Memory Layout

12.4.3 Initializing the Shared-Memory Objects Package

Shared-memory objects are initialized by default in the routine `usrSmObjInit()` in `installDir/vxworks-6.x/target/src/config/usrSmObj.c`. The configuration steps taken for the master CPU differ slightly from those taken for the slaves.

The address for the shared-memory pool must be defined. If the memory is off-board, the value must be calculated (see Figure 12-5).

The example configuration in Figure 12-4 uses the shared memory in the master CPU’s dual-ported RAM.
For the master, the SM_OFF_BOARD parameter is FALSE and SM_ANCHOR_ADRS is 0x600. SM_OBJ_MEM_ADRS is set to NONE, because on-board memory is used (it is malloc’ed at run-time); SM_OBJ_MEM_SIZE is set to 0x20000. For the slave, the board maps the base of the VME bus to the address 0x1000000. SM_OFF_BOARD is TRUE and the anchor address is 0x1800600. This is calculated by taking the VMEbus address (0x800000) and adding it to the anchor address (0x600). Many boards require further address translation, depending on where the board maps VME memory. In this example, the anchor address for the slave is 0x1800600, because the board maps the base of the VME bus to the address 0x1000000.
In the example configuration in Figure 12-5, the shared memory is on a separate memory board. For the master, the SM_OFF_BOARD parameter is TRUE, SM_ANCHOR_ADRS is 0x3000000, SM_OBJ_MEM_ADRS is set to SM_ANCHOR_ADRS, and SM_OBJ_MEM_SIZE is set to 0x100000. For the slave board, SM_OFF_BOARD is TRUE and the anchor address is 0x2100000. This is calculated by taking the VMEbus address of the memory board (0x2000000) and adding it to the local VMEbus address (0x100000).

Some additional configuration is sometimes required to make the shared memory non-cacheable, because the shared-memory pool is accessed by all processors on the backplane. By default, boards with an MMU have the MMU turned on. With the MMU on, memory that is off-board must be made non-cacheable. This is done using the data structure sysPhysMemDesc in sysLib.c. This data structure must contain a virtual-to-physical mapping for the VME address space used for the shared-memory pool, and mark the memory as non-cacheable. (Most BSPs include this mapping by default.)

⚠️ **CAUTION:** For the MC68K in general, if the MMU is off, data caching must be turned off globally; see the API reference for cacheLib.

When shared-memory objects are initialized, the memory size as well as the maximum number of each object type must be specified. The master processor
specifies the size of memory using the constant `SM_OBJ_MEM_SIZE`. Symbolic constants are used to set the maximum number of different objects. These constants are described in Table 12-5.

<table>
<thead>
<tr>
<th>Symbolic Constant</th>
<th>Default Value</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>SM_OBJ_MAX_TASK</td>
<td>40</td>
<td>Maximum number of tasks using shared-memory objects.</td>
</tr>
<tr>
<td>SM_OBJ_MAX_SEM</td>
<td>60</td>
<td>Maximum number of shared semaphores (counting and binary).</td>
</tr>
<tr>
<td>SM_OBJ_MAX_NAME</td>
<td>100</td>
<td>Maximum number of names in the name database.</td>
</tr>
<tr>
<td>SM_OBJ_MAX_MSG_Q</td>
<td>10</td>
<td>Maximum number of shared message queues.</td>
</tr>
<tr>
<td>SM_OBJ_MAX_MEM_PART</td>
<td>4</td>
<td>Maximum number of user-created shared-memory partitions.</td>
</tr>
</tbody>
</table>

If the size of the objects created exceeds the shared-memory region, an error message is displayed on CPU 0 during initialization. After shared memory is configured for the shared objects, the remainder of shared memory is used for the shared-memory system partition.

The routine `smObjShow()` displays the current number of used shared-memory objects and other statistics, as follows:

```c
    -> smObjShow
    value = 0 = 0x0
```

The `smObjShow()` routine is automatically included if VxWorks is configured with the `INCLUDE_SM_OBJ` component.
The output of `smObjShow()` is sent to the standard output device, and looks like the following:

```
Shared Mem Anchor Local Addr : 0x600
Shared Mem Hdr Local Addr   : 0x363ed0
Attached CPU                : 2
Max Tries to Take Lock      : 0

<table>
<thead>
<tr>
<th>Shared Object Type</th>
<th>Current</th>
<th>Maximum</th>
<th>Available</th>
</tr>
</thead>
<tbody>
<tr>
<td>Tasks</td>
<td>1</td>
<td>40</td>
<td>39</td>
</tr>
<tr>
<td>Binary Semaphores</td>
<td>3</td>
<td>30</td>
<td>27</td>
</tr>
<tr>
<td>Counting Semaphores</td>
<td>0</td>
<td>30</td>
<td>27</td>
</tr>
<tr>
<td>Messages Queues</td>
<td>1</td>
<td>10</td>
<td>9</td>
</tr>
<tr>
<td>Memory Partitions</td>
<td>1</td>
<td>4</td>
<td>3</td>
</tr>
<tr>
<td>Names in Database</td>
<td>5</td>
<td>100</td>
<td>95</td>
</tr>
</tbody>
</table>
```

**CAUTION:** If the master CPU is rebooted, it is necessary to reboot all the slaves. If a slave CPU is to be rebooted, it must not have tasks pended on a shared-memory object.

### 12.4.4 Configuration Example

The following example shows the configuration for a multiprocessor system with three CPUs. The master is CPU 0, and shared memory is configured from its dual-ported memory. This application has 20 tasks using shared-memory objects, and uses 12 message queues and 20 semaphores. The maximum size of the name database is the default value (100), and only one user-defined memory partition is required. On CPU 0, the shared-memory pool is configured to be on-board. This memory is allocated from the processor’s system memory. On CPU 1 and CPU 2, the shared-memory pool is configured to be off-board. Table 12-6 shows the parameter values set for the `INCLUDE_SM_OBJ` and `INCLUDE_SM_COMMON` components.

<table>
<thead>
<tr>
<th>CPU</th>
<th>Symbolic Constant</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Master (CPU 0)</td>
<td>SM_OBJ_MAX_TASK</td>
<td>20</td>
</tr>
<tr>
<td></td>
<td>SM_OBJ_MAX_SEM</td>
<td>20</td>
</tr>
<tr>
<td></td>
<td>SM_OBJ_MAX_NAME</td>
<td>100</td>
</tr>
<tr>
<td></td>
<td>SM_OBJ_MAX_MSG_Q</td>
<td>12</td>
</tr>
</tbody>
</table>

Table 12-6 Configuration Settings for Three CPU System
Table 12-6 Configuration Settings for Three CPU System (cont’d)

<table>
<thead>
<tr>
<th>CPU</th>
<th>Symbolic Constant</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>SM_OBJ_MAX_MEM_PART</td>
<td>1</td>
</tr>
<tr>
<td></td>
<td>SM_OFF_BOARD</td>
<td>FALSE</td>
</tr>
<tr>
<td></td>
<td>SM_MEM_ADRS</td>
<td>NONE</td>
</tr>
<tr>
<td></td>
<td>SM_MEM_SIZE</td>
<td>0x10000</td>
</tr>
<tr>
<td></td>
<td>SM_OBJ_MEM_ADRS</td>
<td>NONE</td>
</tr>
<tr>
<td></td>
<td>SM_OBJ_MEM_SIZE</td>
<td>0x10000</td>
</tr>
<tr>
<td>Slaves (CPU 1, CPU 2)</td>
<td>SM_OBJ_MAX_TASK</td>
<td>20</td>
</tr>
<tr>
<td></td>
<td>SM_OBJ_MAX_SEM</td>
<td>20</td>
</tr>
<tr>
<td></td>
<td>SM_OBJ_MAX_NAME</td>
<td>100</td>
</tr>
<tr>
<td></td>
<td>SM_OBJ_MAX_MSG_Q</td>
<td>12</td>
</tr>
<tr>
<td></td>
<td>SM_OBJ_MAX_MEM_PART</td>
<td>1</td>
</tr>
<tr>
<td></td>
<td>SM_OFF_BOARD</td>
<td>FALSE</td>
</tr>
<tr>
<td></td>
<td>SM_ANCHOR_ADRS</td>
<td>(char *) 0xfb800000</td>
</tr>
<tr>
<td></td>
<td>SM_MEM_ADRS</td>
<td>SM_ANCHOR_ADRS</td>
</tr>
<tr>
<td></td>
<td>SM_MEM_SIZE</td>
<td>0x10000</td>
</tr>
<tr>
<td></td>
<td>SM_OBJ_MEM_ADRS</td>
<td>NONE</td>
</tr>
<tr>
<td></td>
<td>SM_OBJ_MEM_SIZE</td>
<td>0x10000</td>
</tr>
</tbody>
</table>

Note that for the slave CPUs, the value of SM_OBJ_MEM_SIZE is not actually used.

12.4.5 Initialization Steps

Initialization is performed by default in `usrSmObjInit()`, in `installDir/vxworks-6.x/target/src/config/usrSmObj.c`. On the master CPU, the initialization of shared-memory objects consists of the following:
12 Shared-Memory Objects: VxMP

12.5 Troubleshooting

1. Setting up the shared-memory objects header and its pointer in the shared-memory anchor, with `smObjSetup()`.
2. Initializing shared-memory object parameters for this CPU, with `smObjInit()`.
3. Attaching the CPU to the shared-memory object facility, with `smObjAttach()`.

On slave CPUs, only steps 2 and 3 are required.

The routine `smObjAttach()` checks the setup of shared-memory objects. It looks for the shared-memory heartbeat to verify that the facility is running. The shared-memory heartbeat is an unsigned integer that is incremented once per second by the master CPU. It indicates to the slaves that shared-memory objects are initialized, and can be used for debugging. The heartbeat is the first field in the shared-memory object header; see 12.5 Troubleshooting, p.605.

12.5 Troubleshooting

Problems with shared-memory objects can be due to a number of causes. This section discusses the most common problems and a number of troubleshooting tools. Often, you can locate the problem by rechecking your hardware and software configurations.

12.5.1 Configuration Problems

Use the following list to confirm that your system is properly configured:

- Be sure to verify that VxWorks is configured with the `INCLUDE_SM_OBJ` component for each processor using VxMP.
- Be sure the anchor address specified is the address seen by the CPU. This can be defined with the `SM_ANCHOR_ADRS` parameter or at boot time `(sm=)` if the target is booted with the shared-memory network.
- If there is heavy bus traffic relating to shared-memory objects, bus errors can occur. Avoid this problem by changing the bus arbitration mode or by changing relative CPU priorities on the bus.
If `memAddToPool()`, `memPartSmCreate()`, or `smMemAddToPool()` fail, check that any address you are passing to these routines is in fact a global address.

### 12.5.2 Troubleshooting Techniques

Use the following techniques to troubleshoot any problems you encounter:

- The routine `smObjTimeoutLogEnable()` enables or disables the printing of an error message indicating that the maximum number of attempts to take a spin-lock has been reached. By default, message printing is enabled.

- The routine `smObjShow()` displays the status of the shared-memory objects facility on the standard output device. It displays the maximum number of tries a task took to get a spin-lock on a particular CPU. A high value can indicate that an application might run into problems due to contention for shared-memory resources.

- The shared-memory heartbeat can be checked to verify that the master CPU has initialized shared-memory objects. The shared-memory heartbeat is in the first 4-byte word of the shared-memory object header. The offset to the header is in the sixth 4-byte word in the shared-memory anchor. (See *Wind River Network Stack for VxWorks 6 Programmer’s Guide*.)

Thus, if the shared-memory anchor were located at 0x800000:

```
[VxWorks Boot]: d 0x800000
800000: 8765 4321 0000 0001 0000 0000 0000 002c ..eC!............,*
800010: 0000 0000 0000 0170 0000 0000 0000 0000 ...p............*
800020: 0000 0000 0000 0000 0000 0000 0000 0000 ................*
```

The offset to the shared-memory object header is 0x170. To view the shared-memory object header display 0x800170:

```
[VxWorks Boot]: d 0x800170
800170: 0000 0050 0000 0000 0bfc 0000 0350 ...P...........P*
```

In the preceding example, the value of the shared-memory heartbeat is 0x50. Display this location again to ensure that the heartbeat is alive; if its value has changed, shared-memory objects are initialized.

- The global variable `smIfVerbose`, when set to 1 (TRUE), causes shared-memory interface error messages to print to the console, along with additional details of shared-memory operations. This variable enables you to get run-time information from the device driver level that would be unavailable at the debugger level. The default setting for `smIfVerbose` is 0 (FALSE). That can be reset programmatically or from the shell.
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