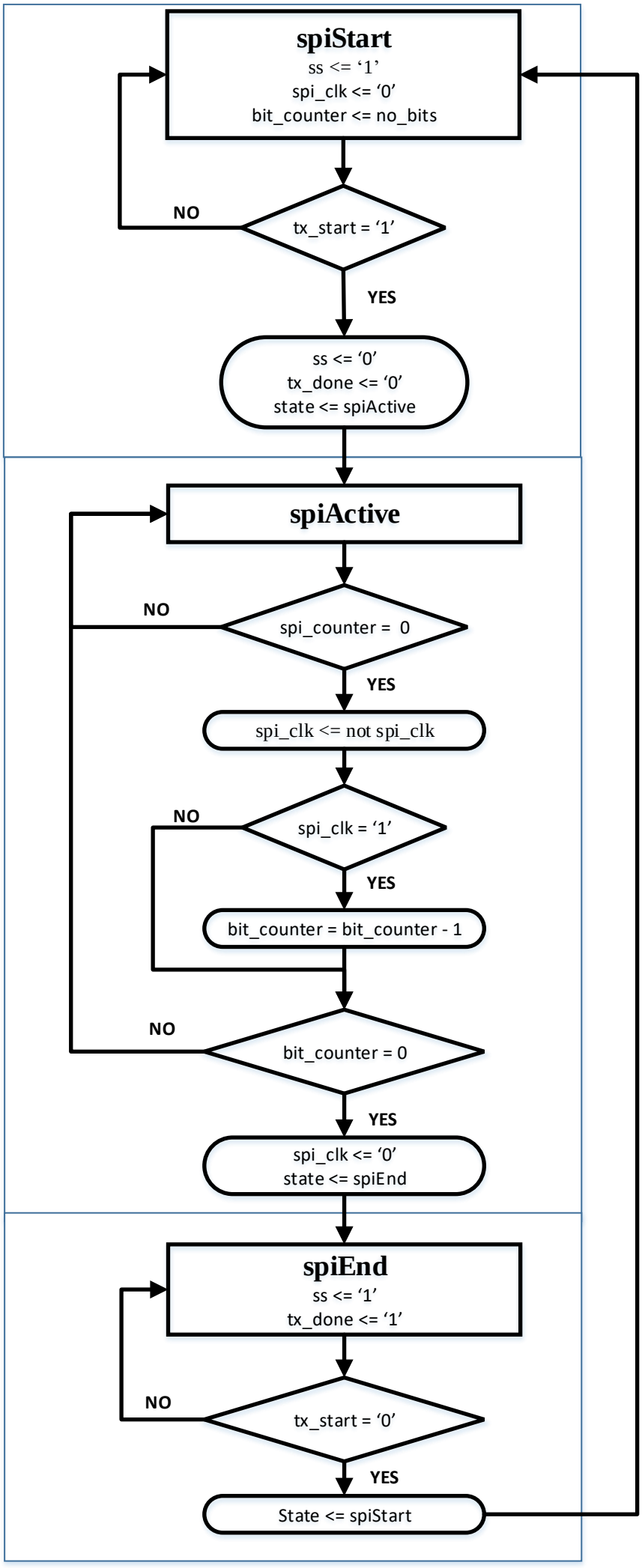

spiStart
ss <= '1'
spi_clk <= '0'
bit_counter <= no_bits
NO
tx_start = '1'
YES
ss <= '0'
tx_done <= '0'
state <= spiActive
spiActive
NO
spi_counter = 0
YES
spi_clk <= not spi_clk
NO
spi_clk = '1'
YES
bit_counter = bit_counter - 1
NO
bit_counter = 0
YES
spi_clk <= '0'
state <= spiEnd
spiEnd
ss <= '1'
tx_done <= '1'
NO
tx_start = '0'
YES
State <= spiStart