University of Oslo
Faculty of Mathematics and Natural Sciences

Final Exam in: INF4411/3410, fall 2015
Date of exam: 17-Dec-2015
Time: 14:30-18:30
This examination paper consists of 5 page(s)
Permitted materials: All printed and written including approved (!) calculator

Make sure that your copy of this examination paper is complete before answering.

Read Carefully:

This is the final exam of INF4410 and INF3410, fall 2015. It will contribute 60% towards the final grade, while the other 40% are contributed by the mandatory labs 2 and 3 of the course. The task 8 under chapter 9 is *RESERVED for INF4411 ONLY!* Students that are registered as bachelor students in INF3410 shall *NOT* solve this task! No extra points will be given if they should solve the task!

It is recommended that you read all the tasks before starting with the exam. A good strategy thereafter is to start with the tasks you feel most comfortable with and leave the most difficult ones for last.

It is also recommended to read through all the tasks once more after you have finished, to make sure you have not overlooked anything.

Start a new page for every task you are answering and write your ID-number and task number on the top of all pages you submit. If you use several pages per task also mark the page number next to the task number. Make sure to answer all questions and follow all instructions. Clearly mark your final answers and results (all that are explicitly asked for or instructed to find in a task) with a double underline. Document your deduction of those results and any assumptions you may have had to make.

Note that you may not hand in the original task papers that you are holding now, i.e. do not write any results here but use exclusively the separate sheets of paper that you are going to hand in.

Each question is graded indicating the weight when grading.

Chapter 1

Task 1 (3p): Using the device parameters of the 0.18μm process in the table 1.5 in the course book (table 1), and L=0.36μm, select the device width W and V_{GS} required to bias the NFET with an intrinsic gain $A_i = 20$ and transconductance $g_m = 50\frac{\mu A}{V}$. What DC current consumption (large signal I_{DS})is required?

Table 1.5 MOSFET parameters representative of various CMOS technologies and used for rough hand calculations in this text.

	0.8 μm		0.35 μm		0.18 μm		45 nm	
Technology	**NMOS**	**PMOS**	**NMOS**	**PMOS**	**NMOS**	**PMOS**	**NMOS**	**PMOS**
μC_{ox} ($\mu A/V^2$)	92	30	190	55	270	70	280	70
V_{t0} (V)	0.80	–0.90	0.57	–0.71	0.45	–0.45	0.45	–0.45
$\lambda \cdot L$ (μm/V)	0.12	0.08	0.16	0.16	0.08	0.08	0.10	0.15
C_{ox} ($fF/\mu m^2$)	1.8	1.8	4.5	4.5	8.5	8.5	25	25
t_{ox} (nm)	18	18	8	8	5	5	1.2	1.2
n	1.5	1.5	1.8	1.7	1.6	1.7	1.85	1.85
θ (1/V)	0.06	0.135	1.5	1.0	1.7	1.0	2.3	2.0
m	1.0	1.0	1.8	1.8	1.6	2.4	3.0	3.0
$C_{ov}/W = L_{ov}C_{ox}$ (fF/μm)	0.20	0.20	0.20	0.20	0.35	0.35	0.50	0.50
$C_{db}/W \approx C_{sb}/W$ (fF/μm)	0.50	0.80	0.75	1.10	0.50	0.55	0.45	0.50

Table 1.5
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Table 1: Typical process parameters

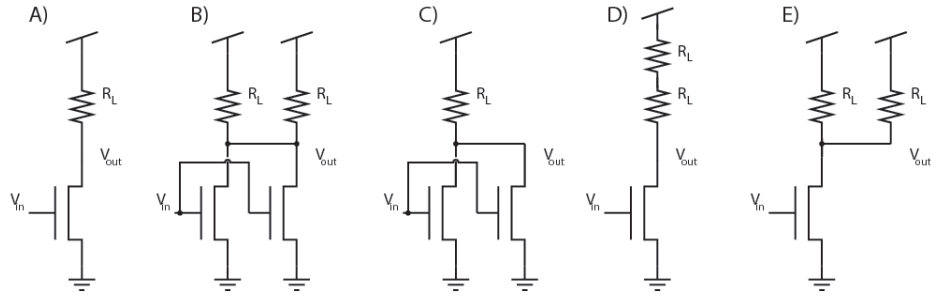


Figure 1: Different common source gain stages.

Chapter 3

Task 2 (4p): Consider the common source gain stages in figure 1. Defining the small signal DC gain of the circuit A) as being $-A_A$, can you express the DC gains $-A_{B,C,D,E}$ of the other circuits B),C),D), E) relative to this gain $-A_A$? Assume that all circuits are biased at the same point of operation in strong inversion and that the resistance R_L is the same as the r_{ds} of all the NFETs. All transistors have the same width to length ratio W/L.

Chapter 4

Task 3 (4p): For the following three transfer functions, draw a Bode plot with both a magnitude and phase plot. Remember to mark the values of interesting points in the graph clearly and to draw gradients correctly! Note that H_3 is the most challenging of the three and worth two of the 4 points. You will

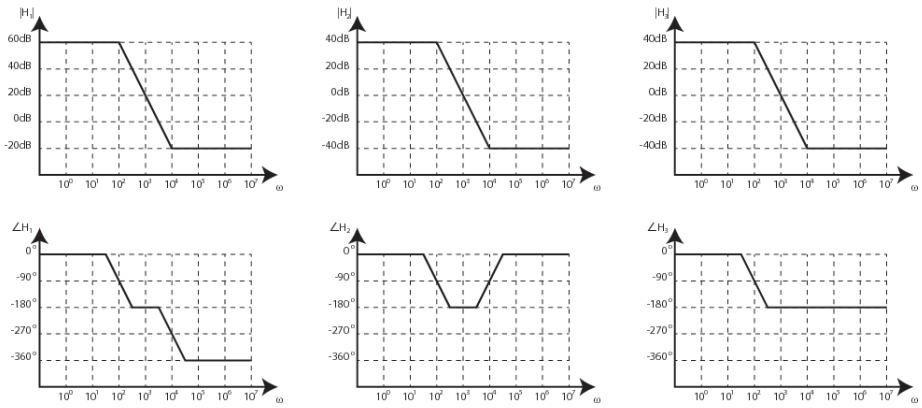


Figure 2: Bode plots for three different transfer functions $H_{1,2,3}$ for task 4

have to reason a bit beyond what is explicitly stated in the book, but a few of the qualitative properties of the graph you might be able to deduce correctly. However, you might not want to dwell on this particular task for too long and might want to save it for last if you have some spare time..

$$H_1(s) = 100\frac{\left(1+\frac{s}{10000}\right)}{\left(1+\frac{s}{10000}\right)\left(1+\frac{s}{100000}\right)} \tag{1}$$

$$H_2(s) = 100\frac{\left(1-\frac{s}{10000}\right)}{\left(1+\frac{s}{10000}\right)\left(1+\frac{s}{100000}\right)} \tag{2}$$

$$H_3(s) = 100\frac{\left(1+\frac{s}{\sqrt{2}\,10000}\right)}{\left(1+\frac{s}{(1+j)10000}\right)\left(1+\frac{s}{(1-j)10000}\right)} \tag{3}$$

Task 4 (3p): For the three Bode plots in figure 2, write down the corresponding transfer function in root-form. If there are several solutions, write down all of them.

Chapter 5

Task 5 (3p) Consider the compensated two stage amplifier in figure 3 without (!) the feedback loop (dashed lines), i.e. in open loop and $V_1 = V_{in}$. Note that the second amplifier stage is an ideal amplifier that has infinite input impedance and zero output impedance and a gain -A_2 that is independent of the frequency. Compute the open loop transfer function of the circuit and sketch a Bode plot. What is the order of the transfer function? $A_2 = 25$, $g_{m1} = 5\mu\frac{\mathrm{A}}{\mathrm{V}}$, $r_{out1} = 300\mathrm{k}\Omega$. (Hint: use the Miller theorem)

Task 6 (3p) Given the open loop 2nd order low pass transfer function of equation (4), if you connect it in a feedback loop where the input $v_x = v_{in} - \beta v_{out}$ as depicted in figure 4, what is the biggest β you can choose for the phase margin to be at least 60^o?

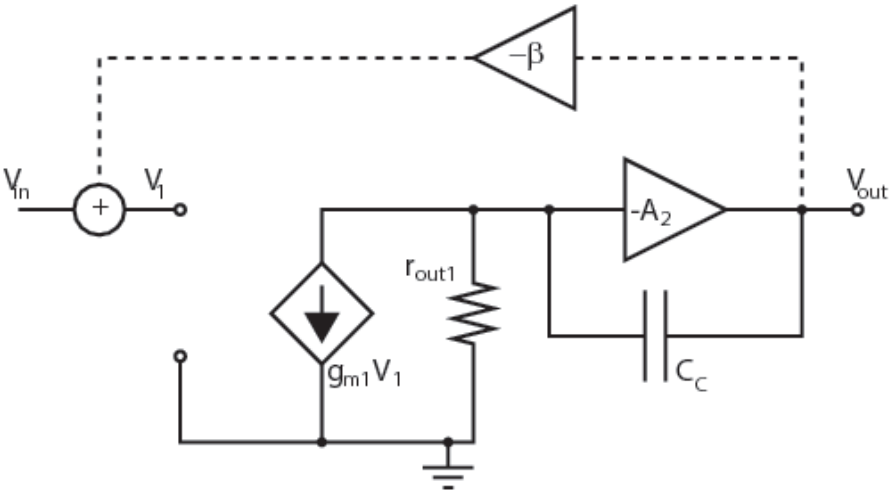


Figure 3: Two stage amplifier with capacitive feedback. The first stage is a transconductance amplifier while the second stage is an idealized voltage amplifier with infinite input impedance and zero output impedance and frequency independent gain.

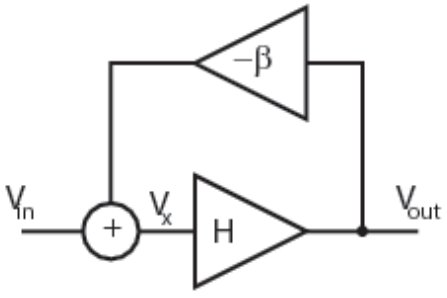


Figure 4: General negative feedback loop

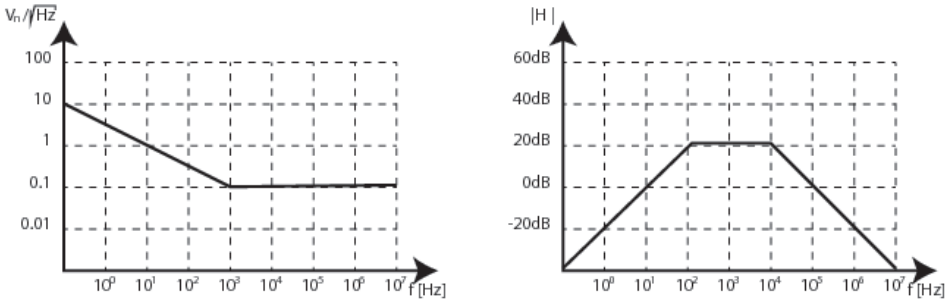


Figure 5: Input noise root spectral density and transfer function of a bandpass amplifier

$$H(s) = 10\frac{1}{(1+\frac{s}{100})(1+\frac{s}{500})} \tag{4}$$

Chapter 6

Task 7 (2p): An OTA has a G_{MA} (transconductance of the entire circuit when measuring change of output current at a fixed output voltage vs. change of input voltage) of 2mA/V and a DC gain of 60dB. What is its output resistance r_{out}?

Chapter 9 (only for INF4411!)

The task 8 of chapter 9 is mandatory for students having registered as master students to course INF4411, and are NOT(!) to be solved for bachelor students registered to course INF3410. No extra point are to be gained for bachelor students registered to course INF3410 by solving this task under chapter 9!

Task 8 (only INF4411, 5p): Consider a band pass amplifier that has equivalent input noise root spectral density and transfer function magnitude as shown in figure 5. Sketch the root spectral density for the output signal. Compute the total output root mean square noise ($V_{n(rms)}$) in the depicted noise bandwidth from 0.1Hz to 10MHz using piece-wise integration of the output spectral density. (Hint: Remember that $\int \frac{1}{x}dx = |\ln x|$ and $\int x^a dx = \frac{1}{1+a}x^{a+1}$ for $a \neq -1$)