



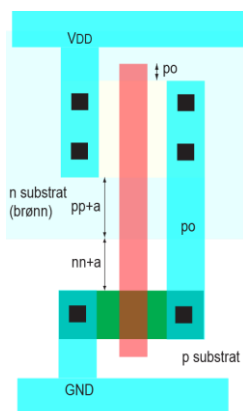
INF3400 Del 4

Moderne MOS transistor modell, transient simulering og enkle utleggsregler

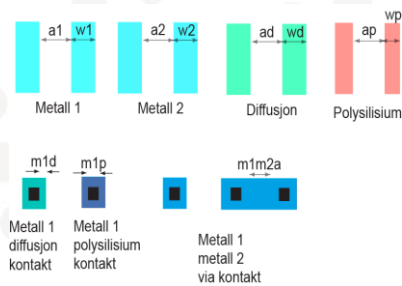
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Introduksjon til utleggsregler

Enkle
utleggsregler:



Inverter



Enkle MOS kapasitans modeller

Gatekapasitens:

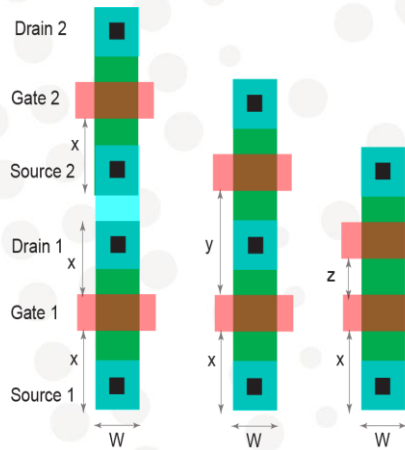
$$C_g = C_{ox}WL$$

der

$$C_g = C_{permicron}W$$

der

$$C_{permicron} = C_{ox}L$$



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Oppgave 2.4

En transistor med lengde 90nm har en tykkelse på gateoksid (t_{ox}) lik 16\AA .
Hva blir gatekapasitans per mikrometer?

Løsning:

$$C_{permicron} = C_{ox}L$$

$$= \frac{\epsilon_{ox}}{t_{ox}} L$$

$$= \frac{3.9 \cdot 8.85 \cdot 10^{-14}}{16 \cdot 10^{-8}} \frac{F}{cm} \cdot 10 \cdot 10^{-7} cm$$

$$= 1.94 \frac{fF}{\mu m}$$



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Gatekapasitans detaljer

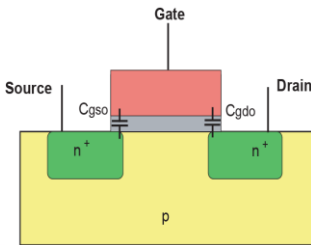
Ubiasert gatekapasitans:

$$C_0 = C_{ox}WL$$

Overlappskapasitanser (statiske):

$$C_{gs0} = C_{gsol}W$$

$$C_{gd0} = C_{gdol}W$$



Operasjonsområde **AV**:

$$C_{gb} \approx C_0$$

Operasjonsområde **LINEÆR**:

$$C_{gs} = C_{gd} = \frac{C_0}{2}$$

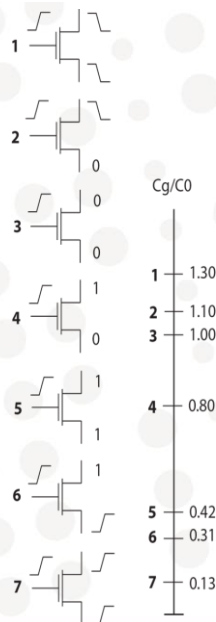
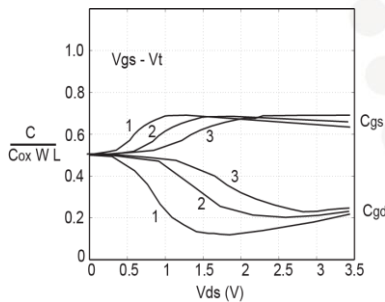
Operasjonsområde **METNING**:

$$C_{gs} = \frac{2C_0}{3}$$

$$C_{gd} = 0$$

Gatekapasitans:

$$C_g = C_{gs} + C_{gd} + C_{gb} \approx C_0$$



Diffusjonskapasitans detaljer

Diffusjonskapasitans source:

$$C_{sb} = AS \cdot C_{jbs} + PS \cdot C_{jbssw}$$

der:

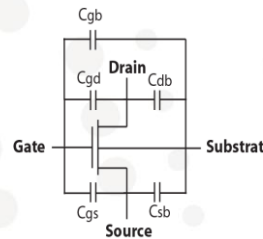
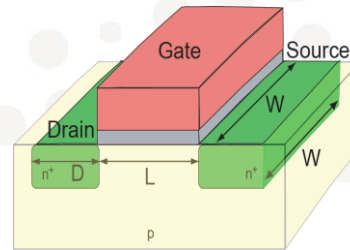
$$AS = W \cdot D$$

$$PS = 2W + 2D$$

$$C_{jbs} = C_j \left(1 + \frac{V_{sb}}{\Psi_0} \right)^{-M_j}$$

$$\Psi_0 = V_T \ln \frac{N_A N_D}{n_i^2}$$

$$C_{jbssw} = C_{JSW} \left(1 + \frac{V_{sb}}{\Psi_0} \right)^{-M_{JSW}}$$



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Oppgave 2.5

Beregn diffusjonskapasitans C_{db} for en transistor med en (minimum) kontakt på drain i en $0.6\mu\text{m}$ prosess når drainspenningen er 0V og $V_{DD} = 5\text{V}$. Anta at substratet er jordet. Parameterverdier er $C_j = 0.42\text{fF}/\mu\text{m}^2$, $M_j = 0.44$, $C_{JSW} = 0.33\text{fF}/\mu\text{m}$, $M_{JSW} = 0.12$ og $\Psi_0 = 0.98\text{V}$ ved romtemperatur.

Løsning:

$$\begin{aligned} C_{db}(0\text{V}) &= AD \cdot C_{jbs} + PD \cdot C_{jbssw} \\ &= 0.8\mu\text{m}^2 \left(0.42 \frac{\text{fF}}{\mu\text{m}^2} \right) + 0.4\mu\text{m} \left(0.33 \frac{\text{fF}}{\mu\text{m}} \right) \\ &= 2.54\text{fF} \end{aligned}$$

$$\begin{aligned} C_{db}(5\text{V}) &= AD \cdot C_{jbs} + PD \cdot C_{jbssw} \\ &= 0.8 \tilde{C}_j \left(1 + \frac{V_{db}}{\Psi_0} \right)^{-M_j} + 0.4\mu\text{m} \tilde{C}_{JSW} \left(1 + \frac{V_{db}}{\Psi_0} \right)^{-M_{JSW}} \\ &= 0.8\mu\text{m}^2 \cdot 0.42 \left(1 + \frac{5}{0.98} \right)^{-0.44} + 0.4\mu\text{m} \cdot 0.33 \left(1 + \frac{5}{0.98} \right)^{-0.12} \\ &= 1.78\text{fF} \end{aligned}$$



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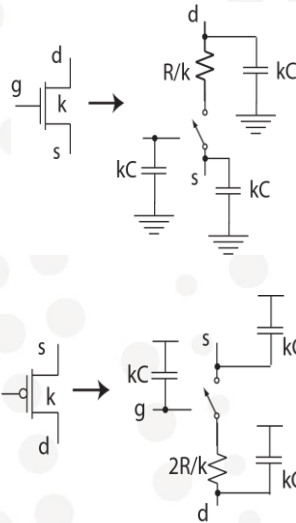
ENKLE RC modeller

Motstand i transistor:

$$R = \left(\frac{\partial I_{ds}}{\partial V_{ds}} \right)^{-1}$$

$$\approx \beta \left(V_{gs} - V_t \right)$$

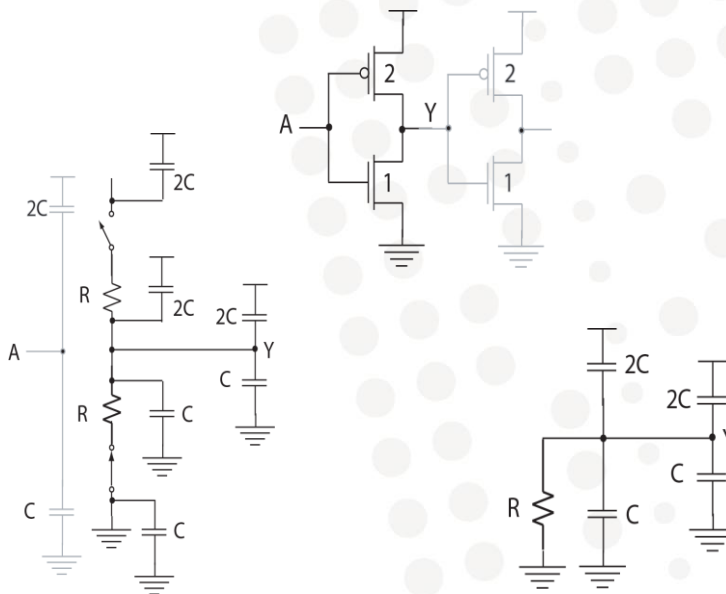
$$\approx \frac{1}{\mu C_{ox}} \frac{L}{W} \left(V_{gs} - V_t \right)$$



ifj



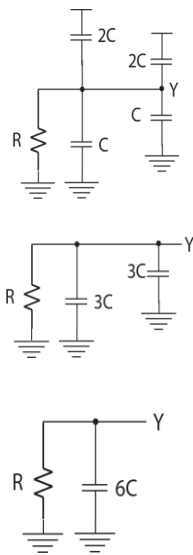
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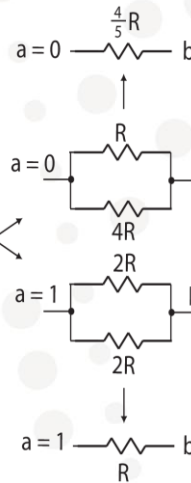
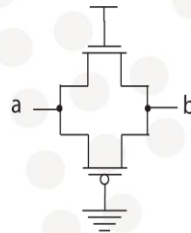
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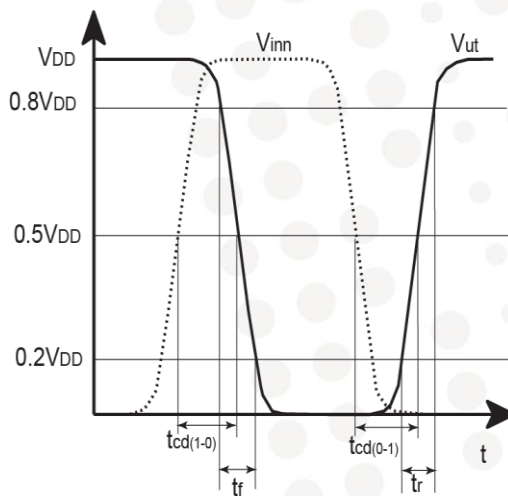
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Transmisjonsport:



RC forsinkelsesmodeller



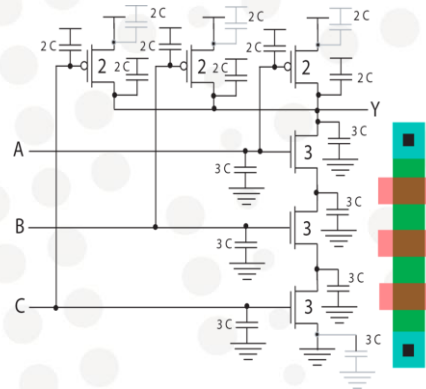
Seriekobling av transistorer:

$$R_{effektiv} = \sum_{i=1}^n \frac{R}{k_i}$$

Parallellkobling av transistorer:

$$\begin{aligned} R_{effektiv} &= R \parallel R \\ &= \frac{R}{2} \end{aligned}$$

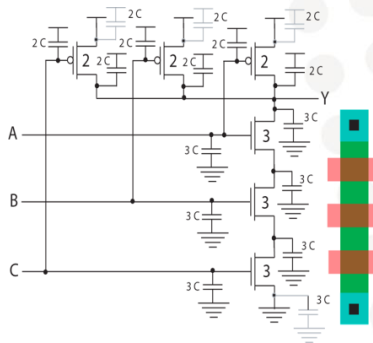
Eksempel NAND3:



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Transisjon fra 0 til 1:

$$\begin{aligned} R_{effektiv} &= \frac{2R}{2} \\ &= R \end{aligned}$$

Transisjon fra 1 til 0:

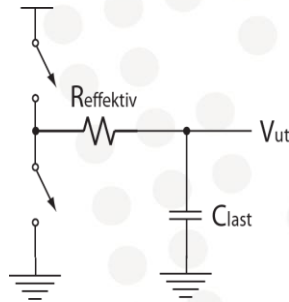
$$\begin{aligned} R_{effektiv} &= \left(\frac{1}{3} + \frac{1}{3} + \frac{1}{3} \right) R \\ &= R \end{aligned}$$

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RC modell

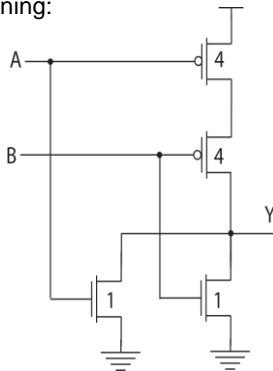


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Oppgave C

Tegn transistorskjematikk for en toinngangs NOR port med transistor bredder slik at effektiv motstand i nedtrekket blir lik en enhetsinverter. Beregn stige og fall forsinkelse når porten skal drive h identiske NOR porter ved å bruke enkle RC modeller.

Løsning:



$$R_{\text{opptrekk}} = \frac{2R}{4} + \frac{2R}{4}$$

$$= R$$

$$C_{\text{parasitic}} = 2C + 4C + 4C$$

$$= 10C$$

$$t_{\text{parasitic}} = R \cdot 10C$$

$$= \frac{10}{3} \tau$$

$$t_{pd} = 10C + h \cdot 5C \cdot R$$

$$= 10C + h \cdot 5RC$$

$$= 10C + h \cdot \frac{5}{3} \tau$$



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Hastighetsmetning

Hastigheten til ladningsbærere:

$$v = \frac{\mu E_{lat}}{1 + \frac{E_{lat}}{E_{sat}}}$$

der:

$$E_{lat} = \frac{V_{ds}}{L}$$

AV $I_{ds} = 0$

LINEÆR $I_{ds} = P_c \frac{\beta}{2} (V_{gs} - V_t) \frac{V_{ds}}{V_{dsat}} (+ \lambda V_{ds})$

METNING $I_{ds} = P_c \frac{\beta}{2} (V_{gs} - V_t) (+ \lambda V_{ds})$

Transistormodeller:

$$I_{ds} = 0$$

AV

$$I_{ds} = P_c \frac{\beta}{2} (V_{gs} - V_t) \frac{V_{ds}}{V_{dsat}}$$

LINEÆR

Metningsspenning

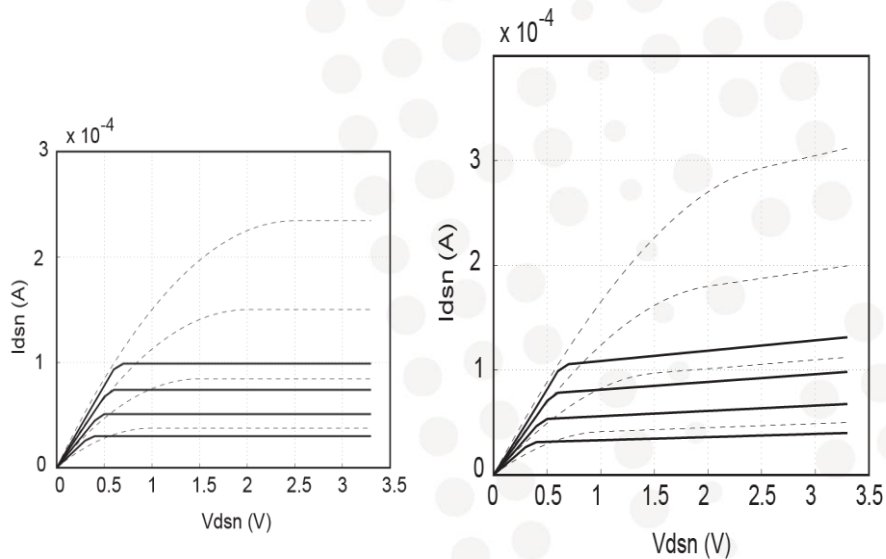
$$V_{dsat} = P_v (V_{gs} - V_t)^{\frac{2}{\lambda}}$$

$$I_{ds} = P_c \frac{\beta}{2} (V_{gs} - V_t)$$

METNING



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