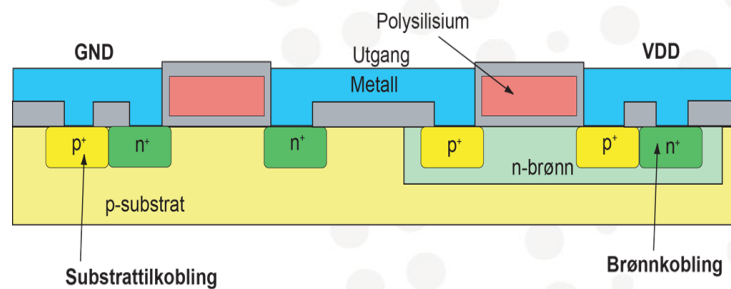
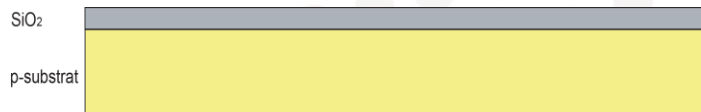
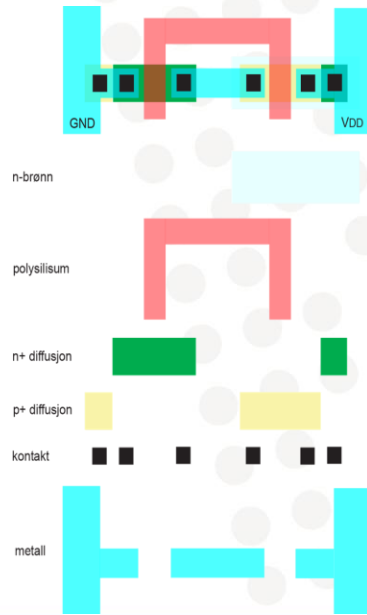
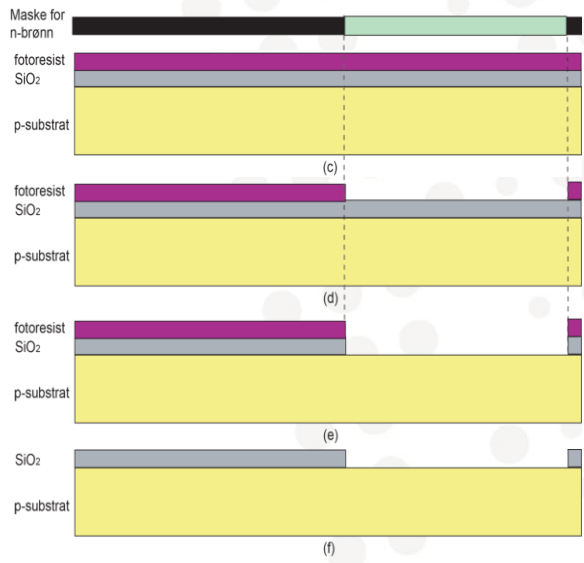




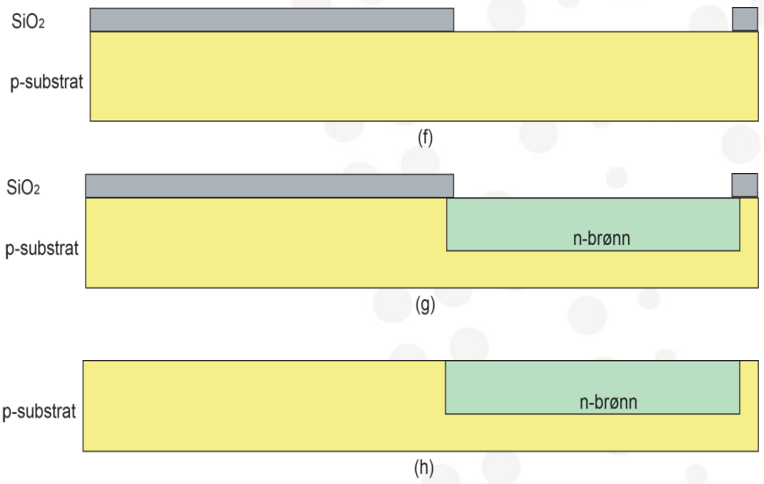
Introduksjon til CMOS fabrikkasjonsprosess



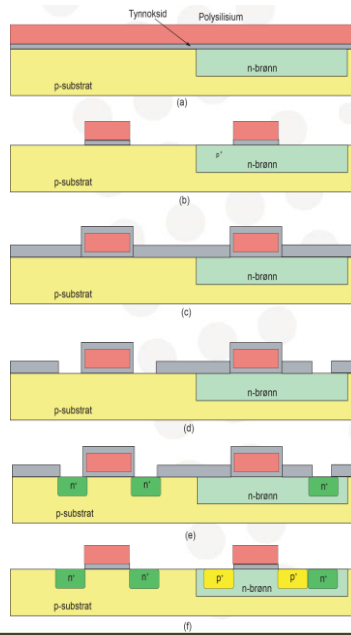




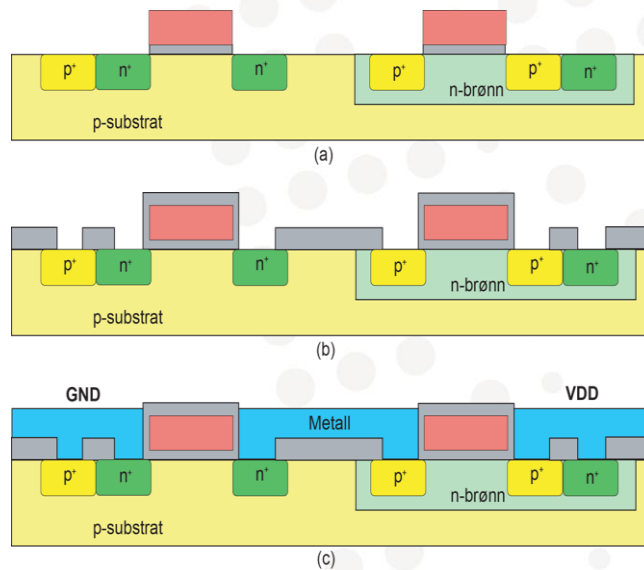
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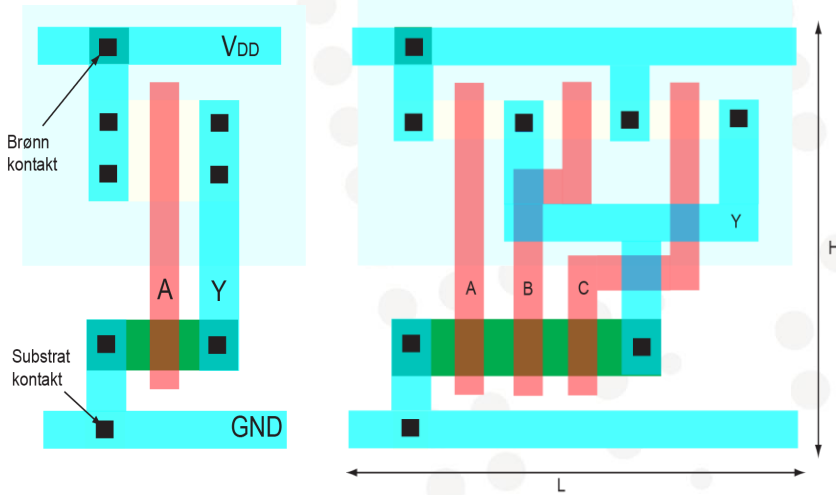


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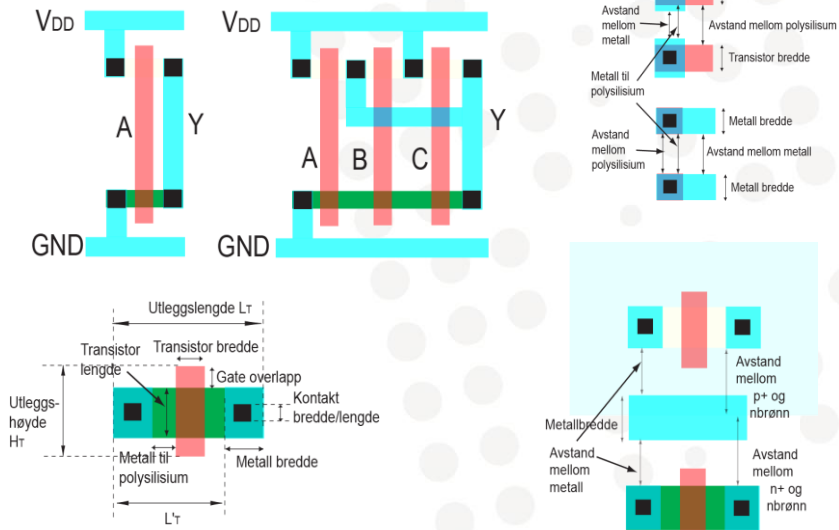


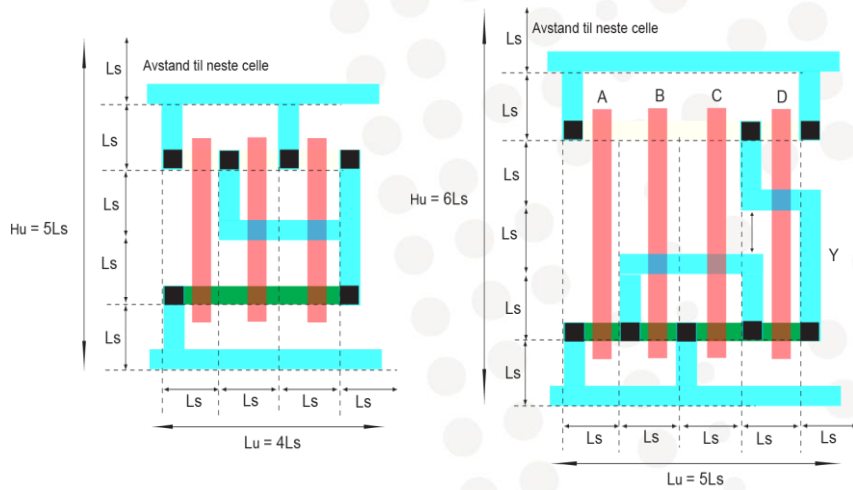
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Utlegg av port



Stick diagrammer

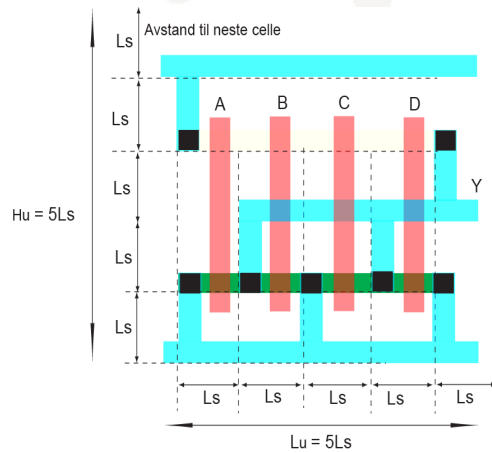




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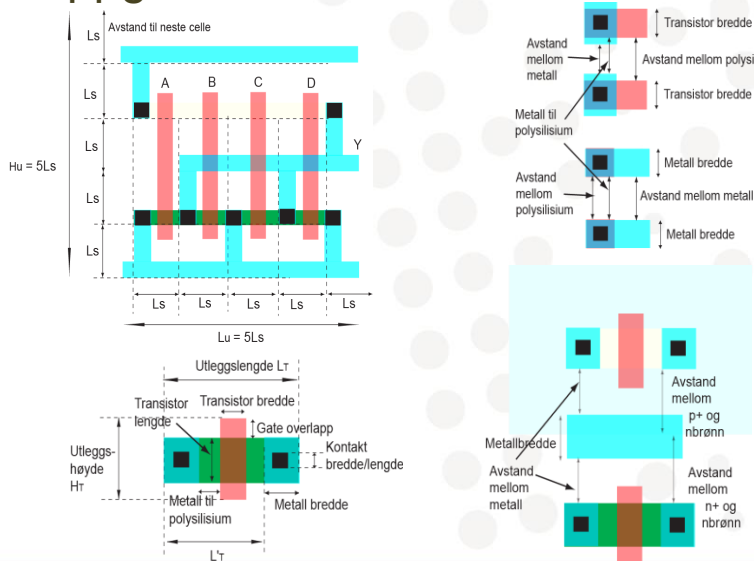
Oppgave 1.8

Lag et stick diagram for en 4inngangs NOR port.

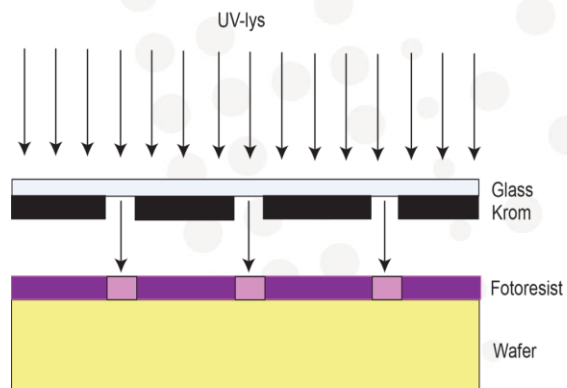


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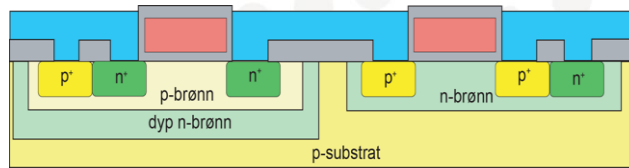
Oppgave 1.9 Estimer arealet til en 4 inngangs NOR port.



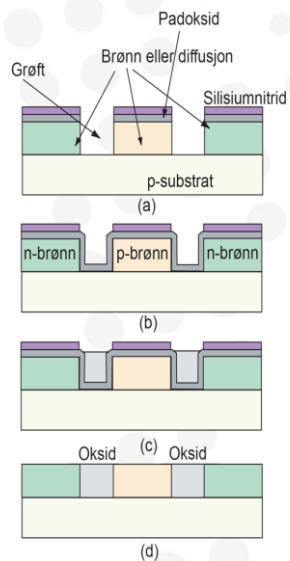
Fotolitografi



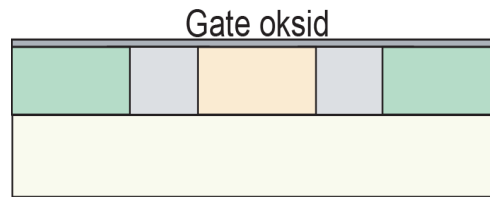
Fremstilling av brønn og kanal



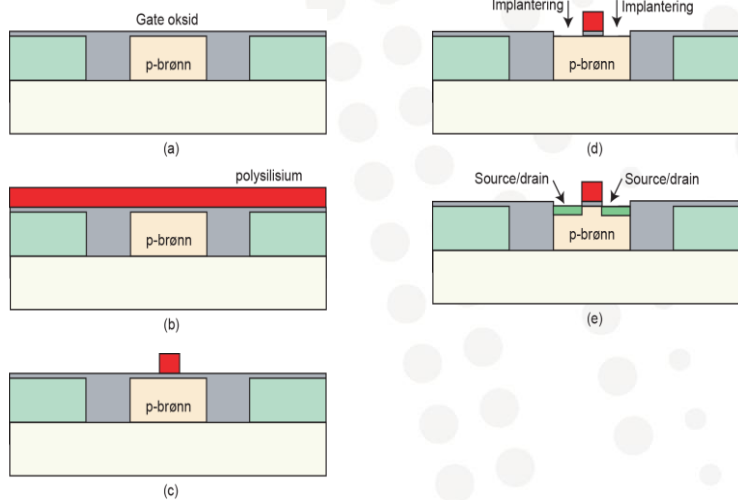
Isolering

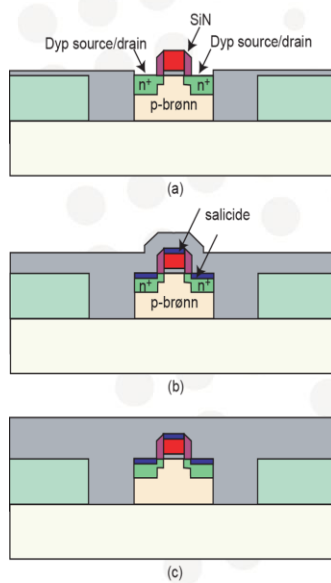


Gate oksid

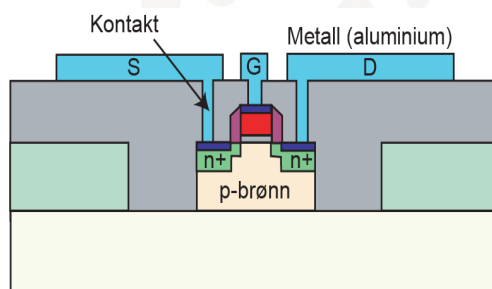


Fremstilling av source og drain





Kontakter



Metall

