INF5140 – Specification and Verification of Parallel Systems

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Sat-based & Bounded model checking

Model checking

$$S \models^? \varphi$$

- origin [Clarke and Emerson, 1982]¹ & [Queille and Sifakis, 1982]
- *S* (model of the) system,
- φ : formula in a suitable logic
 - LTL
 - CTL, CTL*, modal μ -calculus
 - ...
- ultimately a fancy "graph exploration problem" (with big graphs)

¹the conference was 1981, the book was published 1982

Advantages of MC

- no proofs, "push button"
- diagnostic counterexamples
- logics used for MC can express many concurrency problems

Main "disadvantage"

- state space explosion problem (aka state explosion problem)
- problem "solution" space grows *exponential* is the problem "description" space
 - notably reachable state space exponential in the number of processes

The 4 big breakthroughs combatting the SSEP

Apart from

- advances in data structures,
- software engineering,
- tricks, optimizations, heuristics and
- general advances in processing power/memory.

Clarke identifies the following

"big 4" breakthroughs

- 1. symbolic techniques (notably using BDDs)^a
- 2. partial order reduction
- 3. bounded model checking
- 4. CEGAR, localisation reduction [Kurshan, 1993] [Clarke et al., 2010] [Clarke et al., 2000]

^aSee later presentations

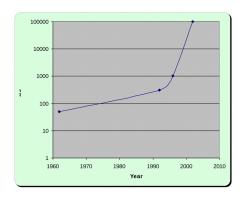
SAT

- (boolean) satisfiability
- famous, prototypical NP-complete problem

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SAT solver progress

- highly competitive field
- yearly "SAT-competition"²



taken from [Clarke, 2017]

²http://www.satcompetition.org/

Bounded model checking

• Origin: [Biere et al., 2009] (see also [Biere et al., 2003])

BMC starting point

Leverage sat-solving, a powerful a successful technique, to do model checking

Cf.: Symbolic model checking and BDDs

- See separate presentation
- successful technique
- used (most prominently for HW) in industrial uses of MC
- Two ingredients of SMC
 - operating symbolically on representation of sets of states
 - use *BDDs* (= specific kind of graph representation of *boolean* functions) to represent and operate on them
- like SMC/BDD-based MC: BMC based on "boolean encodings"

Bad news: the MC problem/reachability is *not* a SAT problem :-(

- MC here:³
 - models are kind of transition systems/Kripke structures . . .
 - spec's are "temporal logic" formulas

solving an MC problem

It all boils down to some form of fancy graph reachability

- "reachability", however:
 - a form of "fixpoint" calculation⁴
 - fixpoints are emphatically not part of boolean logic.⁵

³The term "model checking", i.e., solving $M \models^? \varphi$ can be applied in different settings as well. A boolean assignment can be seen as *model* of a propositional formula, for instance. That *is* of course a SAT problem. But we are interested transition systems satisfying a TL formula.

 $^{^{4}}$ see also the presentation about μ -calculus.

⁵They are not even part of first-order logic. Implicitly they are part in temporal logics, though (eventually, until etc.)

Good news: bounded MC can be seen as SAT :-)

less ambitious goal

Can I find an error (conterexample) in the behavior of the system considering up-to k steps from the initial states

- price to pay: no more "verification" 6
- bug-hunting
- simple core idea

⁶but MC is typically verification of a model/abstraction anyhow and/or verification up until the MC runs out of time/memory.

LTL and "existential" LTL

remember: LTL (linear time temporal logic) and definition of

$$S \models \varphi$$

- $\bullet \varphi$ must hold for all paths of S
- If $S \not\models \varphi$ (error), then exists a paths π such that $\pi \not\models \varphi$

For explicitness' sake

path quantifiers^a

$$\forall \varphi \quad \text{and} \quad \exists \varphi$$

assume NNF

^{*}one single quantifier as prefix to an LTL formula.

Terminology: witnesses

counterexample for

$$S \models \Box p$$
 corresponding to $S \models \forall \Box p$

corresponds the question if there exists a witness⁷

$$\Diamond \neg p$$

- Goal: find finite (fixed bound) prefixes as witness to an existential model checking problem (LTL)
- conceptually easy if original $\forall \varphi$ is a safety prop.
- complications due to loops

⁷in logics in general, a witness is a thing (here a path) that gives (constructive) evidence to an existential formula

Paths with and without loops

No loop



• only prefix with back loop can be witness for $\Box p$

(k, l)-loop $\bullet \longrightarrow \bullet_{S_l} \longrightarrow \bullet \longrightarrow \bullet_{S_i} \longrightarrow \bullet_{S_k}$

Loops

Given: TS/Kripke-structure. transition relation \longrightarrow .

Definition

Assume $l \leq k$. A path π is a (k, l)-loop if $\pi_k \longrightarrow \pi_l$ and

$$\pi = \mathbf{u} \cdot \mathbf{v}^{\omega}$$

with

$$u = \pi_0 \dots \pi_{l-1}$$
 and $v = \pi_l \dots \pi_k$

A path π is a k-loop if there exists an l with $0 \le l \le k$ s.t. π is a (k, l)-loop

- remember: paths π are (infinite) sequences of "states" (worlds)⁸
- loops here is about those states (not "edges" of the picture)

⁸Earlier we also used σ as symbol

Bounded semantics

- remember the "normal" semantics of LTL from before, relating formulas and paths
- $\llbracket \varphi \rrbracket$ or $\pi \models \varphi$
- now: the new "looping paths" (k-loops) as basis for bounded semantics, i.e., basis for BMC
- note: "finite" prefixes (loops) can give information for infinite paths, thus serve as witnesses
- boundes semantics for path

with loop: "unchanged" without loop: be aware of the cut-off and be pessimistic

Bounded semantics: for loops

Definition

Let π be a k-loop. A formula φ is valid along π with bound k, written

$$\pi\models_{\pmb{k}}\varphi\;,$$

iff $\pi \models \varphi$.

Bounded semantics: without loops

Definition

Let π be a path which is *not* a k-loop. Then an LTL formula φ is valid along π with bound k, written

$$\pi \models_{\mathbf{k}} \varphi$$
,

iff $\pi \models^0_{\mathbf{k}} \varphi$, given below.

- earlier $\pi \models \varphi$, corresponding here to \models^0
- k is treated as "cut-off":
- what comes afterward: unknown
- if in doubt: "false", i.e., the path is not valid/does not satisfy the formula in the bounded manner
 - for ○: don't "look" beyond k
 - for □: be pessimistic
 - for ◊: positive answer at least possible within the bound

Bounded semantics: without loops (\models_k^i)

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\begin{array}{llll} \pi \models_{k}^{i} \rho & \text{iff} & p \in L(\pi_{i}) \\ \pi \models_{k}^{i} \neg p & \text{iff} & p \notin L(\pi_{i}) \\ \pi \models_{k}^{i} \varphi_{1} \wedge \varphi_{2} & \text{iff} & \pi \models_{k}^{i} \varphi_{1} & \text{and} & \pi \models_{k}^{i} \varphi_{2} \\ \pi \models_{k}^{i} \varphi_{1} \vee \varphi_{2} & \text{iff} & \pi \models_{k}^{i} \varphi_{1} & \text{or} & \pi \models_{k}^{i} \varphi_{2} \end{array}
 \pi \models_{\iota}^{i} \Box \varphi is always false
 \pi \models_{\mathbf{k}}^{i} \Diamond \varphi iff \exists j.i \leq j \leq k. \pi \models_{\mathbf{k}}^{J} \varphi
 \pi \models_{k}^{\hat{i}} \bigcirc \varphi iff i < k and \pi \models_{k}^{\hat{i}+1} \varphi
 \pi \models_{k}^{i} \varphi_{1}U\varphi_{2} iff \exists j, i \leq j \leq k.\pi \models_{k}^{j} \varphi_{2} and \forall n, i \leq n < j.\pi \models_{k}^{n} \varphi_{1}
 \pi \models_{\iota}^{i} \varphi_{1}R\varphi_{2} iff \exists j, i \leq j \leq k.\pi \models_{\iota}^{j} \varphi_{1} and \forall n, i \leq n < j.\pi \models_{k}^{n} \varphi_{2}
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Bounded → unbounded semantics

- Note, the connection is done for existential LTL (formulas of the form $\exists \varphi$, not like $\forall \varphi$)
- unbounded semantics as limit of the bounded ones (for all/arbitrary bounds k)

Lemma (Easy direction (per path))

$$\pi \models_{\mathit{k}} \varphi \quad \mathit{implies} \quad \pi \models \varphi$$

Lemma (For TSs/KSs)

$$S \models \exists \varphi$$
 implies $S \models_k \exists \varphi$ for some $k \ge 0$

Theorem

$$S \models \exists \varphi \quad \textit{iff} \quad S \models_k \exists \varphi \quad \textit{ for some } k \geq 0$$

BMC via SAT

- so far:
 - definition of the bounded MC problem
 - we convinced ourself: BMC approximates MC (at least for existential path formulas)
- Now: reduce to sat-solving

Goal

 $[\![S,\varphi]\!]_k$ is satisfiable iff π is a witness for φ

- sat-problems: formula with (propositional variables)
- encoding given in 3 parts. given k
 - 1. valid initial path for S and
 - 2. satisfaction of formula if
 - there's a loop or
 - there's no loop

Kripke-structure/transition system

In the modal logics chapter, mild variation in terminology and choice of symbols. [Baier and Katoen, 2008] called (basically) the same things *transition systems* avoiding the word Kripke structure

Definition (Kripke structure)

A Kripke structure is a tuple $(S, I, \longrightarrow, L)$ where S is the set of states, $I \subseteq S$ the set of initial states, $\longrightarrow \subseteq S \times S$ the transition relation, and $L: S \to 2^{AP}$ the labelling function.

- transition relation: a predicate: $^9 \longrightarrow : S^2 \rightarrow Bool$
- initial states: a predicate $I : S \rightarrow Bool$

⁹[Biere et al., 2003] write $T(s_1, s_2)$ for our infix relational notation $s_1 \longrightarrow s_2$, where T is the transition relation predicate.

1^{st} component: Translating S

- remember transition system/Kripke stuctures
 - states s_i. Consider s_i as variables
 - transition relation: as predicate $T(s_k, s_l)$, we write still infix $s_k \longrightarrow s_l$
- unfolding of the transition relation

$$\llbracket S \rrbracket_k \triangleq I(s_0) \land \bigwedge_{i=0}^{k-1} s_i \longrightarrow s_{i+1}$$
 (1)

- states in KS: propositional variables s_k
- beware of the role of index/subscript in equation (1)

Loop condition

• Remember the def. of (k, l)-loop



simple abbreviation

$$_{I}L_{k}\triangleq s_{k}\longrightarrow s_{I}$$

• loop condition holds¹⁰ iff there is a back loop from a state s_k back to a previous state s_l (which can be s_k)

Definition (Loop condition)

$$L_k \triangleq \bigvee_{l=0}^k {}_l L_k$$

 $^{^{10}}$ resp. it will hold when applied to a path consisting of a sequence of states s_i , which are considered as propositional variables, as said. the word "back" makes sense only if one interprets the variables to be "in a sequence".

Successor in a loop

a rather unsurprising definition: define "successor"

succ(i) of i in a (k, l)-loop as

- succ(i) = i + 1 for i < k
- succ(i) = I for k

2^{nd} component: translating formula with a loop

propositional part: boring

$$\begin{array}{ccc}
{}_{I}\llbracket p\rrbracket_{k}^{i} & \triangleq & p(s_{i}) \\
{}_{I}\llbracket \neg p\rrbracket_{k}^{i} & \triangleq & \neg p(s_{i}) \\
{}_{I}\llbracket \varphi_{1} \wedge \varphi_{2}\rrbracket_{k}^{i} & \triangleq & {}_{I}\llbracket \varphi_{1}\rrbracket_{k}^{i} \wedge {}_{I}\llbracket \varphi_{2}\rrbracket_{k}^{i} \\
{}_{I}\llbracket \varphi_{1} \vee \varphi_{2}\rrbracket_{k}^{i} & \triangleq & {}_{I}\llbracket \varphi_{1}\rrbracket_{k}^{i} \vee {}_{I}\llbracket \varphi_{2}\rrbracket_{k}^{i}
\end{array}$$

Cont'd

Actually straightforward

- loop \rightarrow no cut-off \rightarrow "standard semantics"
- remember unrolling of fixpoints¹¹

temporal part: a bit more interesting

$$| [\Box \varphi]_{k}^{i} \triangleq | [\varphi]_{k}^{i} \wedge | [\Box \varphi]_{k}^{succ(i)}$$

$$| [\Diamond \varphi]_{k}^{i} \triangleq | [\varphi]_{k}^{i} \vee | [\Diamond \varphi]_{k}^{succ(i)}$$

$$| [\Box \varphi]_{k}^{i} \triangleq | [\varphi]_{k}^{succ(i)}$$

$$| [\varphi_{1}U\varphi_{2}]_{k}^{i} \triangleq | [\varphi_{1}]_{k}^{i} \vee | [\varphi_{1}U\varphi_{2}]_{k}^{succ(i)}$$

$$| [\varphi_{1}R\varphi_{2}]_{k}^{i} \triangleq | [\varphi_{2}]_{k}^{i} \wedge | [\varphi_{1}R\varphi_{2}]_{k}^{succ(i)}$$

 $^{^{11}}$ Cf. also the presentation about the μ -calculus. Also in the construction of the Büchi-automaton from an LTL formula, that unrolling played a role (for U).

Translation without a loop

- same principles
- "index" / not needed
- instead of the more complex succ(i): simply i + 1.
- otherwise: the definition stays "the same")

3rd component: translating formula without a loop

Inductive case $\forall i \leq k$:

propositional part: boring again

$$\begin{bmatrix} p \end{bmatrix}_{k}^{i} & \triangleq p(s_{i}) \\
 \begin{bmatrix} \neg p \end{bmatrix}_{k}^{i} & \triangleq \neg p(s_{i}) \\
 \begin{bmatrix} \varphi_{1} \land \varphi_{2} \end{bmatrix}_{k}^{i} & \triangleq [\varphi_{1}]_{k}^{i} \land [\varphi_{2}]_{k}^{i} \\
 [\varphi_{1} \lor \varphi_{2}]_{k}^{i} & \triangleq [\varphi_{1}]_{k}^{i} \lor [\varphi_{2}]_{k}^{i}
 \end{bmatrix}$$

Loop-case (cont'd)

Inductive case $\forall i \leq k$:

temporal part: a bit more interesting

$$\begin{bmatrix} \Box \varphi \end{bmatrix}_k^i \triangleq [\varphi]_k^i \wedge [\Box \varphi]_k^{i+1} \\
 [\lozenge \varphi]_k^i \triangleq [\varphi]_k^i \vee [\lozenge \varphi]_k^{i+1} \\
 [\bigcirc \varphi]_k^i \triangleq [\varphi]_k^{i+1} \\
 [\varphi_1 U \varphi_2]_k^i \triangleq [\varphi_1]_k^i \vee [\varphi_1 U \varphi_2]_k^{i+1} \\
 [\varphi_1 R \varphi_2]_k^i \triangleq [\varphi_2]_k^i \wedge [\varphi_1 R \varphi_2]_k^{i+1}$$

• base case: $[\![\varphi]\!]_k^{k+1} \triangleq false$

Putting it together

Theorem

$$[\![S,\varphi]\!]_k$$
 satisfiable iff $S\models_k \exists \varphi$.

Further info

- The technical slides here recap parts of the journal article [Biere et al., 2003] by the inventors of BMC
- BMC for software [Kroening et al., 2004]
- Survey [Prasad et al., 2005]

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References I

[Baier and Katoen, 2008] Baier, C. and Katoen, J.-P. (2008).
Principles of Model Checking.
MIT Press.

[Biere et al., 2009] Biere, A., Cimatti, A., Clarke, E. M., Fujita, M., and Zhu, Y. (2009). Symbolic model checking using SAT procedures instead of BDDs. In Proceedings of DAC'09: Design Automation Conference, pages 317–320. ACM.

[Biere et al., 2003] Biere, A., Cimatti, A., Clarke, E. M., Strichman, O., and Zhu, Y. (2003). Bounded model checking. Advances in Computers, 58.

[Clarke et al., 2000] Clarke, E., Grumberg, O., Jha, S., Lu, Y., and Veith, H. (2000). Counterexample-guided abstraction refinement.

In Emerson, E. A. and Sistla, A. P., editors, Proceedings of the 12th International Conference on Computer-Aided Verification (CAV '00), volume 1855 of Lecture Notes in Computer Science, pages 154–169. Springer Verlag.

[Clarke et al., 2010] Clarke, E. C., Kurshan, R. P., and Veith, H. (2010).

The localization reduction and counter-examble guided abstraction refinement.

In Manna, Z. and Peled, D., editors, *Pnueli Festschrift*, volume 6200 of *Lecture Notes in Computer Science*, pages 61–71. Springer Verlag.

[Clarke, 2008] Clarke, E. M. (2008).

Model checking - my 27-year quest to overcome the state explosion problem.

In Cervesato, I., Veith, H., and Voronkov, A., editors, Logic for Programming, Artificial Intelligence, and Reasoning: 15th International Conference, LPAR 2008, Doha, Qatar, November 22-27, 2008. Proceedings, Lecture Notes in Artificial Intelligence, pages 182–182. Springer Verlag.

References II

[Clarke, 2017] Clarke, E. M. (2017).

SAT-based bounded and unbounded model checking.

Available electronically on the net.

Data of publication unknown.

[Clarke and Emerson, 1982] Clarke, E. M. and Emerson, E. A. (1982).

Design and synthesis of synchronisation skeletons using branching time temporal logic specifications.

In Kozen, D., editor, Proceedings of the Workshop on Logic of Programs 1981, volume 131 of Lecture Notes in Computer Science, pages 244–263. Springer Verlag.

[Kroening et al., 2004] Kroening, D., Lerda, F., and Clarke, E. (2004). Bounded model checking for software.

In Jensen, K. and Podelski, A., editors, *Proceedings of TACAS 2004*, volume 2988 of *Lecture Notes in Computer Science*. Springer Verlag.

[Kurshan, 1993] Kurshan, R. P. (1993).

Automata Theoretic Verification of Coordinating Processes.

Princeton University Press.

[Prasad et al., 2005] Prasad, M. R., Biere, A., and Gupta, A. (2005).

A survey of recent advances in sat-based formal verification.

International Journal on Software Tools for Technology Transfer, 7(2):156-173.

[Queille and Sifakis, 1982] Queille, J. P. and Sifakis, J. (1982).

Specification and verification of concurrent systems in CESAR.

In Dezani-Ciancaglini, M. and Montanari, U., editors, Proceedings of the 5th International Symposium on Programming 1981, volume 137 of Lecture Notes in Computer Science, pages 337–351. Springer Verlag.